

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation (Note 5) (See figure 1)	P_D	200	mW
Thermal Resistance Junction to Ambient Air (Note 5)	$R_{\theta JA}$	625	$^{\circ}\text{C}/\text{W}$
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to +150	$^{\circ}\text{C}$

Electrical Characteristics (@ $T_A = +25^{\circ}\text{C}$, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	$V_{(BR)R}$	400	—	—	V	$I_R = 150\mu\text{A}$
Forward Voltage	V_F	—	—	0.93 1.09 1.29	V	$I_F = 20\text{mA}$ $I_F = 100\text{mA}$ $I_F = 200\text{mA}$
Reverse Current (Note 6)	I_R	—	—	1 100	μA μA	$V_R = 240\text{V}$ $V_R = 240\text{V}, T_J = +150^{\circ}\text{C}$
Total Capacitance	C_T	—	0.9	2.5	pF	$V_R = 0\text{V}, f = 1.0\text{MHz}$
Reverse Recovery Time	t_{rr}	—	—	50	ns	$I_F = I_R = 30\text{mA}$, $I_{rr} = 3.0\text{mA}, R_L = 100\Omega$

Notes: 5. Part mounted on FR-4 board with recommended pad layout, which can be found on our website at <http://www.diodes.com>.
 6. Short duration pulse test used to minimize self-heating effect.

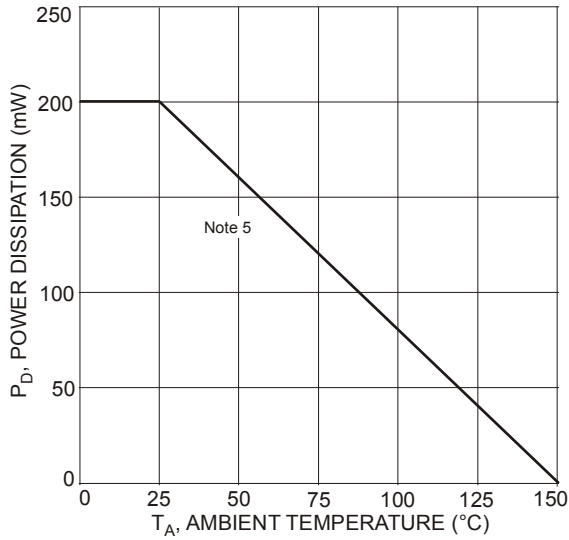


Fig. 1 Power Derating Curve

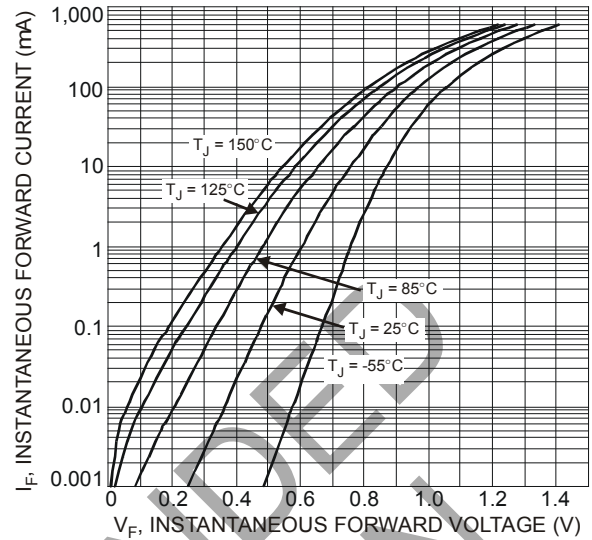


Fig. 2 Typical Forward Characteristics

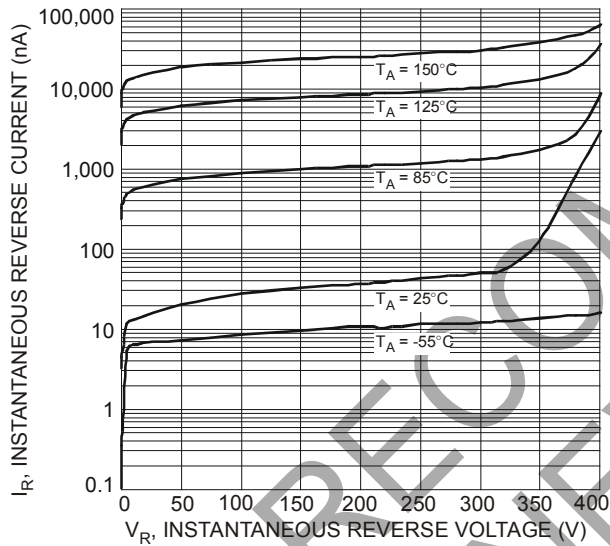


Fig. 3 Typical Reverse Characteristics

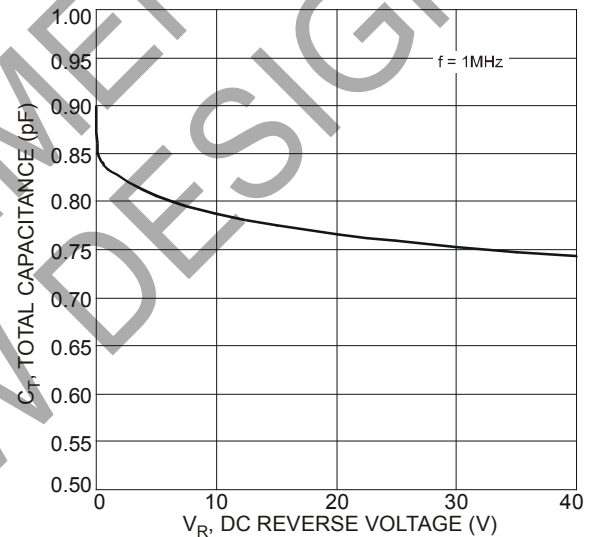
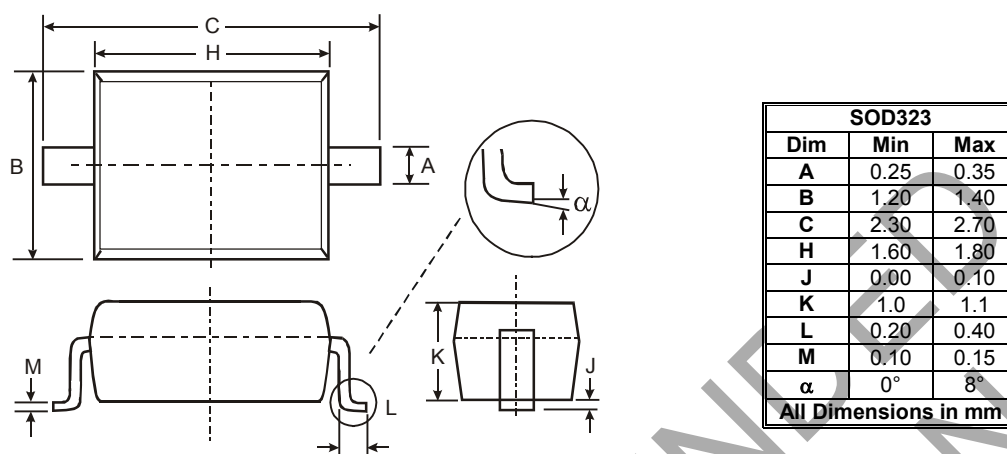
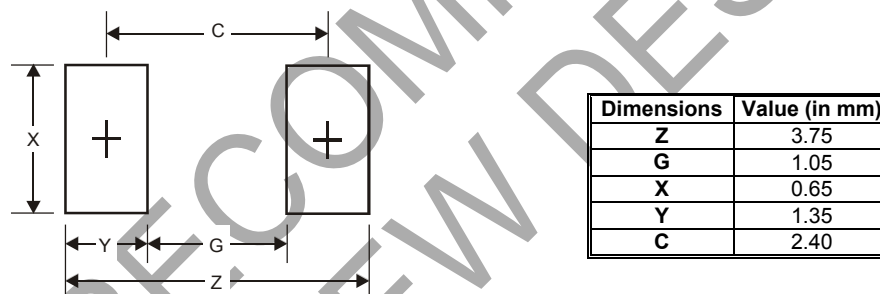


Fig. 4 Typical Total Capacitance vs. Reverse Voltage

Package Outline Dimensions



Suggested Pad Layout



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