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## 1 Revision History

The revision history describes the changes that were implemented in the document. The changes are listed by revision, starting with the most current publication.

#### 1.1 Revision C

Revision C was published in March 2018. The following is a summary of the changes in revision C of this document.

- The new Microsemi template and format was applied.
- The package outline drawing was updated. For more information, see Package Outline Drawing.

### 1.2 Revision B

Revision B was published in May 2011. The following is a summary of the changes in revision B of this document.

- The patent information was removed from the document.
- For TO-247 packages: the maximum lead thickness was changed from 0.70 in (0.031 mm) to 1.016 in (0.040 mm).

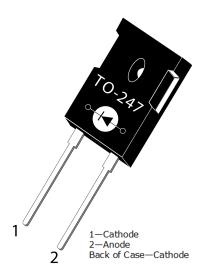
#### 1.3 Revision A

Revision A was published in January 2006. It is the first publication of this document.



## 2 Product Overview

This section outlines the product overview for the APT60DQ120BG device.



#### 2.1 Features

The following are key features of the APT60DQ120BG device:

- Ultrafast recovery times
- Soft recovery characteristics
- Low forward voltage
- Low leakage current
- Avalanche energy rated
- RoHS compliant
- AEC-Q101 qualified

#### 2.2 Benefits

The following are benefits of the APT60DQ120BG device:

- Higher switching frequency
- Low switching losses
- Low noise (EMI) switching
- Higher reliability systems
- Increased system power density

### 2.3 Applications

The APT60DQ120BG device is designed for the following applications:

- Power factor correction (PFC)
- Anti-parallel diode
  - Switch-mode power supply
  - Inverters/converters
  - Motor controllers
- Freewheeling diode
  - Switch-mode power supply
  - Inverters/converters
- Snubber/clamp diode



## **3** Electrical Specifications

This section shows the electrical specifications for the APT60DQ120BG device.

### 3.1 Absolute Maximum Ratings

The following table shows the absolute maximum ratings for the APT60DQ120BG device.

All ratings: Tc = 25 °C unless otherwise specified.

**Table 1 • Absolute Maximum Ratings** 

Symbol	Parameter	Ratings	Unit
VR	Maximum DC reverse voltage	1200	V
VRRM	Maximum peak repetitive reverse voltage	1200	_
VRWM	Maximum working peak reverse voltage	1200	_
I <sub>F(AV)</sub>	Maximum average forward current (Tc = 103 °C, duty cycle = 0.5)	60	Α
IF(RMS)	RMS forward current	87	_
lfsм	Non-repetitive forward surge current (T <sub>J</sub> = 45 °C, 8.3 ms)	540	
Eavl	Avalanche energy (1 A, 40 mH)	20	mJ
Tı , Tstg	Operating and storage temperature range	-55 to 175	°C
TL	Lead temperature for 10 seconds	300	

The following table shows the thermal and mechanical characteristics of the APT60DQ120BG device.

**Table 2 • Thermal and Mechanical Characteristics** 

Symbol	Characteristic	Min	Тур	Max	Unit
Rejc	Junction-to-case thermal resistance			0.40	°C/W
WT	Package weight		0.22		OZ
			5.9		g
Torque	Maximum mounting torque			10	lb-in
				1.1	N-m

### **3.2** Electrical Perfromance

The following table shows the static characteristics of the APT60DQ120BG device.

**Table 3 • Static Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
VF	F Forward voltage	I <sub>F</sub> = 60 A		2.8	3.3	V
		I <sub>F</sub> = 120 A		3.35		_
		I <sub>F</sub> = 60 A, T <sub>J</sub> = 125 °C		2.11		_
Irm	Maximum reverse leakage current	V <sub>R</sub> = 1200 V			100	μΑ
		V <sub>R</sub> = 1200 V, T <sub>J</sub> = 125 °C			500	=
Cı	Junction capacitance	V <sub>R</sub> = 200 V		37		pF



## 3.3 Dynamic Characteristics

The following table shows the dynamic characteristics of the APT60DQ120BG device.

**Table 4 • Dynamic Characteristics** 

Symbol	Characteristic	Test Conditions	Min	Тур	Max	Unit
trr	Reverse recovery time	$I_F = 1 \text{ A}$ $di_F/dt = -100 \text{ A/}\mu\text{s}$ $V_R = 30 \text{ V}$ $T_J = 25 \text{ °C}$		30		ns
trr	Reverse recovery time	I <sub>F</sub> = 60 A		320		=
Qrr	Reverse recovery change	$-di_F/dt = -200 A/\mu s$ $V_R = 800 V$ $-T_C = 25 °C$		630		nC
IRRM	Maximum reverse recovery current	1c = 25 C		5		Α
trr	Reverse recovery time	I <sub>F</sub> = 60 A		420		ns
Qrr	Reverse recovery charge	di <sub>F</sub> /dt = -200 A/μs V <sub>R</sub> = 800 V		2810		nC
IRRM	Maximum reverse recovery current	Tc = 125 °C		12		Α
trr	Reverse recovery time	I <sub>F</sub> = 60 A		190		ns
Qrr	Reverse recovery change	$-di_F/dt = -1000 A/\mu s$ $V_R = 800 V$ $-T_C = 125 °C$		4415		nC
IRRM	Maximum reverse recovery current	10 - 125 C		38		Α



## 3.4 Typical Performance Curves

This section shows the typical performance curves for the APT60DQ120BG device.

Figure 1 • Maximum Transient Thermal Impedance

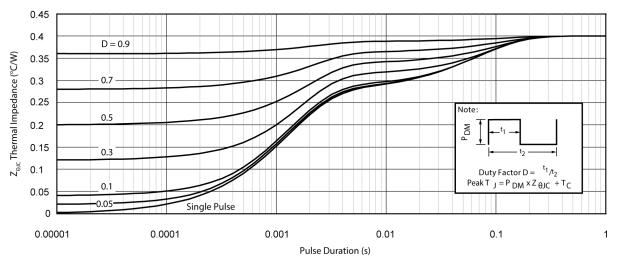


Figure 2 • Forward Current vs. Forward Voltage

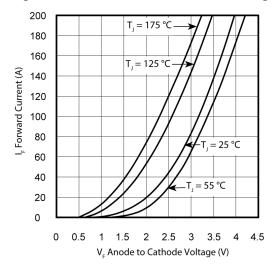


Figure 3 • trr vs. Current Rate of Change

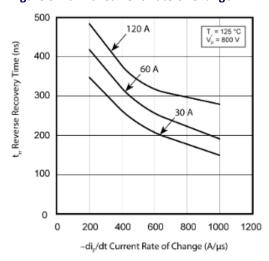




Figure 4 • Qrr vs. Current Rate of Change

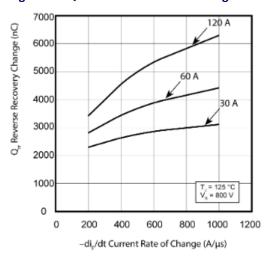


Figure 6 • Dynamic Parameters vs. Junction Temperature

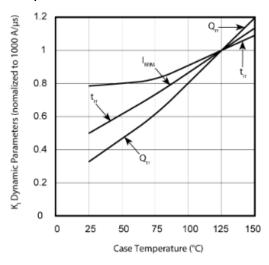


Figure 8 • Junction Capacitance vs. Reverse Voltage

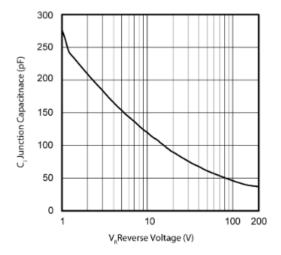


Figure 5 • IRRM vs. Current Rate of Change

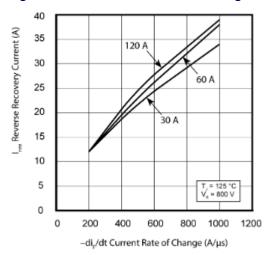
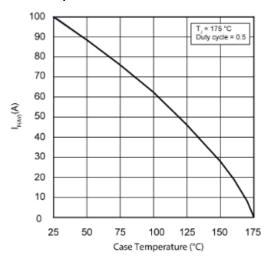


Figure 7 • Maximum Average Forward Current vs. Case Temperature

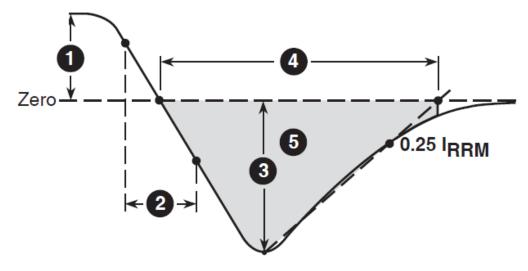




## 4 Reverse Recovery Overview

The following illustration shows the reverse recovery testing and measurement information for the APT60DQ120BG device.

Figure 9 • Diode Reverse Recovery Waveform and Definitions



- 1. IF—Forward conduction current.
- 2. di<sub>F</sub>/dt—Rate of diode current change through zero crossing.
- 3. IRRM—Maximum reverse recovery current.
- 4.  $t_{rr}$ —Reverse recovery time, measured from zero crossing where diode current goes from positive to negative, to the point at which the straight line through IRRM and  $0.25 \times I_{RRM}$  passes through zero.
- 5. Qrr—Area under the curve defined by IRRM and trr.



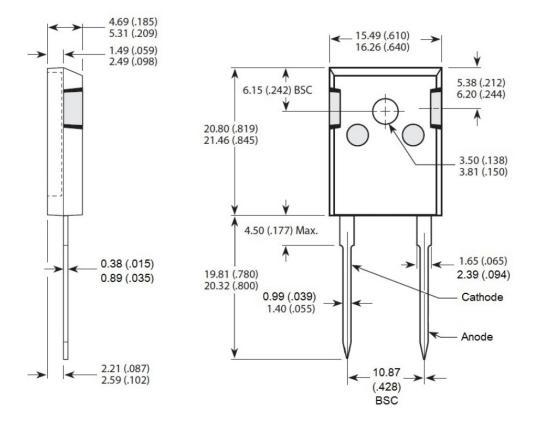
# **5** Package Specification

This section shows the package specification for the APT60DQ120BG device.

## 5.1 Package Outline Drawing

This section shows the TO-247 package drawing of the APT60DQ120BG device. Dimensions are in millimeters and (inches).

Figure 10 • Package Outline Drawing







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