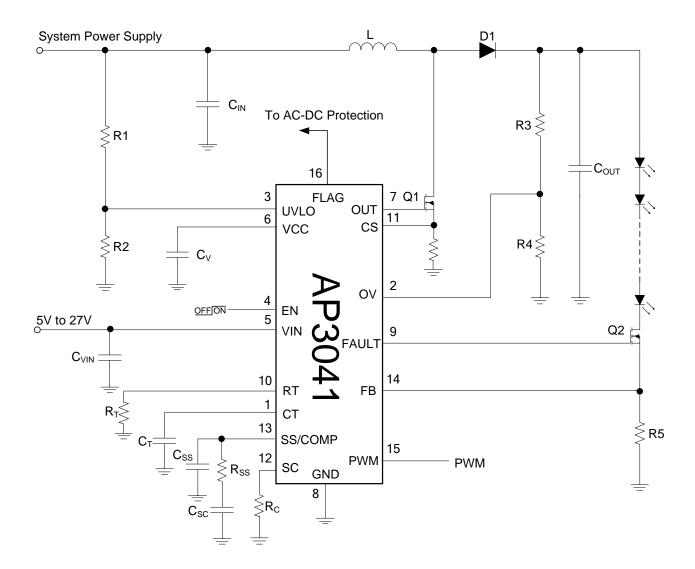


Typical Applications Circuit



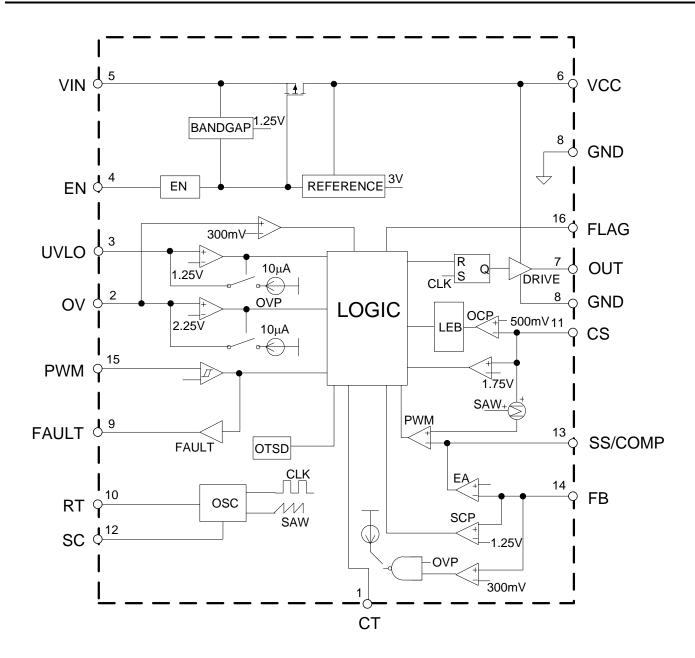


Pin Descriptions

Pin Number	Pin Name	Function
1	СТ	Capacitor Setting Shut Down Delay Time
2	OV	Over Output Voltage Protection Pin
3	UVLO	Two resistors connected from this pin to GND and the power supply respectively is used to set start-up and shutdown level.
4	EN	Enable Pin
5	VIN	Input supply pin. This pin must be locally bypassed.
6	VCC	This pin should be bypassed to GND with a ceramic capacitor.
7	OUT	Connect this pin to the gate of external MOSFET, the gate driver has 1A peak current capability.
8	GND	Ground
9	FAULT	This pin can be used to drive the external MOSFET. The logic of the driver signal is controlled by that of PWM pin and the signal phase of them is the same. This pin is pulled to ground under fault condition of OVP, UVLO, OTP, diode short circuit and LED short circuit.
10	RT	An external resistor connected from this pin to GND is used to set the operating frequency.
11	CS	Sense switch current pin, which is used for current mode control and for current limit. The current limit voltage is 500mV.
12	SC	Slope compensation for current sense. A resistor between SC and GND will program the slope compensation.
13	SS/COMP	An external soft-start time capacitor is placed from this pin to GND and is charged by an internal 20µA current source to control regulator soft-start time. Compensation pin. This pin is the output of the internal error amplifier.
14	FB	Voltage feedback pin. The reference voltage is 500mV.
15	PWM	This pin can be connected to current matched chip and receives error signal used to shut down the system.
16	FLAG	Fault condition output pin. When the output is in short circuit condition, the FLAG outputs logic low to shut down the power path. Please refer to the application circuit.



Functional Block Diagram





Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Value	Unit
V _{IN}	Input Voltage	30	V
Vcc	VCC Pin Voltage	20	V
V _{оит}	OUT Pin Voltage	20	V
VFAULT	FAULT Pin Voltage	20	V
V _{FB}	FB Pin Voltage	20	V
V _{UVLO}	UVLO Pin Voltage	7	V
V _{CS}	CS Pin Voltage	20	V
V _{SC}	SC Pin Voltage	7	V
V _{FLAG}	FLAG Pin Voltage	7	V
V _{SS/COMP}	SS/COMP Pin Voltage	7	V
V _{RT}	RT Pin Voltage	7	V
V _{PWM}	PWM Pin Voltage	7	V
V _{EN}	EN Pin Voltage	30	V
Vov	OV Pin Voltage	7	V
θ_{JA}	Thermal Resistance (Junction to Ambient, No Heat Sink, Free Air)	82	°C/W
TJ	Operating Junction Temperature	+150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
T _{LEAD}	Lead Temperature (Soldering, 10s)	+260	°C
_	ESD (Human Body Model)	2000	V
_	ESD (Machine Model)	200	V

Note: 4. Stresses greater than those listed under *Absolute Maximum Ratings* can cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under *Recommended Operating Conditions* is not implied. Exposure to *Absolute Maximum Ratings* for extended periods can affect device reliability.

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{IN}	Input Voltage	5	27	V
f	Operating Frequency	0.1	1	MHz
TJ	Operating Temperature Range	-40	+85	°C



Electrical Characteristics (V_{CC}=12V, V_{EN}=3.3V, T_A=+25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{IN}	Input Voltage	_	5	_	27	V
V _{FB}	Feedback Voltage	_	490	500	510	mV
Icc	Supply Current	V _{FB} =V _{IN} , No Switching	_	1.5	5	mA
ΙQ	Shutdown Supply Current	V _{EN} =0	_	1	2	μΑ
.,	VCC Bin Voltage	11V≤V _{IN} ≤27V	9.5	10.0	10.5	V
V _{CC}	VCC Pin Voltage	5V≤V _{IN} ≤10V, I _{CC} =5mA	V _{IN} -0.5	_	_	V
I _{CC-LIM}	VCC Pin Current Limit	_	_	35	_	mA
V _{CC-HI}	VCC Pin UVLO Rising Threshold	_	_	4.7	_	V
V _{CC-HYS}	VCC Pin UVLO Falling Hysteresis	_	_	300	_	mV
fosc	Oscillator Frequency	_	0.1	_	1	MHz
V _{SC_PK}	SC Peak Voltage	_	530	_	680	mV
V _{UVLO}	UVLO Threshold	_	1.20	1.25	1.30	V
I _{UVLO-HYS}	UVLO Hysteresis Current Source	_	_	10	_	μΑ
V _{CS}	Current Limit Threshold Voltage	_	0.48	0.5	0.52	V
V _{RT}	RT Voltage	_	1.20	1.25	1.30	V
Gv	Error Amplifier Voltage Gain	_	_	680	_	V /V
G _S	Error Amplifier Transconductance	_	_	500	_	μ A /V
V _{EH}	EN Die Threehold Voltore	_	2.5	_	_	V
V _{EL}	- EN Pin Threshold Voltage	_	_	_	0.5	V
V _{IH}	DIAMA Dia Thuashald Makara	_	2.5	_	_	V
V _{IL}	- PWM Pin Threshold Voltage	_	_	_	0.5	V
V _{OV}	OV Threshold	_	_	2.25	_	V
I _{OV-HYS}	OV Hysteresis Current Source	_	_	10	_	μΑ
V _{FB_LOW}	LED- Short to GND Detection Level	_	_	0.3	_	V
V _{FB_HIGH}	LED+ Short to LED-Detection Level	_	_	1.25	_	V
V _{OV_LOW}	LED+ Short to GND Detection Level	_	_	0.3	_	V
V _{CS_HIGH}	Diode Short Detection Level	_	_	1.75	_	V
I _{CT}	CT Current Source	_	_	5	_	μΑ
V _{СТ}	CT Threshold Voltage	_	_	2.6	_	V

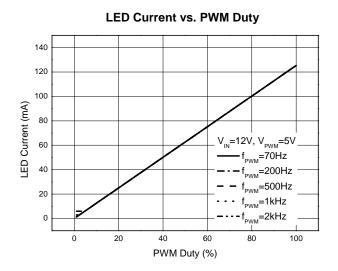


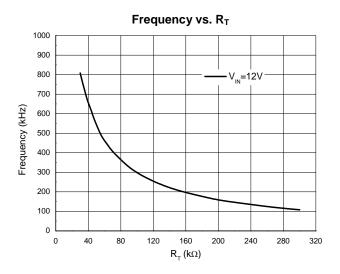
Electrical Characteristics (continued, V_{CC}=12V, V_{EN}=3.3V, T_A=+25°C, unless otherwise specified.)

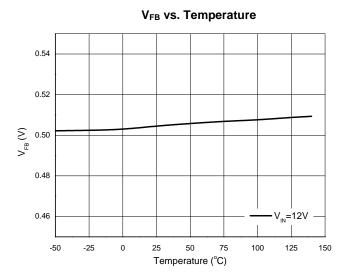
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
D _{MAX}	Maximum Duty Cycle	f=200kHz	80	90	_	%
I _{SS}	Soft-Start Current Source	_	16	22	28	μΑ
t _{RISE}	OUT Pin Rising Time	C _{OUT} =1nF	_	20	_	no
t _{FALL}	OUT Pin Falling Time	C _{OUT} =1nF	_	20	_	ns
V _{OUT-H}	OUT High Voltage Level (V _{CC} -V _{OUT})	I _{OUT} =50mA	_	0.25	0.75	V
V _{OUT-L}	OUT Low Voltage Level (V _{OUT})	I _{OUT} =50mA	_	0.25	0.75	V
t _{RISE}	FAULT Pin Rising Time	C _{FAULT} =1nF	_	100	_	20
t _{FALL}	FAULT Pin Falling Time	C _{FAULT} =1nF	_	100	_	ns
V _{FAULT_} H	FAULT High Voltage Level (Vcc-VFAULT)	I _{FAULT} =10mA	_	0.25	0.75	V
V _{FAULT_L}	FAULT Low Voltage Level	I _{FAULT} =10mA	_	0.25	0.75	V
t _D	FAULT Minimum Pulse Width	Oscillator Frequency=200kHz	_	20	_	μs
V_{FLAG-H}	FLAG High Voltage Level	I _{FLAG} =500μA	_	2	_	V
V _{FLAG-L}	FLAG Low Voltage Level	I _{FLAG} =500μA	_	0.25	0.75	V
T _{OTSD}	Thermal Shutdown Temperature	_	_	+160	_	°C
T _{HYS}	Thermal Shutdown Hysteresis	_	_	+20	_	°C

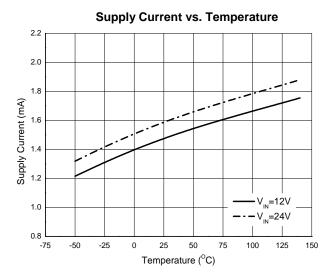


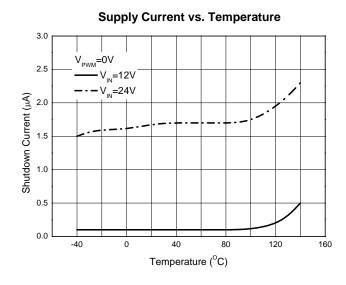
Performance Characteristics

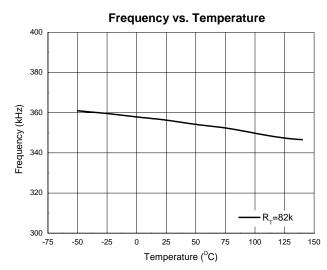






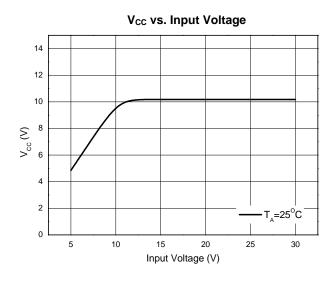


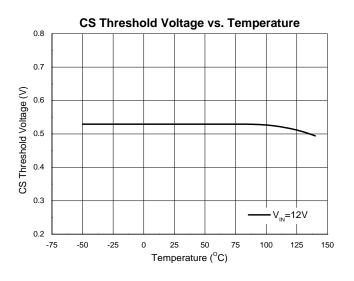


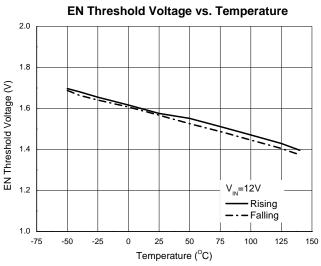


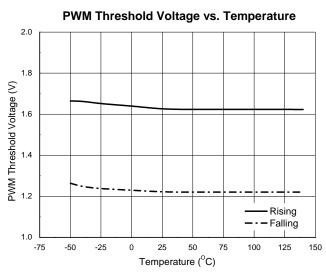


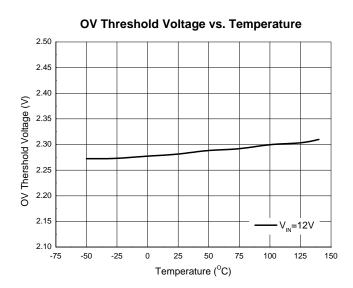
Performance Characteristics (continued)

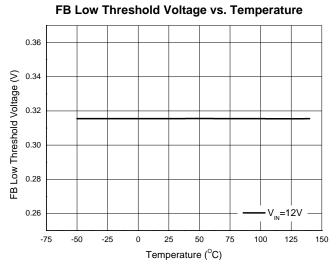






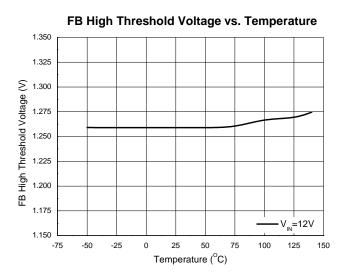


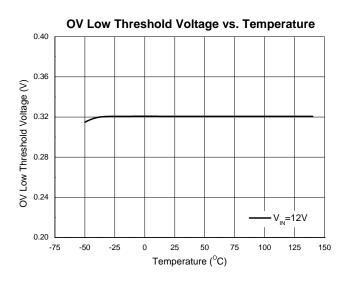




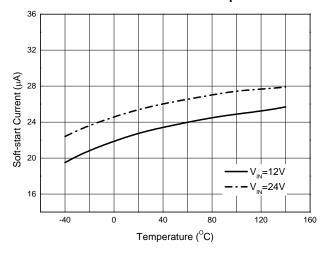


Performance Characteristics (continued)

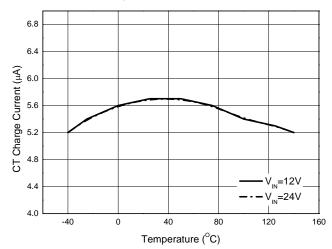




Soft-start Current vs. Temperature

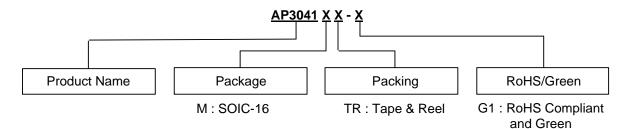


CT Charge Current vs. Temperature



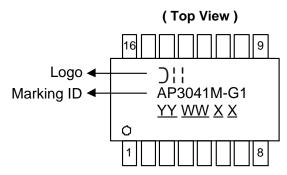


Ordering Information



Package	Temperature Range	Part Number	Marking ID	Packing
SOIC-16	-40 to +85°C	AP3041MTR-G1	AP3041M-G1	4,000/13"Tape & Reel

Marking Information



YY: Year: 19, 20,21~ WW: Week: 01~52; 52 represents 52 and 53 week

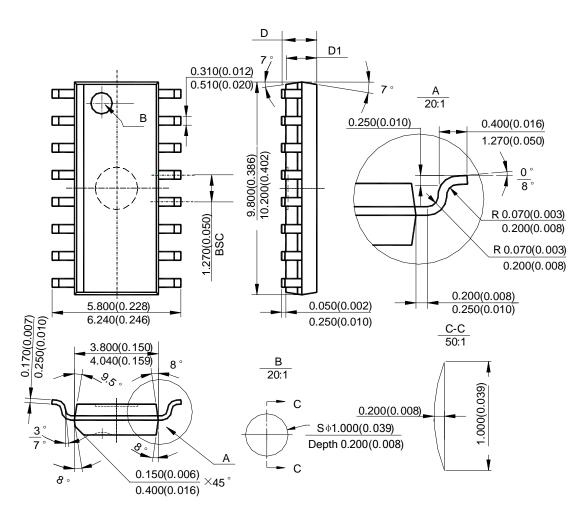
XX: Internal Code



Package Outline Dimensions (All dimensions in mm(inch).)

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOIC-16



Note: Eject hole, oriented hole and mold mark is optional.

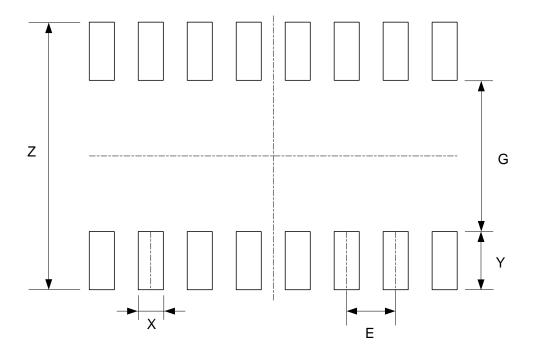
Symbol	D			D1				
Cymbol	min(mm)	max(mm)	min(inch)	max(inch)	min(mm)	max(mm)	min(inch)	max(inch)
Option1	1.350	1.750	0.053	0.069	1.250	1.650	0.049	0.065
Option2	-	1.260	-	0.050	1.020	-	0.040	-



Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

(1) Package Type: SOIC-16



Dimensions	Z	G	X	Y	E
	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)	(mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050



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