Absolute Maximum Ratings, T_A = 25 °C (Unless Otherwise Noted)

Rating	Symbol	Value	Unit
Repetitive peak off-state voltage	V_{DRM}	±190	V
Non-repetitive peak on-state pulse current (see Notes 1,2 and 3) 8/20 (IEC 61000-4-5, combination wave generator, 1.2/50 voltage waveshape)	I_{PPSM}	1000	A
Initial rate of rise of on-state current, Linear current ramp, Maximum ramp value <50 A	di_T/dt	800	A/µs
Junction temperature	T_J	-40 to +150	°C
Storage temperature range	T_{stg}	-65 to +150	°C

NOTES:

1. Initially, the device must be in thermal equilibrium with T_J = 25 °C.
2. These non-repetitive rated currents are peak values of either polarity. The surge may be repeated after the device returns to its initial conditions.
3. When used as intended; see Application section on page 5.

Electrical Characteristics, T_A = 25 °C (Unless Otherwise Noted)

Parameter		Test Conditions		Min.	Typ.	Max.	Unit
I_{DRM}	Repetitive peak off-state current	$V_D = \pm V_{DRM}$	T_A = 25 °C T_A = 85 °C			± 5 ±10	µA
$V_{(BO)}$	AC breakover voltage	dv/dt = ±250 V/ms, R_{SOURCE} = 300 ohms				±250	V
$V_{(BO)}$	Ramp breakover voltage	dv/dt ≤ ±1000 V/µs, Linear voltage ramp, Maximum ramp value = ±500 V di/dt = ±20 A/µs, Linear current ramp, Maximum ramp value = ±10 A				±263	V
$I_{(BO)}$	Breakover current	dv/dt = ±250 V/ms, R_{SOURCE} = 300 ohms				±600	mA
I_H	Holding current	I_T = ±5 A, di/dt = ±30 mA/ms		±20			mA
I_D	Off-state current	V_D = ±50 V	T_A = 85 °C			±10	µA
C_{off}	Off-state capacitance	f = 1 MHz, V_d = 1 Vrms, V_D = 0			105	125	pF

Thermal Characteristics

Parameter		Test Conditions	Min.	Typ.	Max.	Unit
$R_{\theta JA}$	Junction to free air thermal resistance	EIA/JESD51-2 PCB, $I_T = I_{TSM(1000)}$, T_A = 25 °C, (see Note 4)			90	°C/W

NOTE 4: EIA/JESD51-2 environment and PCB has standard footprint dimensions connected with 5 A rated printed wiring track widths.

Parameter Measurement Information

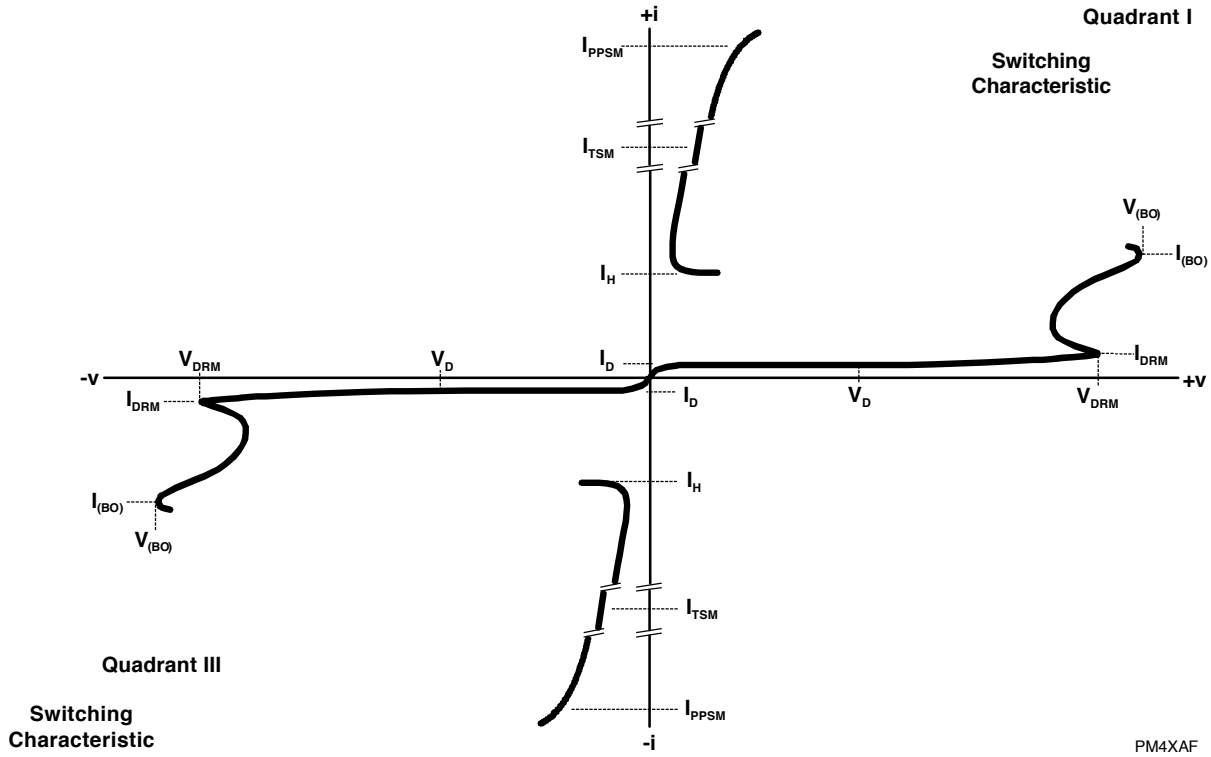
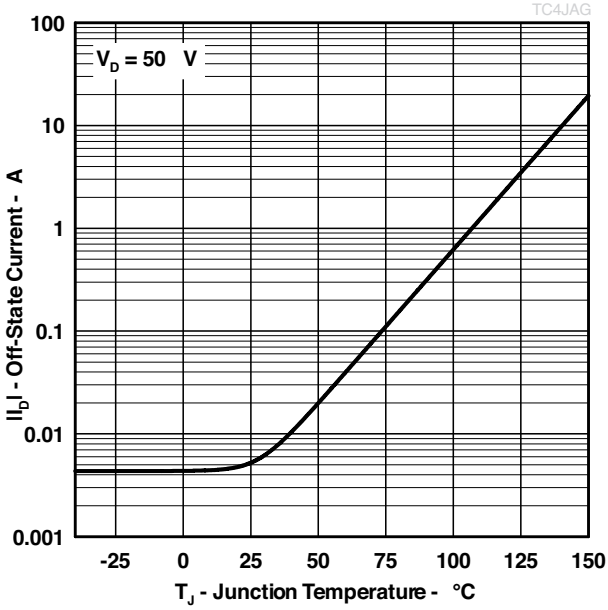


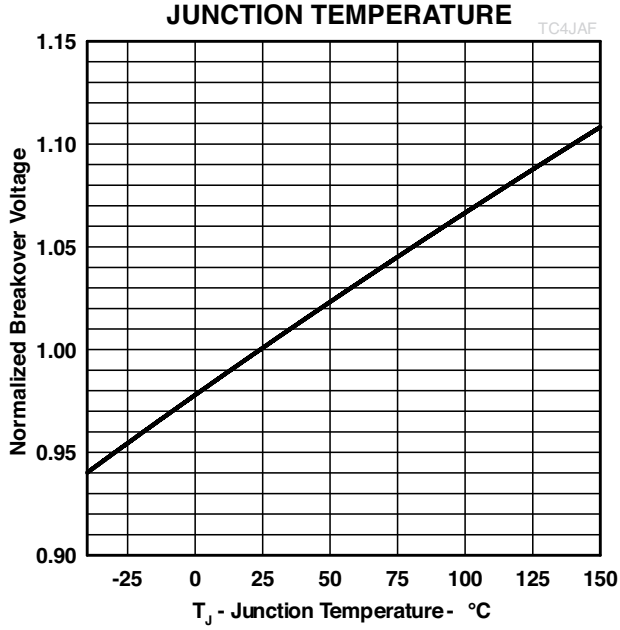
Figure 1. Voltage-Current Characteristic for Terminals 1-2
All Measurements are Referenced to Terminal 2

Typical Characteristics

OFF-STATE CURRENT vs JUNCTION TEMPERATURE

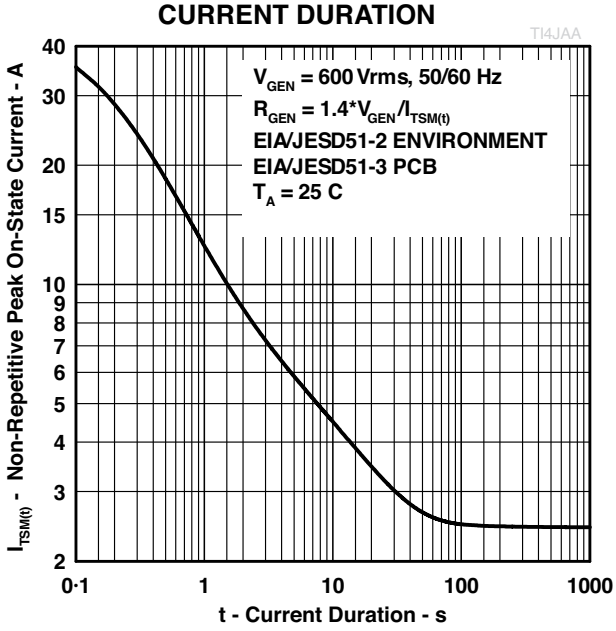


NORMALIZED BREAKOVER VOLTAGE vs JUNCTION TEMPERATURE

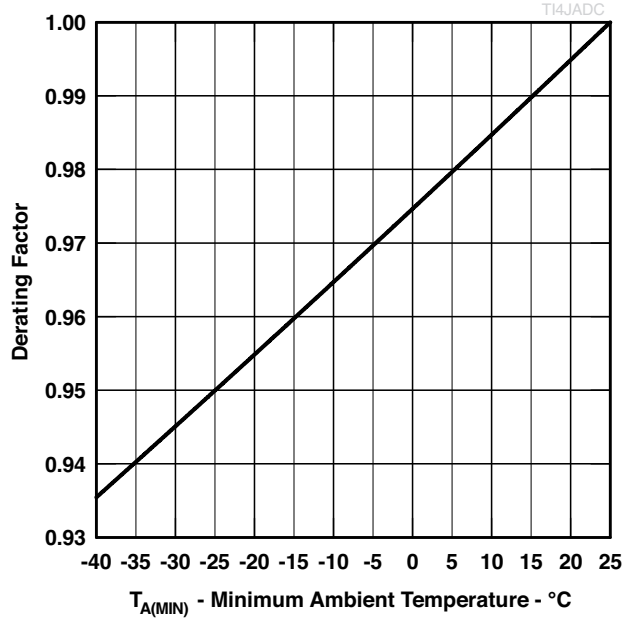


Rating and Thermal Information

NON-REPETITIVE PEAK ON-STATE CURRENT vs CURRENT DURATION



V_{DRM} DERATING FACTOR vs MINIMUM AMBIENT TEMPERATURE



APPLICATION INFORMATION

Enhancing the Performance of an MOV

In many applications, an offline Switch Mode Power Supply (SMPS) is subjected to possible damage from indirect lightning strikes, switching transients, line voltage swells and other overvoltage conditions. Metal Oxide Varistors (MOVs) are often used to provide protection against lightning and other short duration transients. However, an MOV can easily be overstressed by a power line voltage swell due to the low frequency characteristic of this overvoltage condition. To address this problem, the Model ACTP250J1BJ bidirectional transient protector can be placed in series with the MOV so that it does not conduct during AC line voltage swells up to a specific voltage level while allowing the series combination to clamp at voltage levels just above the MOV clamp voltage during a lightning transient.

For example, assume an offline SMPS that is designed to operate at a maximum line voltage of 260 Vrms and a 275 Vrms MOV is being used to provide protection against a lightning surge with a peak voltage of 2.5 kV per IEC 61000-4-5 (1.2/50 μs voltage, 8/20 μs current combination wave). To prevent the MOV from being damaged by a line voltage swell as high as 400 Vrms (566 Vpeak), a Model ACTP250J1B1 protector can be used in series with the MOV (as shown in Figure 1 below). At 25 °C, this combination has a minimum breakdown voltage of 577 V (387 + 190 = 577), where 387 V is V_{BD} of the 275 V_{RMS} MOV, and 190 V is the V_{DRM} of the Model ACTP250J1BJ, assuring that it will not operate at a line voltage that is < 400 Vrms.

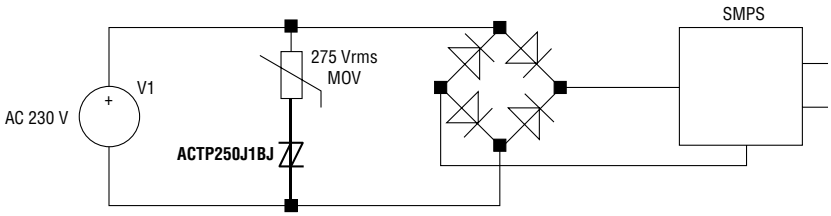


Figure 1. MOV/ACTP250J1BJ AC Line Protection

Adding the Model ACTP250J1BJ device in series with the MOV generates only a small increase in the clamp level. Figure 2 below shows the clamp voltage level of the MOV alone and the MOV/ACTP series combination for a 2.5 kV 1.2/50, 8/20 µs combination wave surge. Note that the Model ACTP250J1BJ device only adds a few volts to the MOV clamp voltage.

The waveforms in Figure 3 show that the series combination does not clamp the voltage waveform or conduct current when subjected to a 400 Vrms line voltage.

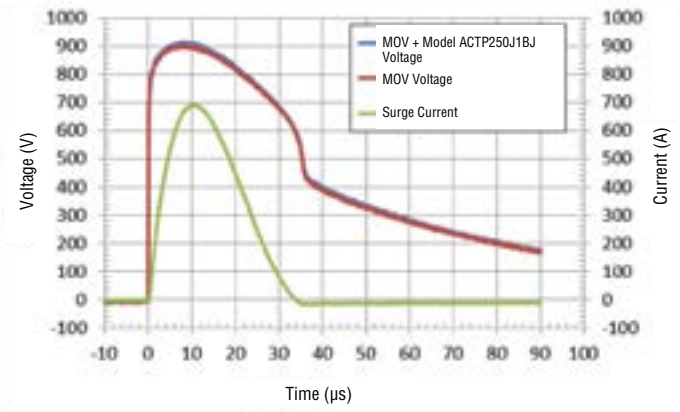


Figure 2. Protection Circuit Clamp Voltages for a 2.5 kV Surge

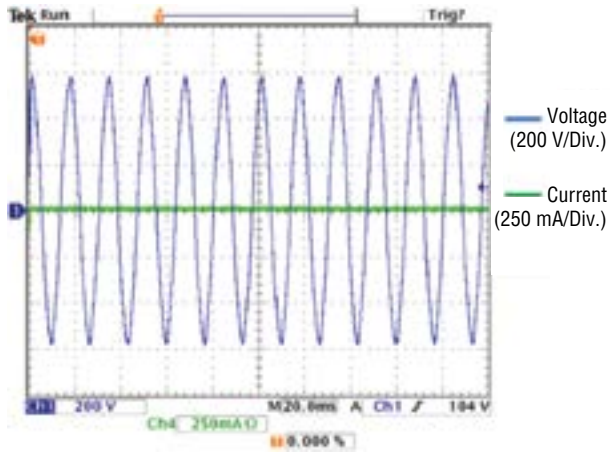


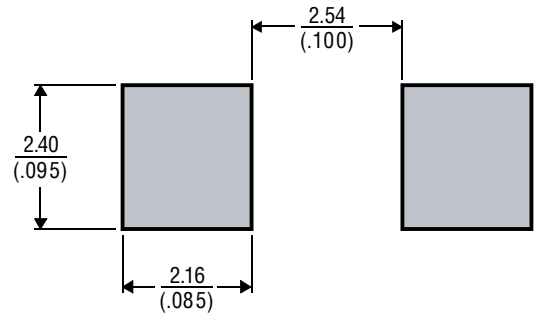
Figure 3. Performance of the Series Protection Circuit when Subjected to a 400 Vrms Line Voltage

In conclusion, adding the Model ACTP250J1B1 in series with the MOV reduces the MOV's susceptibility to damage from a line voltage swell while having a minimal impact on the lightning protection performance of the design.

MECHANICAL DATA

Recommended Printed Wiring Land Pattern Dimensions

SMB Land Pattern



DIMENSIONS ARE: MILLIMETERS / (INCHES)

MDXXBID

Device Symbolization Code

Devices will be coded as below. As the device parameters are symmetrical, terminal 1 is not identified.

Device	Symbolization Code
ACTP250J1BJ	250J1

Carrier Information

For production quantities, the carrier will be embossed tape reel pack. Evaluation quantities may be shipped in bulk pack or embossed tape.

Package	Carrier	Standard Quantity
SMB	Embossed Tape Reel Pack	3000

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