ELECTRICAL CHARACTERISTICS

PARAMETER	TEST CONDITIONS	TEMPERATURE ⁽²⁾	TYPICAL
		CASE TEMP	
Output Voltage ⁽¹⁾	I _{OUT} = 300 mA	+25°C to +200°C	V _{OUT} ± 1.0%
	$V_{IN} = V_{OUT} + 3VDC$		
Line Regulation	$V_{IN} = V_{OUT} + 3VDC$	+25°C to +200°C	$V_{OUT} \pm 0.3\%$
	to V _{IN} = 38 V		
	I _{OUT} = 50 mA		
Load Regulation	$V_{IN} = V_{OUT} + 5VDC$	+25°C to +200°C	V _{OUT} ± 0.5%
	I _{OUT} = 50 to 300mA		
Ripple Rejection at 120 Hz	$V_{IN} = V_{OUT} + 5VDC$	+25°C	-60dB
Standby Current		+25°C	30mA
	$v_{\rm IN} = v_{\rm OUT} + 3 v_{\rm DC}$	+23 0	JUIIA
	1001 – 0		
Short Circuit Current	V _{IN} = V _{OUT} +5VDC	+25°C	400mA
Short Circuit Current	V _{IN} = V _{OUT} +5VDC	+200°C	200mA
Foldback Current (knee)	$V_{IN} = V_{OUT} + 5VDC$	+25°C	2A
Foldback Current (knee)	$V_{IN} = V_{OUT} + 5VDC$	+200°C	1.5A
Noise Output	$V_{IN} = V_{OUT} + 5VDC$	+25°C	2mVRMS
	I _{OUT} = 300 mA		
Differential Voltage	I _{OUT} = 300 mA	+25°C to +200°C	3 VDC min
* ($\Delta V = V_{IN} - V_{OUT}$)			

Notes:

1) $V_{IN} = 10V$ Min

2) Regulator operation guaranteed by design @ -25°C for test condition listed. To designate factory screening add a "-1" to the Micropac part number; i.e. 42095-015-1.

Micropac Industries cannot assume any responsibility for any circuits shown or represent that they are free from patent infringement. Micropac reserves the right to make changes at any time in order to improve design and to supply the best product possible.

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Mechanical Configuration



Electrical Connection		
Case	V _{IN}	
Pin 1	Ground	
Pin 2	V _{OUT}	

C_{IN} & C_O:

- Recommended as good analog design practice to ensure regulator stability. These are in addition to supply and load capacitance.
- Required if regulator is located more than 2" or 3" away from input supply or output load capacitor(s). Capacitors must be installed as close to the regulator Terminals as possible to ensure stability.
- X5R & X7R type ceramic capacitors are recommended because of minimal variation in value and ESR over temperature.

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