

# PCIe Gen3 Storage Switch Family

PM8546, PM8545, PM8544, PM8543, PM8542, and PM8541

## Diagnostics and Debug (Continued)

- External loopback at PHY and TLP layers
- Errors, statistics, performance, and TLP latency counters

## Peripheral I/O Interfaces

- Up to 11 Two-Wire Interfaces (TWIs) with SMBus support
- Up to 4 SFF-8485-compliant SGPIO ports
- Up to 106 parallel GPIO pins
- 10/100 Ethernet MAC port (MII/RMII) (PSX 96x/80x/64xG3)
- 16-bit local bus interface with ECC protection
- Up to 4 UARTs
- JTAG and EJTAG interface

## High-speed I/O

- PCIe Gen3 8 GT/s
- Supports PCIe-compliant link training and manual PHY configuration

## Chiplink Diagnostic Tools

- Extensive debug, diagnostics, configuration, and analysis tools with an intuitive GUI
- Access to configuration data, management capabilities, and signal integrity analysis tools (such as real-time eye capture)
- Connects to device over in-band PCIe or sideband signals (UART and EJTAG)

## Ordering Information

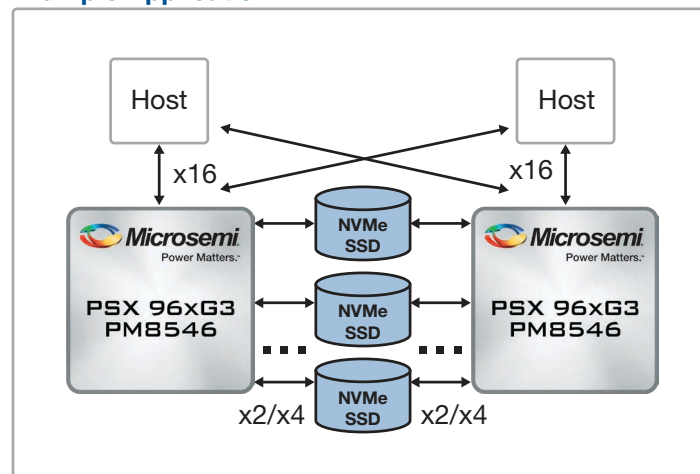
Product	Lanes	Ports/NTBs	Partitions	Hot-plug Controllers	Package	Ordering No.
PSX 96xG3 PCIe Storage Switch	96	48	24	48	37.5 mm x 37.5 mm	PM8546B-FEI
PSX 80xG3 PCIe Storage Switch	80	40	20	40	37.5 mm x 37.5 mm	PM8545B-FEI
PSX 64xG3 PCIe Storage Switch	64	32	16	32	37.5 mm x 37.5 mm	PM8544B-FEI
PSX 48xG3 PCIe Storage Switch	48	24	12	24	27.0 mm x 27.0 mm	PM8543B-F3EI
PSX 32xG3 PCIe Storage Switch	32	16	8	16	27.0 mm x 27.0 mm	PM8542B-F3EI
PSX 24xG3 PCIe Storage Switch	24	12	6	12	27.0 mm x 27.0 mm	PM8541B-F3EI

## Evaluation Kits

The evaluation kits are used to evaluate and test device functionality, measure signal integrity, and customize and develop PSX firmware. The following kits are available:

- PM5461-KIT—PSX/PFX 96/80/64xG3, 1-Slot, 16 HD Evaluation Kit (PMC-2151996)
- PM5462-KIT—PSX/PFX 48/32/24xG3, 3-Slot Evaluation Kit (PMC-2151645)

## Example Application



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