

**Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Units
Drain-Source Voltage			V <sub>DSS</sub>	-40	V
Gate-Source Voltage			V <sub>GSS</sub>	±20	V
Continuous Drain Current (Note 6) V <sub>GS</sub> = -10V	Steady State	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-10.3 -8.3	A
	t < 10s	T <sub>A</sub> = +25°C T <sub>A</sub> = +70°C	I <sub>D</sub>	-13.7 -11	A
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I <sub>DM</sub>	80	A
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	2.6	A
Avalanche Current, L = 0.1mH			I <sub>AS</sub>	34	A
Avalanche Energy, L = 0.1mH			E <sub>AS</sub>	58	mJ

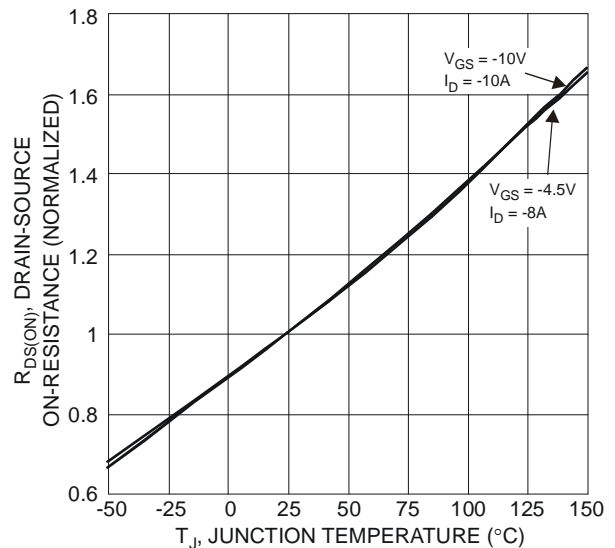
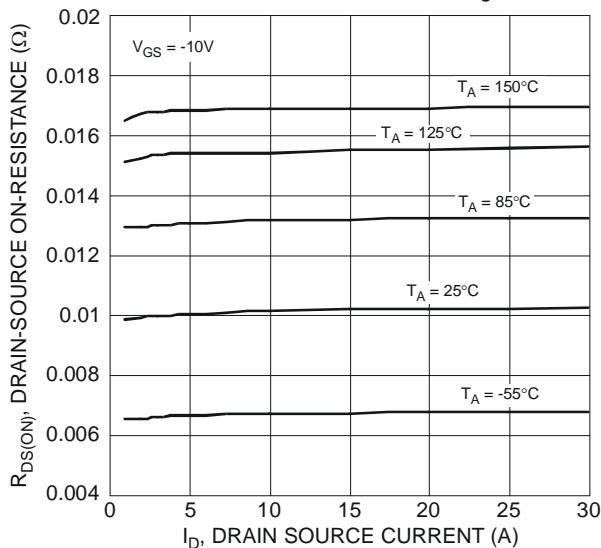
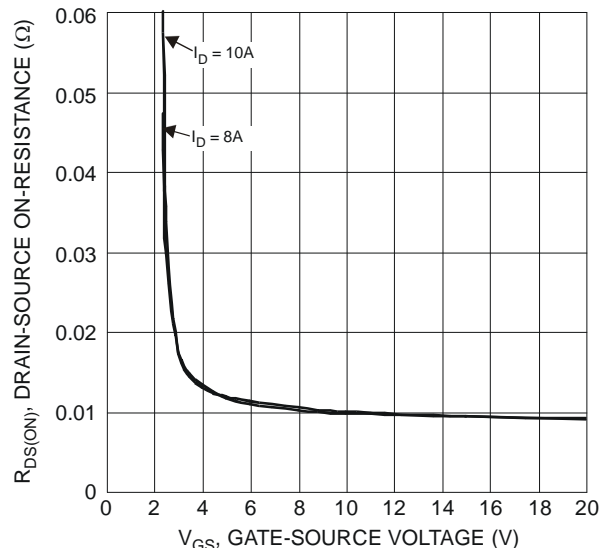
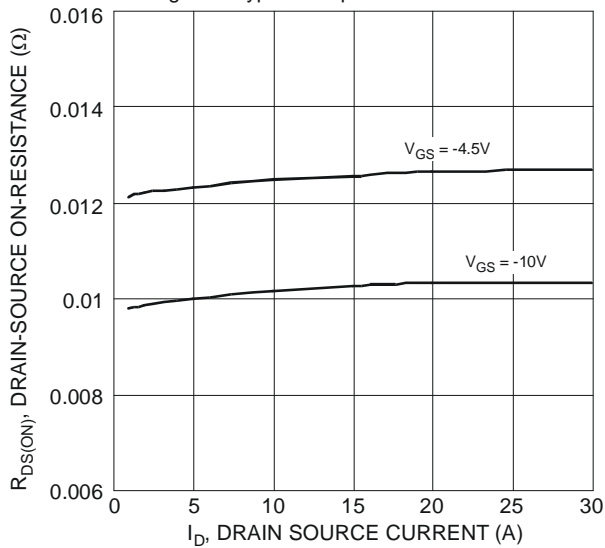
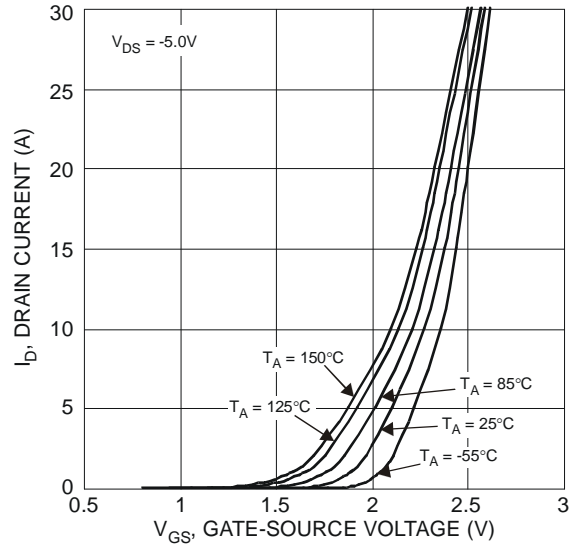
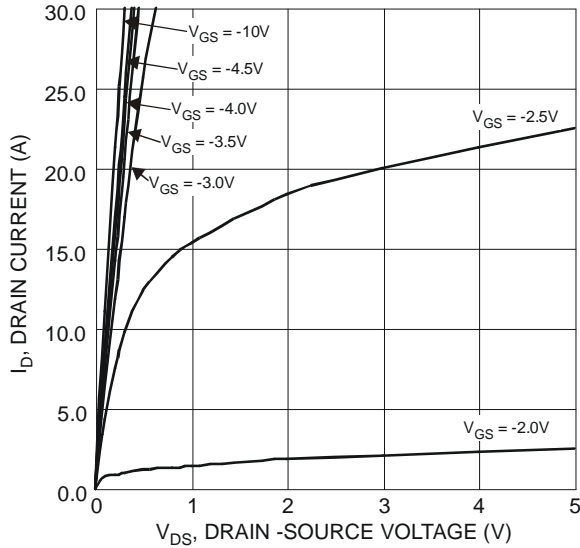
**Thermal Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

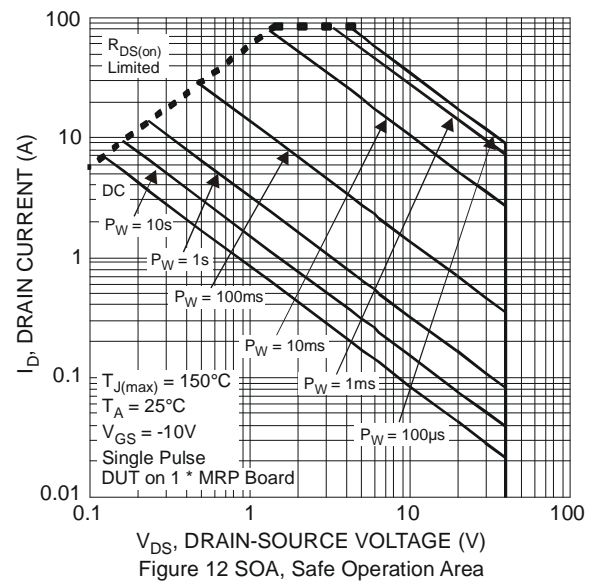
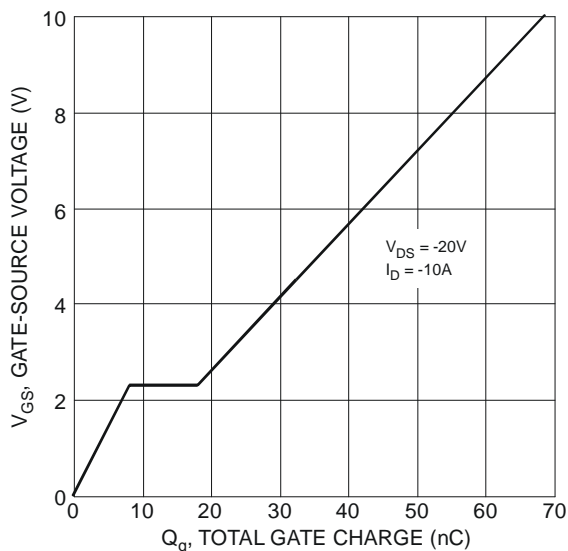
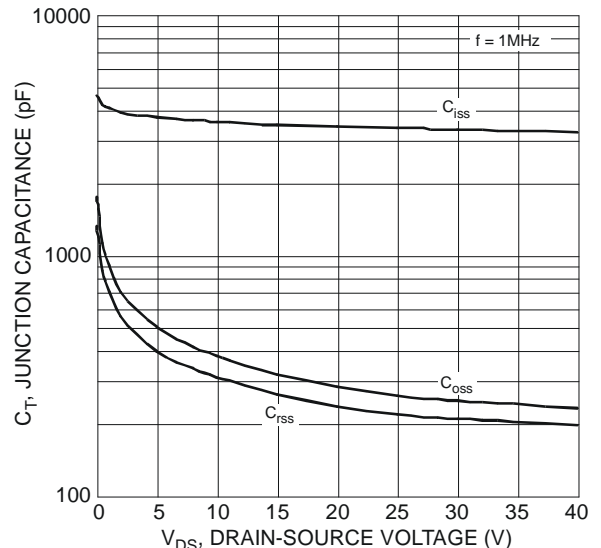
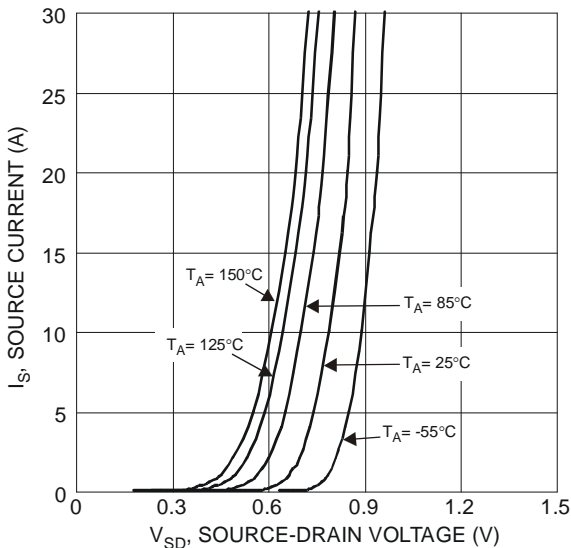
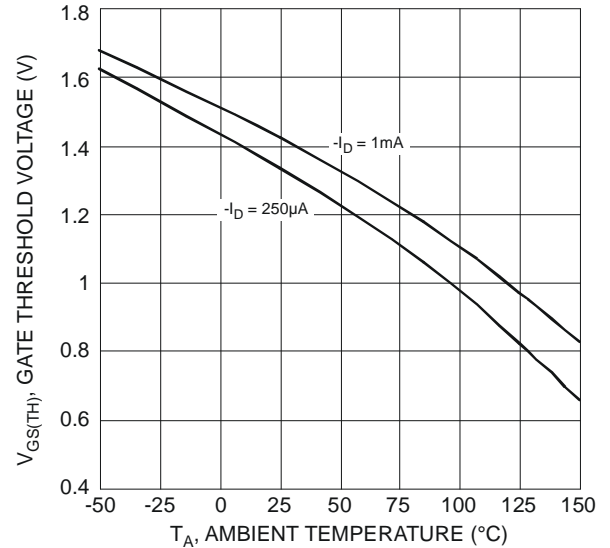
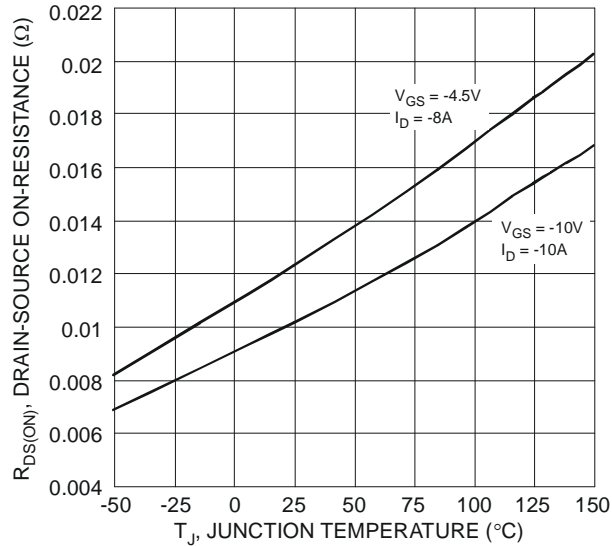
Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P <sub>D</sub>	1	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	R <sub>θJA</sub>	123	°C/W
	t < 10s		69	
Total Power Dissipation (Note 6)		P <sub>D</sub>	2.1	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>θJA</sub>	60	°C/W
	t < 10s		34	
Thermal Resistance, Junction to Case (Note 6)		R <sub>θJC</sub>	3.3	
Operating and Storage Temperature Range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C

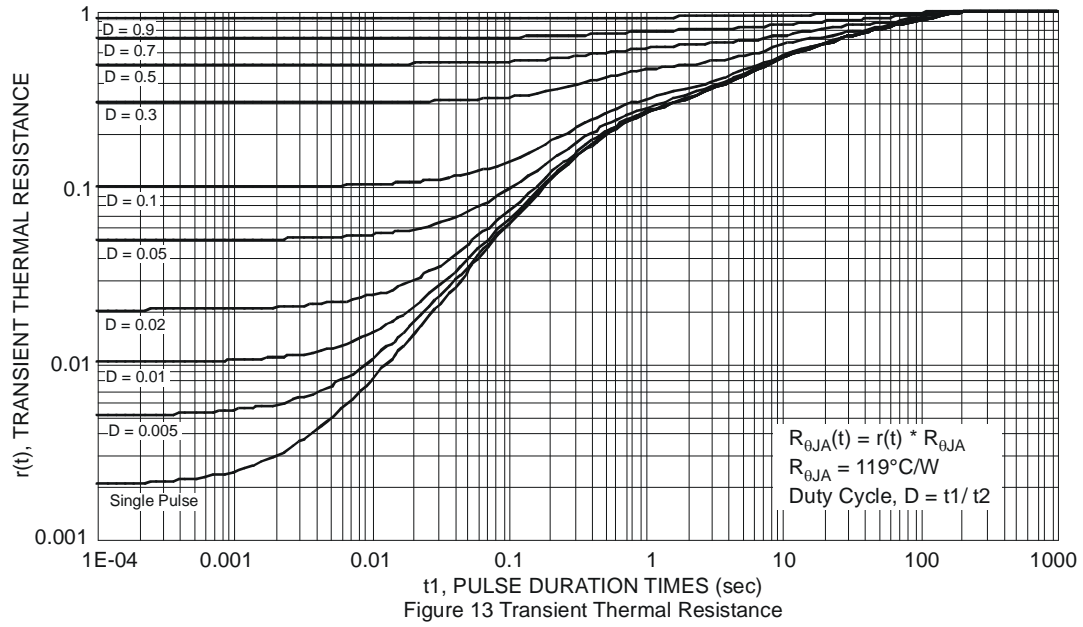
**Electrical Characteristics** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
<b>OFF CHARACTERISTICS (Note 7)</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-40	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250µA
Zero Gate Voltage Drain Current T <sub>J</sub> = +25°C	I <sub>DSS</sub>	—	—	-1	µA	V <sub>DS</sub> = -40V, V <sub>GS</sub> = 0V
Gate-Source Leakage	I <sub>GSS</sub>	—	—	±100	nA	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V
<b>ON CHARACTERISTICS (Note 7)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	-1	—	-3	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250µA
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	—	9.4	13	mΩ	V <sub>GS</sub> = -10V, I <sub>D</sub> = -10A
		—	12.3	18		V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -8A
Diode Forward Voltage	V <sub>SD</sub>	—	-0.7	-1.2	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A
<b>DYNAMIC CHARACTERISTICS (Note 8)</b>						
Input Capacitance	C <sub>iss</sub>	—	3,426	—	pF	V <sub>DS</sub> = -20V, V <sub>GS</sub> = 0V, f = 1MHz
Output Capacitance	C <sub>oss</sub>	—	283	—	pF	
Reverse Transfer Capacitance	C <sub>rss</sub>	—	235	—	pF	
Gate Resistance	R <sub>g</sub>	—	4.7	—	Ω	V <sub>DS</sub> = 0V, V <sub>GS</sub> = 0V, f = 1MHz
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Q <sub>g</sub>	—	32.5	—	nC	V <sub>DS</sub> = -20V, I <sub>D</sub> = -10A
Total Gate Charge (V <sub>GS</sub> = -10V)	Q <sub>g</sub>	—	68.6	—	nC	
Gate-Source Charge	Q <sub>gs</sub>	—	8.2	—	nC	
Gate-Drain Charge	Q <sub>gd</sub>	—	9.9	—	nC	
Turn-On Delay Time	t <sub>D(on)</sub>	—	5.3	—	ns	V <sub>DD</sub> = -20V, V <sub>GEN</sub> = -10V, R <sub>G</sub> = 3Ω, I <sub>D</sub> = -10A
Turn-On Rise Time	t <sub>r</sub>	—	20	—	ns	
Turn-Off Delay Time	t <sub>D(off)</sub>	—	126	—	ns	
Turn-Off Fall Time	t <sub>f</sub>	—	83	—	ns	
Body Diode Reverse Recovery Time	t <sub>rr</sub>	—	19.5	—	nS	I <sub>F</sub> = -10A, di/dt = 100A/µs
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	—	9.8	—	nC	

- Notes: 5. Device mounted on FR-4 PC board, with minimum recommended pad layout, single sided.  
6. Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1-inch square copper plate.  
7. Short duration pulse test used to minimize self-heating effect.  
8. Guaranteed by design. Not subject to product testing.



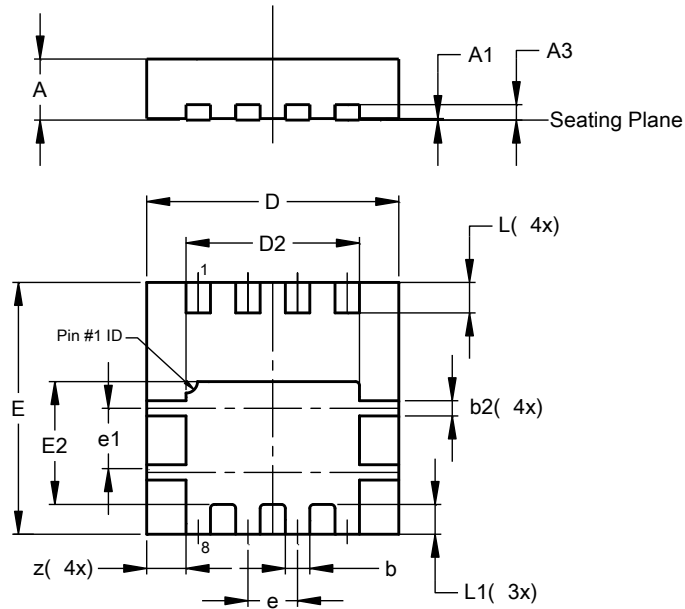




## Package Outline Dimensions

Please see AP02002 at <http://www.diodes.com/datasheets/ap02002.pdf> for the latest version.

### POWERDI®3333-8

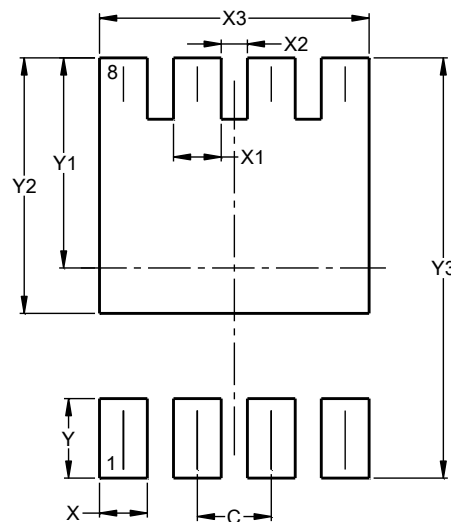


POWERDI®3333-8			
Dim	Min	Max	Typ
A	0.75	0.85	0.80
A1	0.00	0.05	0.02
A3	—	—	0.203
b	0.27	0.37	0.32
b2	—	—	0.20
D	3.25	3.35	3.30
D2	2.22	2.32	2.27
E	3.25	3.35	3.30
E2	1.56	1.66	1.61
e	—	—	0.65
e1	0.79	0.89	0.84
L	0.35	0.45	0.40
L1	—	—	0.39
z	—	—	0.515
All Dimensions in mm			

## Suggested Pad Layout

Please see AP02001 at <http://www.diodes.com/datasheets/ap02001.pdf> for the latest version.

### POWERDI®3333-8



Dimensions	Value (in mm)
C	0.650
X	0.420
X1	0.420
X2	0.230
X3	2.370
Y	0.700
Y1	1.850
Y2	2.250
Y3	3.700

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