ELECTRICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)						
PARAMETER	**TEST CONDITIONS / REMARKS**	**SYMBOL**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Protection paths	Number of lines which can be protected	$N_{channel}$	-	-	1	lines
Reverse stand-off voltage	Pin 1 to pin 2; max. reverse working voltage	V_{RWM}	-	-	16	V
	Pin 2 to pin 1; max. reverse working voltage		-	-	26.5	
Reverse voltage	Pin 1 to pin 2; at I_R = 0.05 µA	V_R	16	-	-	V
	Pin 2 to pin 1; at I_R = 0.05 µA		26.5	-	-	
Reverse current	Pin 1 to pin 2; at V_{RWM} = 16 V	I_R	-	-	0.05	µA
	Pin 2 to pin 1; at V_{RWM} = 26.5 V		-	-	0.05	
Reverse breakdown voltage	Pin 1 to pin 2; at I_R = 1 mA	V_{BR}	17.1	18.7	20.3	V
	Pin 2 to pin 1; at I_R = 1 mA		28	30	32	
Reverse clamping voltage	Pin 1 to pin 2; at I_{PP} = 1 A; t_p = 8/20 µs	V_C	-	22	25	V
	Pin 1 to pin 2; at I_{PP} = 6 A; t_p = 8/20 µs		-	29	33	
	Pin 2 to pin 1; at I_{PP} = 1 A; t_p = 8/20 µs		-	32	40	
	Pin 2 to pin 1; at I_{PP} = 4 A; t_p = 8/20 µs		-	39	50	
Capacitance	At V_R = 0 V, f = 1 MHz	C_D	-	15.5	18	pF

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

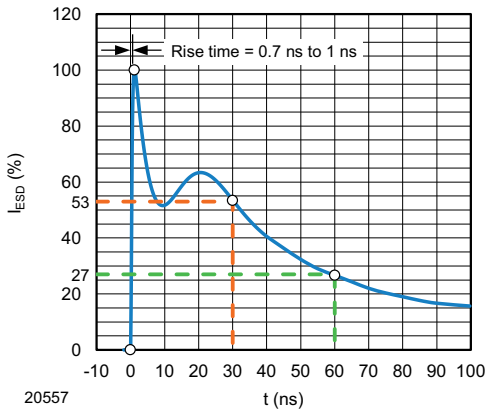


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

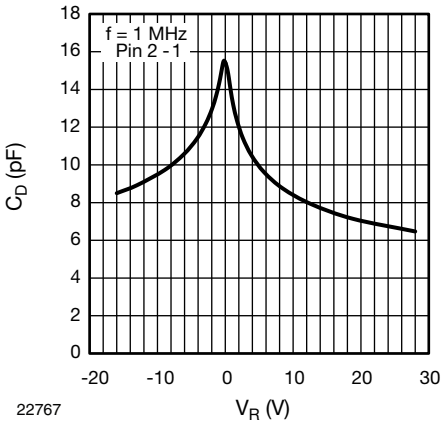


Fig. 3 - Typical Capacitance C_D vs. Reverse Voltage V_R

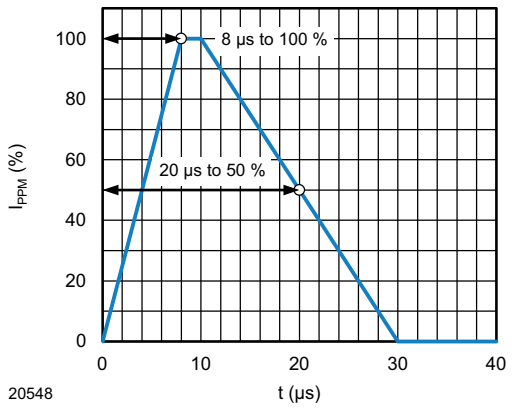


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

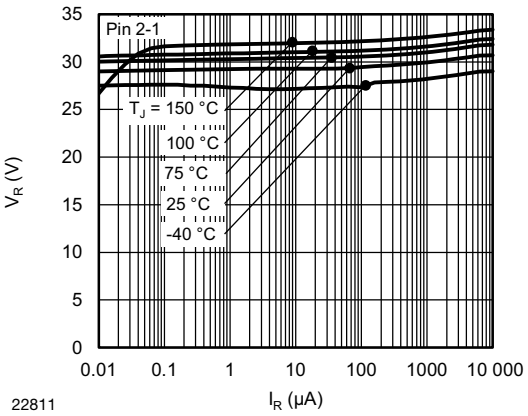


Fig. 4 - Typical Reverse Voltage V_R vs. Reverse Current I_R

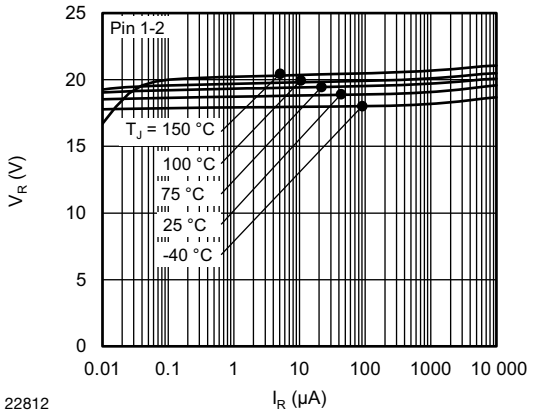


Fig. 5 - Typical Reverse Voltage V_R vs. Reverse Current I_R

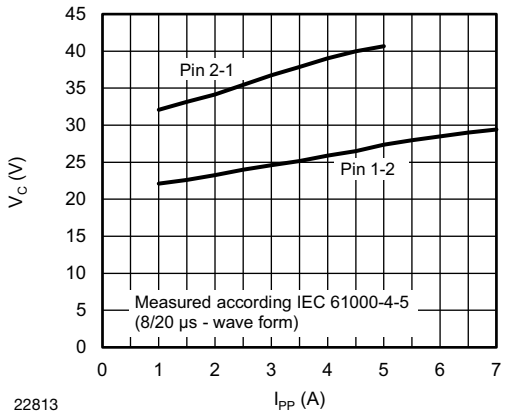


Fig. 6 - Typical Peak Clamping Voltage V_C vs.
Peak Pulse Current I_{PP}

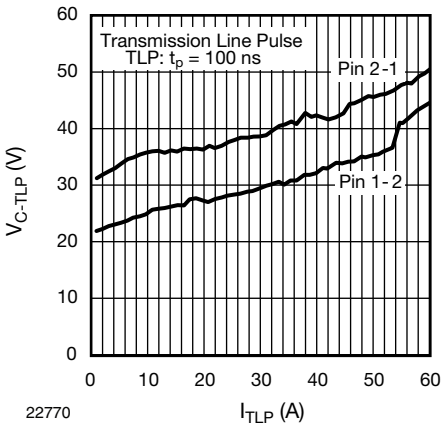
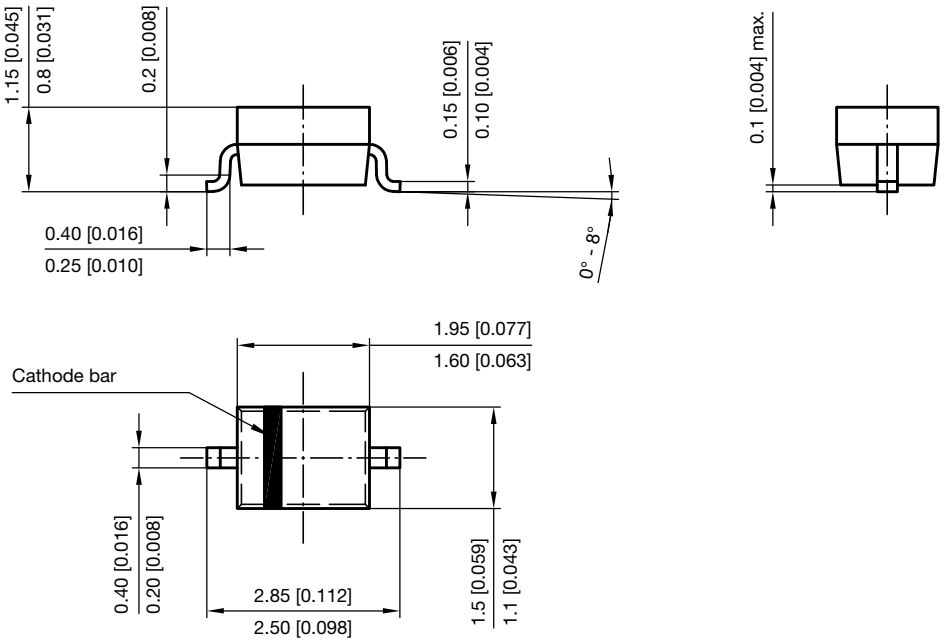
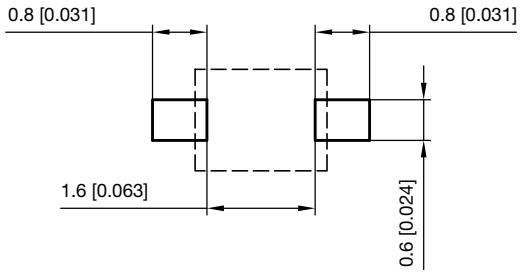


Fig. 7 - Typical Clamping Voltage V_{C-TLP} vs. Pulse Current I_{TLP}

PACKAGE DIMENSIONS in millimeters (inches) **SOD-323**



Footprint recommendation:



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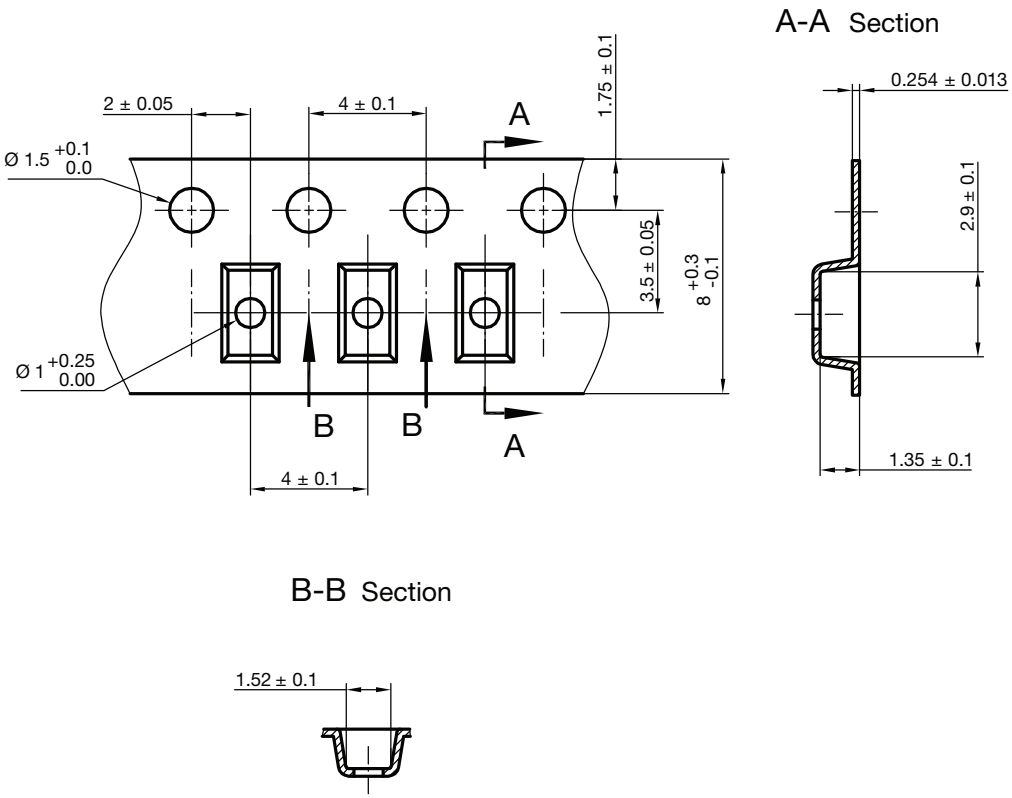
22771

CARRIER TAPE SOD-323



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Created - Date: 09. Feb. 2010
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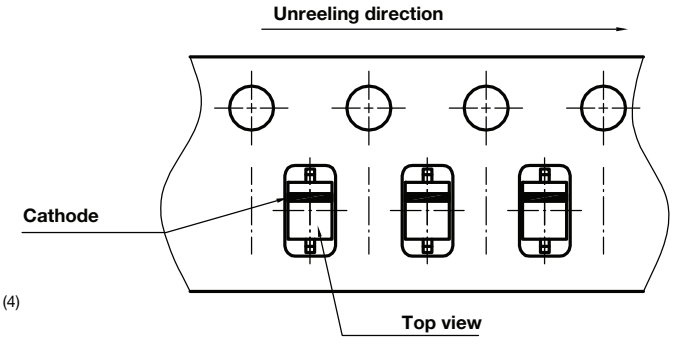
ORIENTATION IN CARRIER TAPE SOD-323



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