

Absolute Maximum Rating

Rating	Symbol	Value	Units
Peak Pulse Power (tp = 8/20μs)	P _{pk}	100	Watts
Peak Pulse Current (tp = 8/20μs)	I _{PP}	8	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V _{ESD}	20 15	kV
Lead Soldering Temperature	T _L	260 (10 seconds)	°C
Operating Temperature	T _J	-55 to +125	°C
Storage Temperature	T _{STG}	-55 to +150	°C

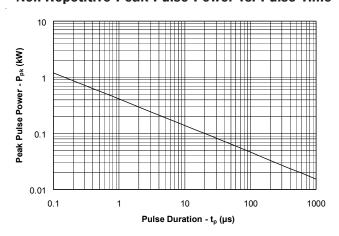
Electrical Characteristics

SMF05C						
Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V _{RWM}				5	V
Reverse Breakdown Voltage	V _{BR}	I _t = 1mA	6			V
Reverse Leakage Current	I _R	V _{RWM} = 5V, T=25°C			5	μΑ
Clamping Voltage	V _c	$I_{pp} = 5A, t_p = 8/20\mu s$			9.8	V
Clamping Voltage	V _c	$I_{pp} = 8A, t_{p} = 8/20 \mu s$			12.5	V
Junction Capacitance	C _j	V _R = OV, f = 1MHz			130	pF

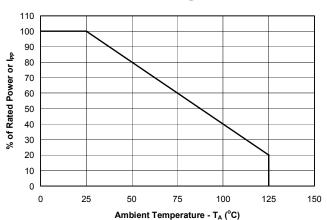


Typical Characteristics

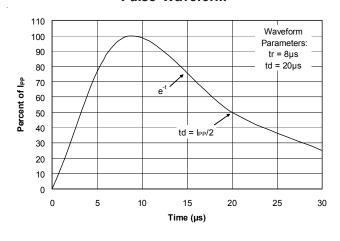
Non-Repetitive Peak Pulse Power vs. Pulse Time



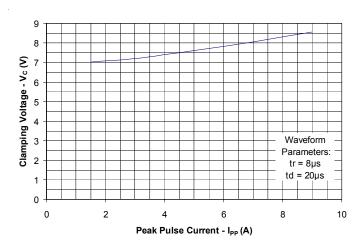
Power Derating Curve



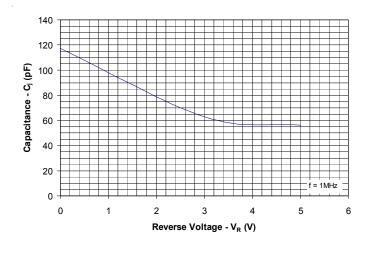
Pulse Waveform



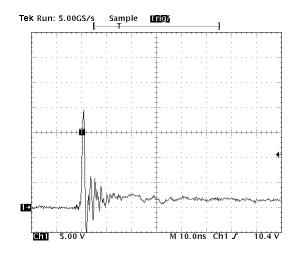
Clamping Voltage vs. Peak Pulse Current



Capacitance vs. Reverse Voltage



ESD Clamping Characteristics (8kV Contact Discharge per IEC 61000-4-2)





Applications Information

Device Connection for Protection of Five Data Lines

The SMF05C is designed to protect up to five unidirectional data lines. The device is connected as follows:

1. Unidirectional protection of five I/O lines is achieved by connecting pins 1, 3, 4, 5 and 6 to the data lines. Pin 2 is connected to ground. The ground connection should be made directly to the ground plane for best results. The path length is kept as short as possible to reduce the effects of parasitic inductance in the board traces.

Circuit Board Layout Recommendations for Suppression of ESD.

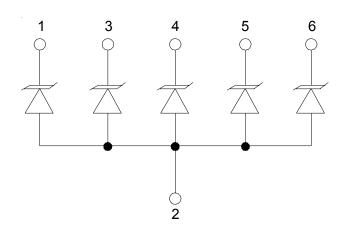
Good circuit board layout is critical for the suppression of ESD induced transients. The following guidelines are recommended:

- Place the SMF05C near the input terminals or connectors to restrict transient coupling.
- Minimize the path length between the SMF05C and the protected line.
- Minimize all conductive loops including power and ground loops.
- The ESD transient return path to ground should be kept as short as possible.
- Never run critical signals near board edges.
- Use ground planes whenever possible.

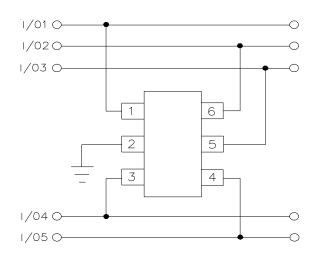
Matte Tin Lead Finish

Matte tin has become the industry standard lead-free replacement for SnPb lead finishes. A matte tin finish is composed of 100% tin solder with large grains. Since the solder volume on the leads is small compared to the solder paste volume that is placed on the land pattern of the PCB, the reflow profile will be determined by the requirements of the solder paste. Therefore, these devices are compatible with both lead-free and SnPb assembly techniques. In addition, unlike other lead-free compositions, matte tin does not have any added alloys that can cause degradation of the solder joint.

SMF05C Circuit Diagram

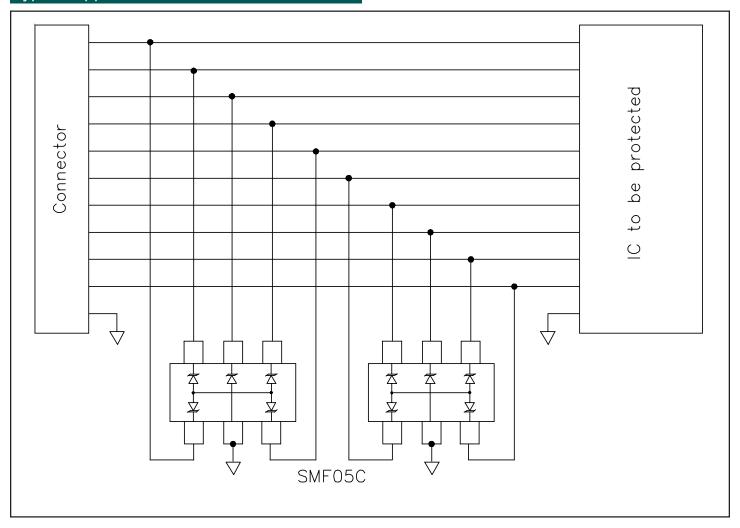


Protection of Five Unidirectional Lines



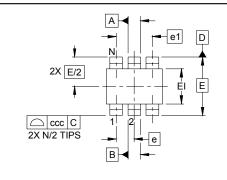


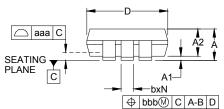
Typical Applications

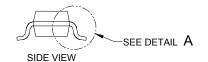


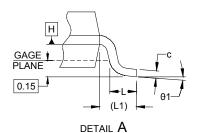


Outline Drawing SC-70 6L







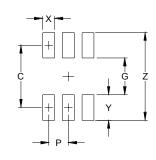


DIMENSIONS						
DIM	INCHES		MILLIMETERS		ERS	
DIIVI	MIN	NOM	MAX	MIN	NOM	MAX
Α	-	-	.043	-	-	1.10
A1	.000	-	.004	0.00	-	0.10
A2	.028	.035	.039	0.70	0.90	1.00
b	.006	-	.012	0.15	-	0.30
С	.003	-	.009	0.08	-	0.22
D	.075	.079	.083	1.90	2.00	2.10
E1	.045	.049	.053	1.15	1.25	1.35
E	.(.083 BSC 2.10 BSC		C		
е	.(026 BS	С	0	.65 BS	С
e1		.051		1	.30 BS	С
L	.010	.014	.018	0.26	0.36	0.46
L1		(.017)			(0.42)	
N		6			6	
θ1	0°	-	8°	0°	-	8°
aaa	.004		0.10			
bbb	.004		0.10			
CCC	.012		0.30			

NOTES:

- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. DATUMS -A- AND -B- TO BE DETERMINED AT DATUM PLANE-H-
- 3. DIMENSIONS "E1" AND "D" DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- 4. REFERENCE JEDEC STD MO-203, VARIATION AB.

Land Pattern - SC-70 6L



DIMENSIONS				
DIM	INCHES	MILLIMETERS		
С	(.073)	(1.85)		
G	.039	1.00		
Р	.026	0.65		
Х	.016	0.40		
Υ	.033	0.85		
Ζ	.106	2.70		

NOTES:

THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY.
CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR
COMPANY'S MANUFACTURING GUIDELINES ARE MET.



Marking Codes

Part Number	Marking Code	
SMF05C	5C	

Note:

(1) Pin 1 Identified with a dot

Ordering Information

Part Number	Lead Finish	Qty per Reel	Reel Size
SMF05C.TC	SnPb	3,000	7 Inch
SMF05C.TCT	Matte Sn	3,000	7 Inch

Contact Information for Semtech International AG

Taiwan Branch	Tel: 886-2-2748-3380 Fax: 886-2-2748-3390	Semtech Switzerland GmbH Japan Branch	Tel: 81-3-6408-0950 Fax: 81-3-6408-0951
Korea Branch	Tel: 82-2-527-4377 Fax: 82-2-527-4376	Semtech Limited (U.K.)	Tel: 44-1794-527-600 Fax: 44-1794-527-601
Shanghai Office	Tel: 86-21-6391-0830 Fax: 86-21-6391-0831	Semtech France SARL	Tel: 33-(0)169-28-22-00 Fax: 33-(0)169-28-12-98
Semtech International AG is a wholly-owned subsidiary of Semtech Corporation, which has its headquarters in the U.S.A.		Semtech Germany GmbH	Tel: 49-(0)8161-140-123 Fax: 49-(0)8161-140-124