

## Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	20	V
Working Peak Reverse Voltage	V <sub>RWM</sub>		
DC Blocking Voltage	V <sub>RM</sub>		
RMS Reverse Voltage	V <sub>R(RMS)</sub>	14	V
Average Rectified Output Current (See Figure 4)	I <sub>O</sub>	500	mA
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	10	A

## Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Ambient (Note 5)	R <sub>θJA</sub>	236	°C/W
Operating and Storage Temperature Range	T <sub>J</sub> , T <sub>STG</sub>	-65 to +150	°C

## Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Forward Voltage Drop	V <sub>F</sub>	—	0.28	0.32	V	I <sub>F</sub> = 0.1A, T <sub>J</sub> = +25°C
		—	0.30	0.34		I <sub>F</sub> = 0.2A, T <sub>J</sub> = +25°C
		—	0.35	0.39		I <sub>F</sub> = 0.5A, T <sub>J</sub> = +25°C
Leakage Current (Note 6)	I <sub>R</sub>	—	11	50	μA	V <sub>R</sub> = 20V, T <sub>J</sub> = +25°C
			2.5	10	mA	V <sub>R</sub> = 20V, T <sub>J</sub> = +125°C
Total Capacitance	C <sub>T</sub>	—	14	—	pF	f = 1MHz, V <sub>R</sub> = 20V
Reverse Recovery Time	t <sub>rr</sub>	—	15	—	ns	I <sub>F</sub> = I <sub>R</sub> = 10mA, I <sub>R(REC)</sub> = 1mA, R <sub>L</sub> = 100Ω
		—	6	—		I <sub>F</sub> = 500mA, di/dt = 600A/μs, V <sub>R</sub> = 10V

Notes: 5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.  
6. Short duration pulse test used to minimize self-heating effect.

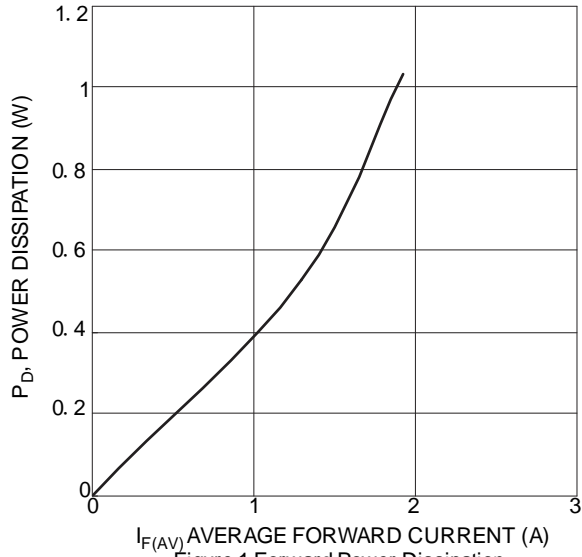


Figure 1 Forward Power Dissipation

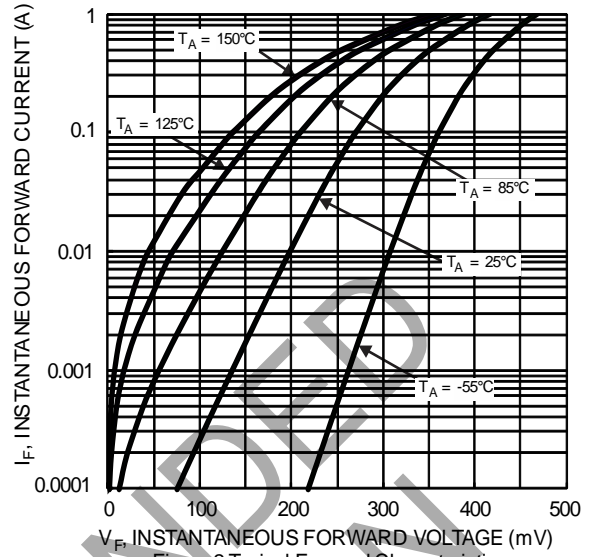


Figure 2 Typical Forward Characteristics

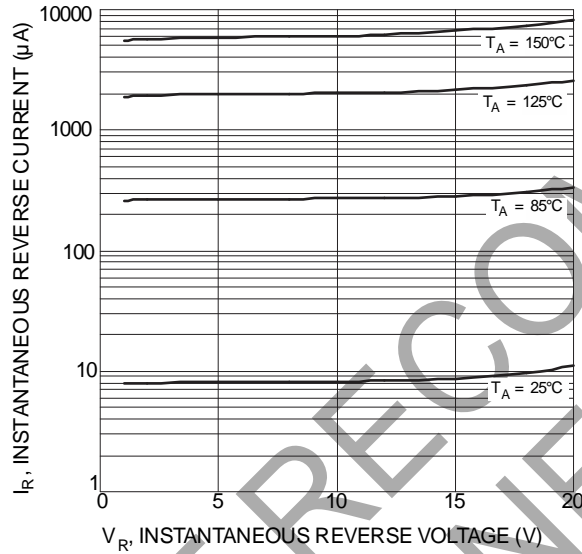


Figure 3 Typical Reverse Characteristics

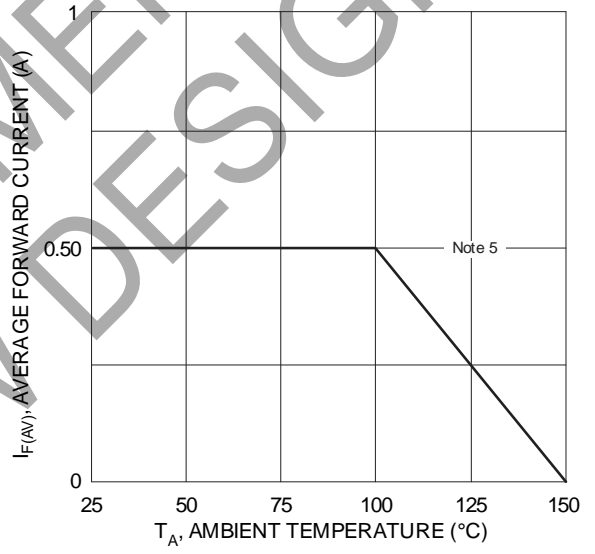


Figure 4 Forward Current Derating Curve

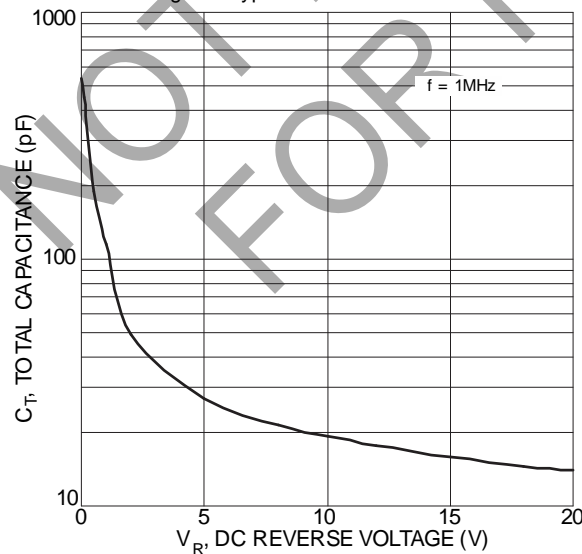
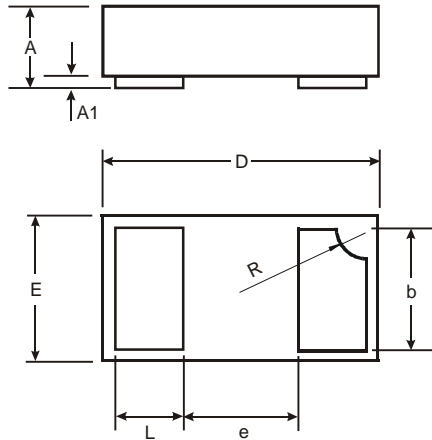


Figure 5 Total Capacitance vs. Reverse Voltage

## Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X1-DFN1006-2**

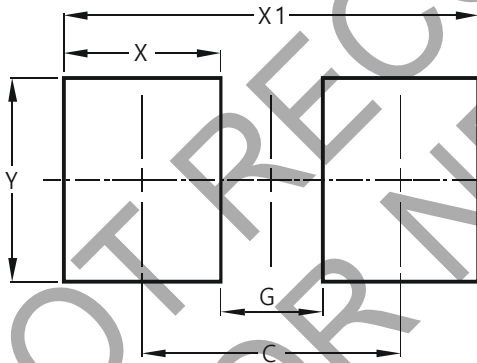


X1-DFN1006-2			
Dim	Min	Max	Typ
A	0.47	0.53	0.50
A1	0	0.05	0.03
b	0.45	0.55	0.50
D	0.95	1.075	1.00
E	0.55	0.675	0.60
e	—	—	0.40
L	0.20	0.30	0.25
R	0.05	0.15	0.10
All Dimensions in mm			

## Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

**X1-DFN1006-2**



Dimensions	Value (in mm)
C	0.70
G	0.30
X	0.40
X1	1.10
Y	0.70

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