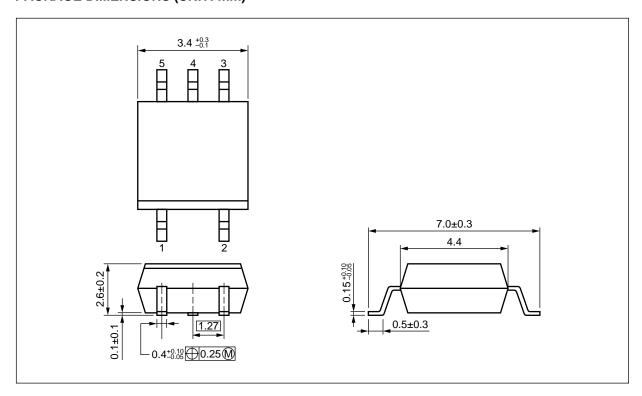
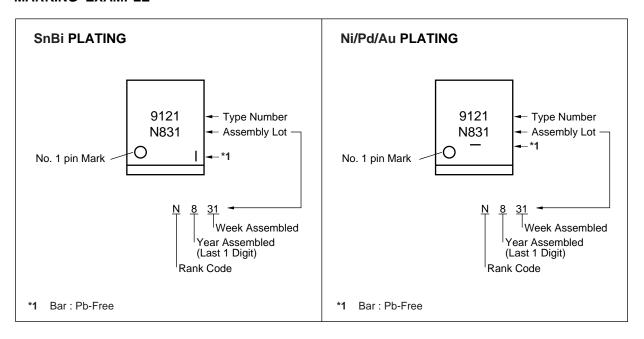
## PACKAGE DIMENSIONS (UNIT: mm)



### <R> MARKING EXAMPLE



## <R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style Safety Standards Approval		Application *1 Part Number
PS9121	PS9121-A	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9121
PS9121-F3	PS9121-F3-A	(SnBi)	Embossed Tape 2 500 pcs/reel	(UL approved)	
PS9121-F4	PS9121-F4-A				
PS9121-V	PS9121-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	
PS9121-V-F3	PS9121-V-F3-A		Embossed Tape 2 500 pcs/reel	(VDE0884 Part2)	
PS9121-V-F4	PS9121-V-F4-A			approved (Option)	
PS9121	PS9121-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	
PS9121-F3	PS9121-F3-AX	(Ni/Pd/Au)	Embossed Tape 2 500 pcs/reel	(UL approved)	
PS9121-F4	PS9121-F4-AX				
PS9121-V	PS9121-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	
PS9121-V-F3	PS9121-V-F3-AX		Embossed Tape 2 500 pcs/reel	(VDE0884 Part2)	
PS9121-V-F4	PS9121-V-F4-AX			approved (Option)	

<sup>\*1</sup> For the application of the Safety Standard, following part number should be used.

## ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current*1	lF	30	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	7	V
	Output Voltage	Vo	7	V
	Output Current	lo	25	mA
	Power Dissipation*2	Pc	40	mW
Isolation Voltage*3		BV	3 750	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		T <sub>stg</sub>	-55 to +125	°C

- \*1 Reduced to 0.3 mA/ $^{\circ}$ C at T<sub>A</sub> = 25 $^{\circ}$ C or more.
- \*2 Applies to output pin Vo (collector pin). Reduced to 1.5 mW/ $^{\circ}$ C at T<sub>A</sub> = 65 $^{\circ}$ C or more.
- \*3 AC voltage for 1 minute at  $T_A = 25^{\circ}C$ , RH = 60% between input and output. Pins 1-2 shorted together, 3-5 shorted together.

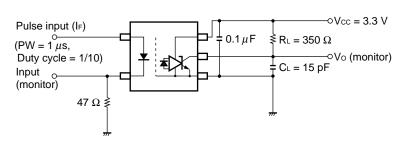
### RECOMMENDED OPERATING CONDITIONS

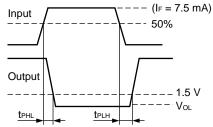
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	V <sub>FL</sub>	0		0.8	V
High Level Input Current	lғн	6.3	10	12.5	mA
Supply Voltage	Vcc	2.7	3.3	3.6	V
TTL (R <sub>L</sub> = 1 kΩ, loads)	N			5	
Pull-up Resistor	RL	330		4 k	Ω

## ELECTRICAL CHARACTERISTICS ( $T_A = -40 \text{ to } +85^{\circ}\text{C}$ , unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP.*1	MAX.	Unit
Diode	Forward Voltage	VF	IF = 10 mA, TA = 25°C	1.4	1.65	1.8	V
	Reverse Current	IR	V <sub>R</sub> = 3 V, T <sub>A</sub> = 25°C			10	μΑ
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		30		pF
Detector	High Level Output Current	Іон	Vcc = Vo = 3.3 V, V <sub>F</sub> = 0.8 V		1	80	μΑ
			Vcc = Vo = 5.5 V, V <sub>F</sub> = 0.8 V		1*2		
	Low Level Output Voltage*3	Vol	$Vcc = 3.3 \text{ V}, I_F = 5 \text{ mA}, I_{OL} = 13 \text{ mA}$		0.2	0.6	V
			$Vcc = 5.5 \text{ V}, I_F = 5 \text{ mA}, I_{OL} = 13 \text{ mA}$		0.2*2		
	High Level Supply Current	Іссн	Vcc = 3.3 V, I <sub>F</sub> = 0 mA, Vo = Open		4	7	mA
			Vcc = 5.5 V, I <sub>F</sub> = 0 mA, Vo = Open		5 <sup>*2</sup>		
	Low Level Supply Current	Iccl	Vcc = 3.3 V, I <sub>F</sub> = 10 mA, Vo = Open		7	10	1
			Vcc = 5.5 V, I <sub>F</sub> = 10 mA, Vo = Open		9 <sup>*2</sup>		
Coupled	Threshold Input Current	IFHL	$Vcc = 3.3 \text{ V}, Vo = 0.8 \text{ V}, RL = 350 \Omega$		2.5	5	
	$(H \rightarrow L)$		$Vcc = 5 \text{ V}, Vo = 0.8 \text{ V}, R_L = 350 \Omega$		2.5 <sup>*2</sup>		
	Isolation Resistance	Ri-o	V <sub>I-O</sub> = 1 kV <sub>DC</sub> , RH = 40 to 60%, T <sub>A</sub> = 25°C	10 <sup>11</sup>			Ω
	Isolation Capacitance	Cı-o	V = 0 V, f = 1 MHz, T <sub>A</sub> = 25°C		0.6		pF
	Propagation Delay Time	<b>t</b> PHL	T <sub>A</sub> = 25°C		40	75	ns
	$(H \rightarrow L)^{*4}$		$Vcc = 3.3 \text{ V}, \text{ R}_L = 350 \Omega, \text{ I}_F = 7.5 \text{ mA}$			100	
			$Vcc = 5 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		37 <sup>*2</sup>		
	Propagation Delay Time	<b>t</b> PLH	T <sub>A</sub> = 25°C		45	75	
	$(L \rightarrow H)^{*4}$		$Vcc = 3.3 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$			100	
			$Vcc = 5 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		40 <sup>*2</sup>		
	Rise Time	tr	$Vcc = 3.3 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		20		
			$Vcc = 5 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		20 <sup>*2</sup>		
	Fall Time	tf	$Vcc = 3.3 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		5		
			$Vcc = 5 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		5 <sup>*2</sup>		
	Pulse Width Distortion	tphl-tplh	$Vcc = 3.3 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		5	35	
	(PWD)*4		$Vcc = 5 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$		3*2		
	Propagation Delay Skew	<b>t</b> PSK	$Vcc = 3.3 \text{ V}, \text{ RL} = 350 \Omega, \text{ IF} = 7.5 \text{ mA}$			40	
-	Common Mode Transient Immunity at High	СМн	$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 3.3 \ \mbox{V, RL} = 350 \ \Omega, \ \mbox{Ta} = 25 \ensuremath{^{\circ}}\mbox{C}, \\ \mbox{IF} = 0 \ \mbox{mA, Vo} > 2 \ \mbox{V, VcM} = 1 \ \mbox{kV} \end{array}$	15	20		kV/μs
	Level Output*5		$\label{eq:Vcc} \begin{array}{l} \mbox{Vcc} = 5 \mbox{ V, RL} = 350 \ \Omega, \mbox{ TA} = 25 \mbox{°C}, \\ \mbox{IF} = 0 \mbox{ mA, Vo} > 2 \mbox{ V, VcM} = 1 \mbox{ kV} \end{array}$		20 <sup>*2</sup>		
	Common Mode Transient Immunity at Low		$V_{CC} = 3.3 \text{ V, RL} = 350 \Omega, T_A = 25^{\circ}\text{C},$ $I_F = 7.5 \text{ mA}, V_O < 0.8 \text{ V, V}_{CM} = 1 \text{ kV}$	15	20		
	Level Output*5		$V_{CC} = 5$ V, $R_L = 350$ Ω, $T_A = 25$ °C, $I_F = 7.5$ mA, $V_{C} < 0.8$ V, $V_{CM} = 1$ kV		20 <sup>*2</sup>		

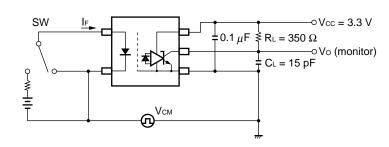
- \*1 Typical values at T<sub>A</sub> = 25°C
- \*2 These values are reference values.
- \*3 Because Vol of 2 V or more may be output when LED current input and when output supply of Vcc = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.
- \*4 Test circuit for propagation delay time

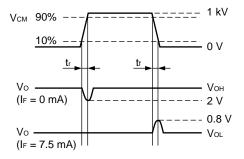




Remark CL includes probe and stray wiring capacitance.

\*5 Test circuit for common mode transient immunity



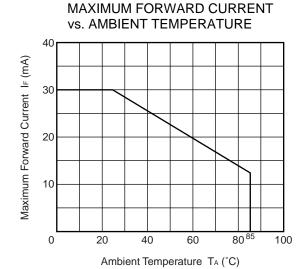


Remark CL includes probe and stray wiring capacitance.

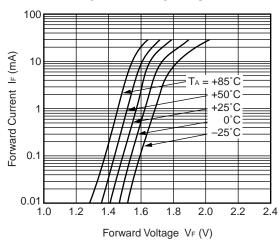
### **USAGE CAUTIONS**

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of 0.1  $\mu$ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.

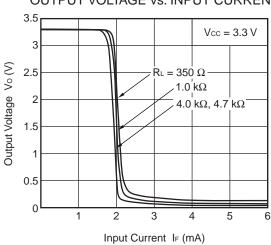
### TYPICAL CHARACTERISTICS (TA = 25°C, unless otherwise specified)



# FORWARD CURRENT vs. FORWARD VOLTAGE

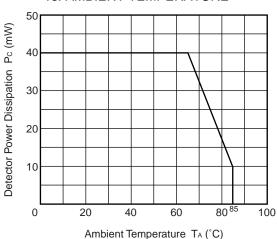


## OUTPUT VOLTAGE vs. INPUT CURRENT

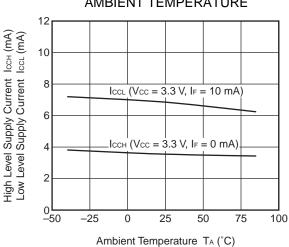


**Remark** The graphs indicate nominal characteristics.

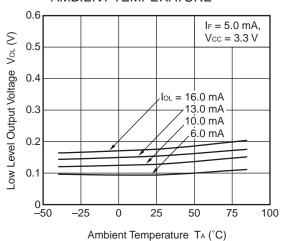




## SUPPLY CURRENT vs. AMBIENT TEMPERATURE

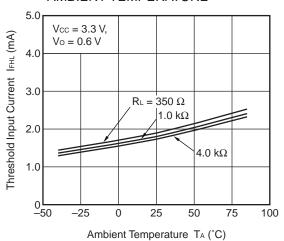


# LOW LEVEL OUTPUT VOLTAGE vs. AMBIENT TEMPERATURE

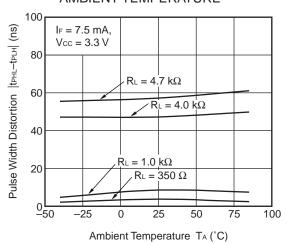


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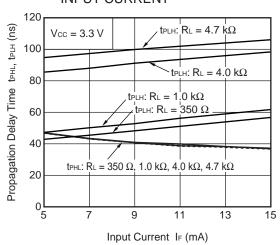
# THRESHOLD INPUT CURRENT vs. AMBIENT TEMPERATURE



# PULSE WIDTH DISTORTION vs. AMBIENT TEMPERATURE

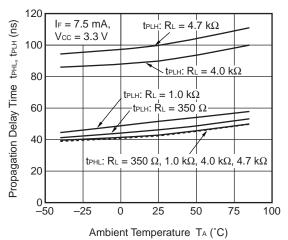


# PROPAGATION DELAY TIME vs. INPUT CURRENT

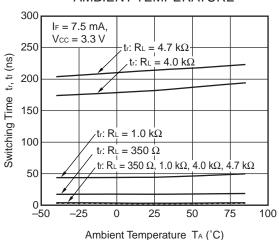


Remark The graphs indicate nominal characteristics.

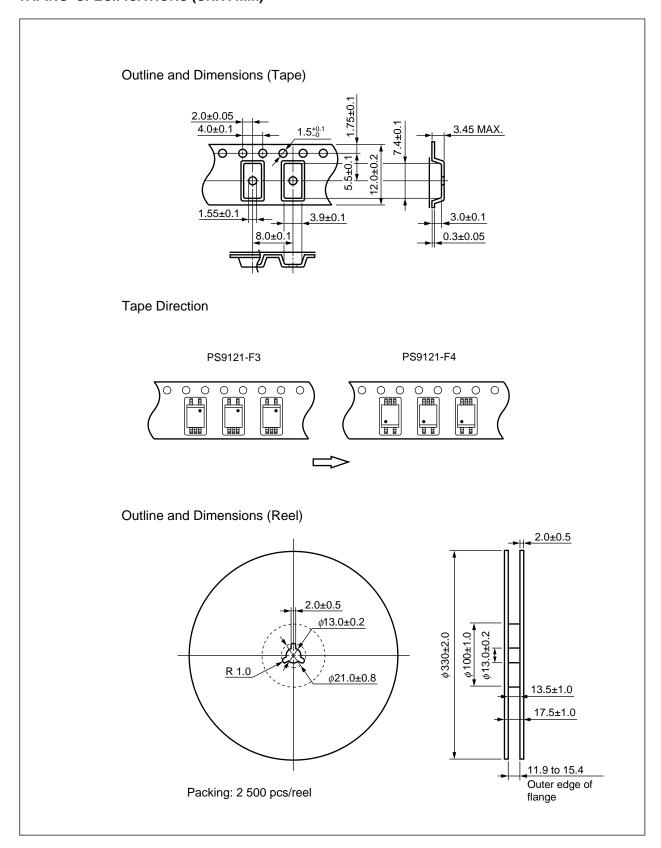
# PROPAGATION DELAY TIME vs. AMBIENT TEMPERATURE



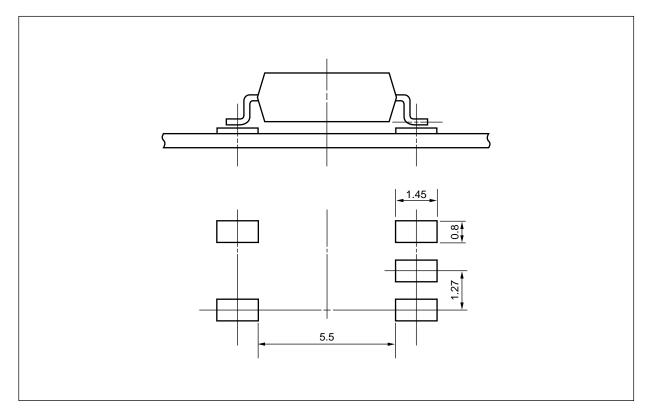
## SWITCHING TIME vs. AMBIENT TEMPERATURE



## TAPING SPECIFICATIONS (UNIT: mm)



## <R> RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



#### NOTES ON HANDLING

### 1. Recommended soldering conditions

#### (1) Infrared reflow soldering

Peak reflow temperature
 260°C or below (package surface temperature)

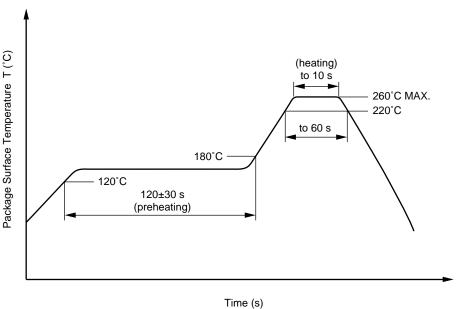
Time of peak reflow temperature
 Time of temperature higher than 220°C
 60 seconds or less

Time to preheat temperature from 120 to 180°C 120±30 s
 Number of reflows Three

• Flux Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

### Recommended Temperature Profile of Infrared Reflow



#### (2) Wave soldering

• Temperature 260°C or below (molten solder temperature)

• Time 10 seconds or less

• Preheating conditions 120°C or below (package surface temperature)

• Number of times One (Allowed to be dipped in solder including plastic mold portion.)

Flux
 Rosin flux containing small amount of chlorine (The flux with a maximum chlorine

content of 0.2 Wt% is recommended.)

### (3) Soldering by Soldering Iron

Peak Temperature (lead part temperature) 350°C or below
 Time (each pins) 3 seconds or less

Flux
 Rosin flux containing small amount of chlorine (The flux with a

maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

### (4) Cautions

• Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

## 2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

### **USAGE CAUTIONS**

- 1. Protect against static electricity when handling.
- 2. Avoid storage at a high temperature and high humidity.

## <R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Application classification (DIN EN 60664-1 VDE0110 Part 1) for rated line voltages $\leq$ 300 Vr.m.s. for rated line voltages $\leq$ 600 Vr.m.s.		IV III	
Climatic test class (DIN EN 60664-1 VDE0110)		40/85/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{\text{pr}} = 1.5 \times U_{\text{IORM}},  P_{\text{d}} < 5  \text{pC}$	UIORM Upr	707 1 061	V <sub>peak</sub> V <sub>peak</sub>
Test voltage (partial discharge test, procedure b for all devices) $U_{\text{pr}} = 1.875 \times U_{\text{IORM}},  P_{\text{d}} < 5  \text{pC}$	Upr	1 326	V <sub>peak</sub>
Highest permissible overvoltage	Utr	6 000	V <sub>peak</sub>
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Clearance distance		>4.2	mm
Creepage distance		>4.2	mm
Comparative tracking index (DIN IEC 112/VDE 0303 Part 1)	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	Tstg	-55 to +125	°C
Operating temperature range	TA	-40 to +85	°C
Isolation resistance, minimum value  VIO = 500 V dc at TA = 25°C  VIO = 500 V dc at TA MAX. at least 100°C	Ris MIN. Ris MIN.	10 <sup>12</sup> 10 <sup>11</sup>	Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I <sub>F</sub> , Psi = 0) Power (output or total power dissipation)	Tsi Isi Psi	150 200 300	°C mA mW
Isolation resistance Vio = 500 V dc at T <sub>A</sub> = Tsi	Ris MIN.	10 <sup>9</sup>	Ω

#### Caution

**GaAs Products** 

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
- Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
- 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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