

MURS320T3, MURS340T3, MURS360T3

MAXIMUM RATINGS

Rating	Symbol	MURS320T3	MURS340T3	MURS360T3	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	200	400	600	V
Average Rectified Forward Current	$I_{F(AV)}$	3.0 @ $T_L = 140^\circ\text{C}$ 4.0 @ $T_L = 130^\circ\text{C}$	3.0 @ $T_L = 130^\circ\text{C}$ 4.0 @ $T_L = 115^\circ\text{C}$	3.0 @ $T_L = 130^\circ\text{C}$ 4.0 @ $T_L = 115^\circ\text{C}$	A
Non-Repetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I_{FSM}	75			A
Operating Junction Temperature	T_J	- 65 to +175			$^\circ\text{C}$

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Lead	$R_{\theta JL}$	11	$^\circ\text{C/W}$
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ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 1) ($i_F = 3.0\text{ A}$, $T_J = 25^\circ\text{C}$) ($i_F = 4.0\text{ A}$, $T_J = 25^\circ\text{C}$) ($i_F = 3.0\text{ A}$, $T_J = 150^\circ\text{C}$)	V_F	0.875 0.89 0.71	1.25 1.28 1.05	1.25 1.28 1.05	V
Maximum Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_J = 25^\circ\text{C}$) (Rated dc Voltage, $T_J = 150^\circ\text{C}$)	i_R	5.0 150	10 250	10 250	μA
Maximum Reverse Recovery Time ($i_F = 1.0\text{ A}$, $di/dt = 50\text{ A}/\mu\text{s}$) ($i_F = 0.5\text{ A}$, $i_R = 1.0\text{ A}$, I_{REC} to 0.25 A)	t_{rr}	35 25	75 50	75 50	ns
Maximum Forward Recovery Time ($i_F = 1.0\text{ A}$, $di/dt = 100\text{ A}/\mu\text{s}$, Recovery to 1.0 V)	t_{fr}	25	50	50	ns

1. Pulse Test: Pulse Width = 300 μs , Duty Cycle $\leq 2.0\%$.

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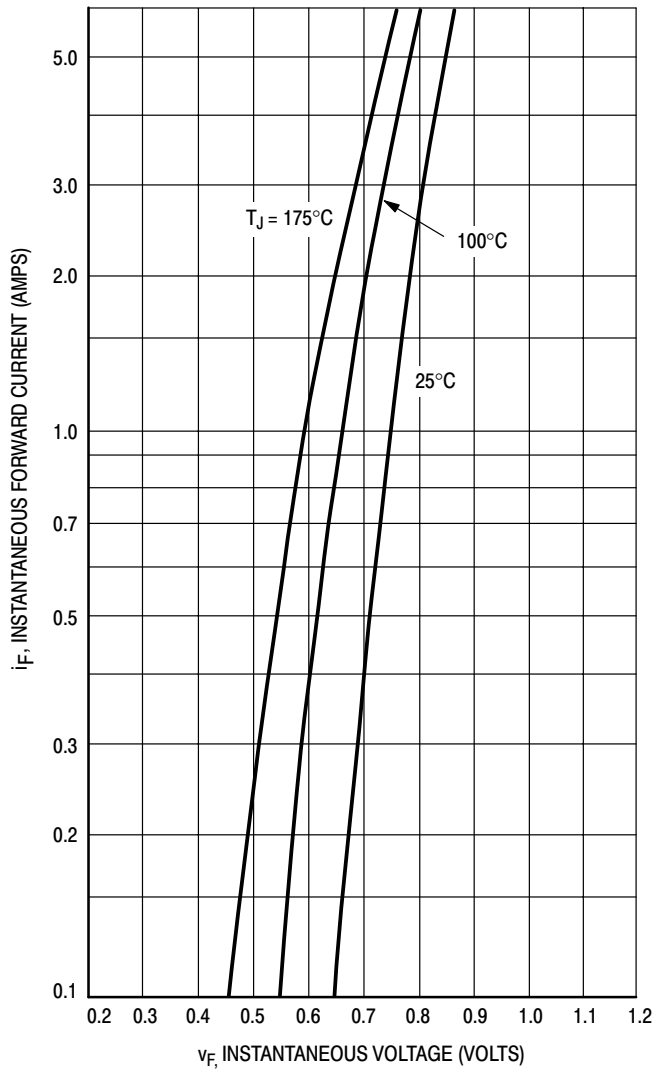


Figure 1. Typical Forward Voltage

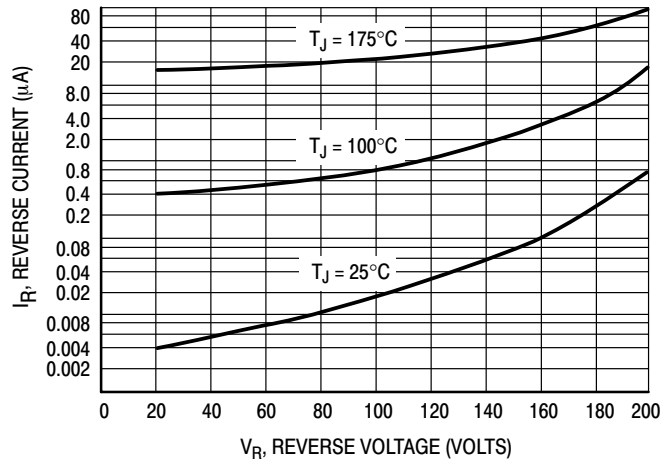


Figure 2. Typical Reverse Current*

* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if V_R is sufficiently below rated V_R .

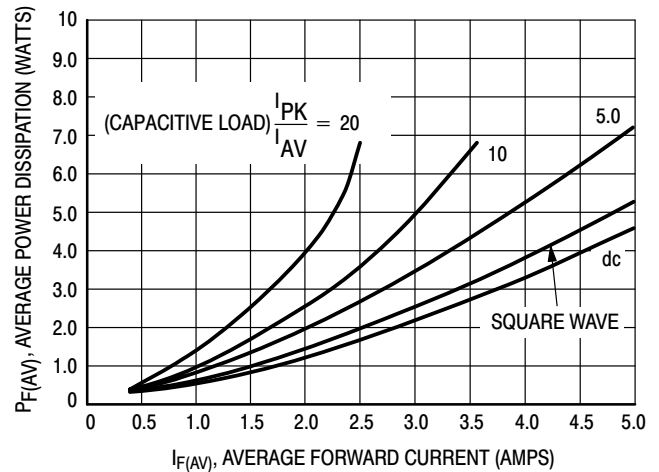


Figure 3. Power Dissipation

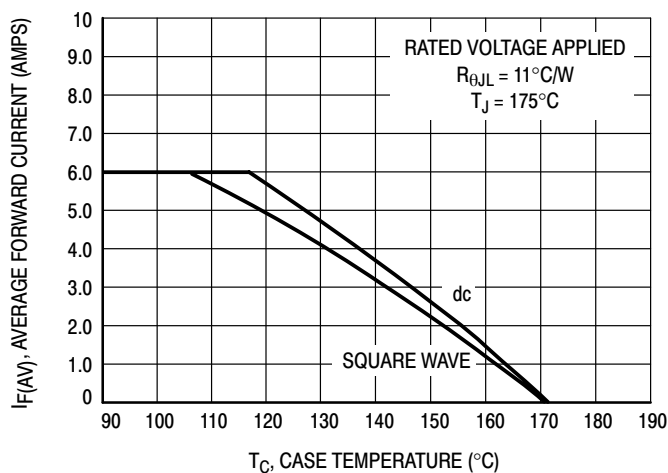


Figure 4. Current Derating, Case

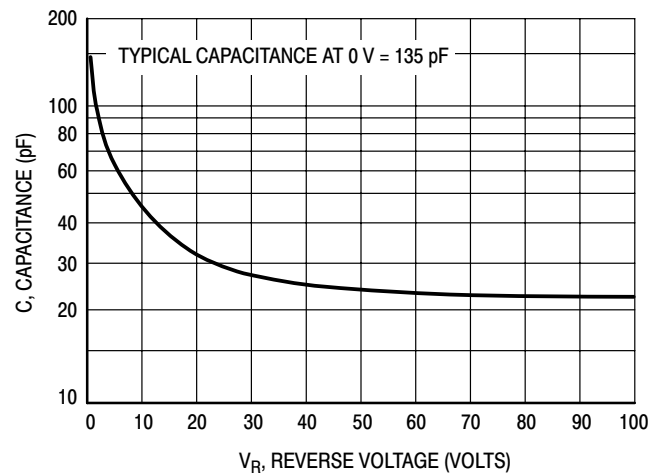


Figure 5. Typical Capacitance

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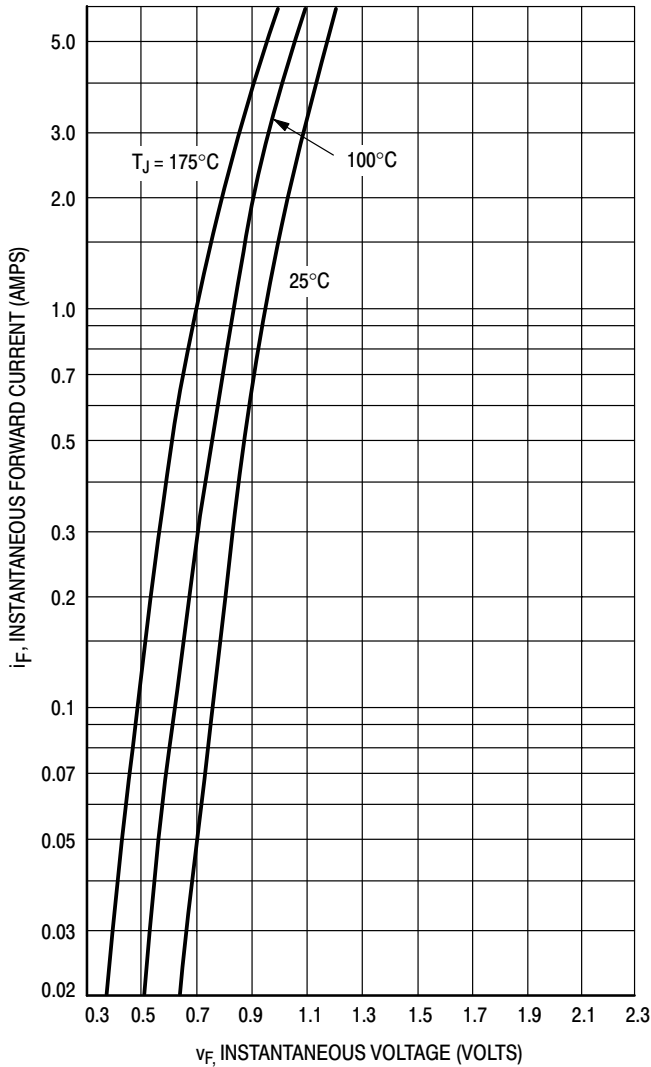


Figure 6. Typical Forward Voltage

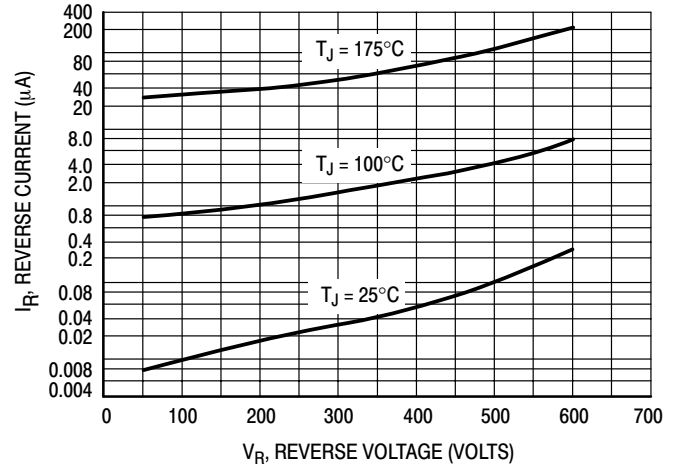


Figure 7. Typical Reverse Current*

* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if V_R is sufficiently below rated V_R .

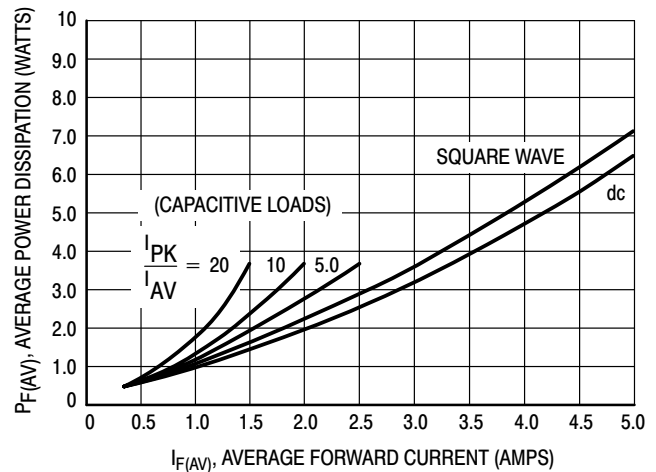


Figure 8. Power Dissipation

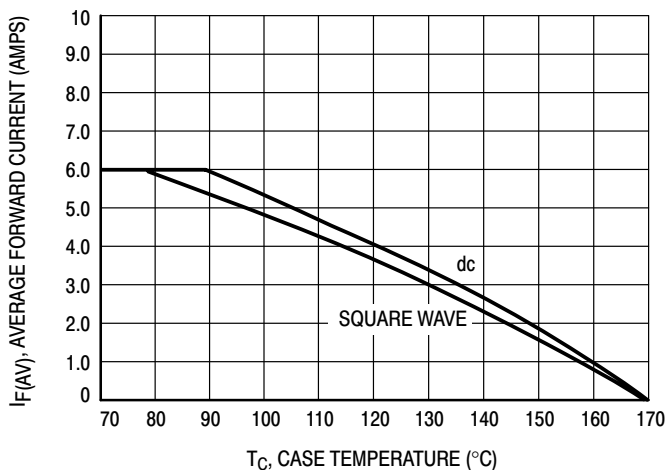


Figure 9. Current Derating, Case

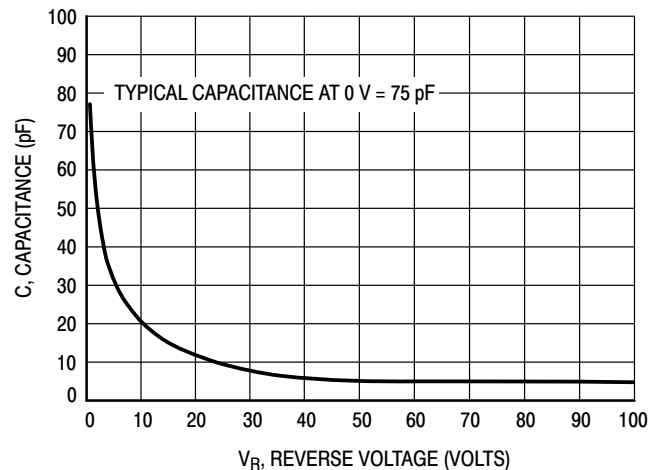
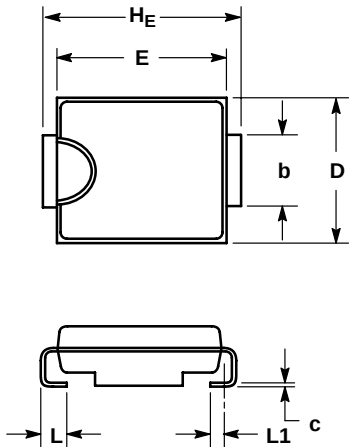


Figure 10. Typical Capacitance

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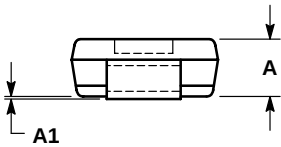
PACKAGE DIMENSIONS

SMC
PLASTIC PACKAGE
CASE 403-03
ISSUE E

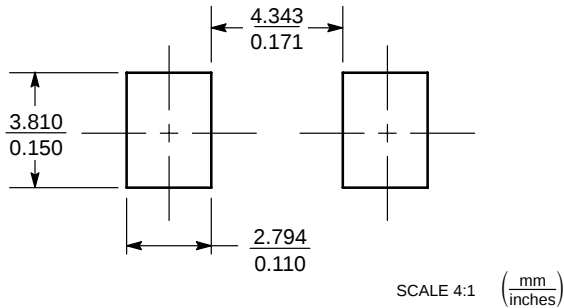


- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. D DIMENSION SHALL BE MEASURED WITHIN DIMENSION P.
 4. 403-01 THRU -02 OBSOLETE, NEW STANDARD 403-03.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.90	2.13	2.41	0.075	0.084	0.095
A1	0.05	0.10	0.15	0.002	0.004	0.006
b	2.92	3.00	3.07	0.115	0.118	0.121
c	0.15	0.23	0.30	0.006	0.009	0.012
D	5.59	5.84	6.10	0.220	0.230	0.240
E	6.60	6.86	7.11	0.260	0.270	0.280
HE	7.75	7.94	8.13	0.305	0.313	0.320
L	0.76	1.02	1.27	0.030	0.040	0.050
L1	0.51 REF			0.020 REF		



SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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