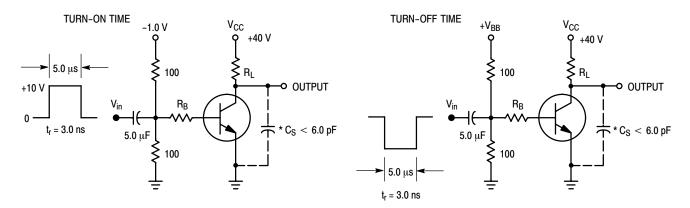
# MMBTA55LT1, MMBTA56LT1

## **ELECTRICAL CHARACTERISTICS** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS			-		
Collector – Emitter Breakdown Voltage (Note 3) $(I_C = -1.0 \text{ mAdc}, I_B = 0)$	MMBTA55 MMBTA56	V <sub>(BR)CEO</sub>	-60 -80	- -	Vdc
Emitter – Base Breakdown Voltage ( $I_E = -100 \ \mu Adc, I_C = 0$ )		V <sub>(BR)EBO</sub>	-4.0	_	Vdc
Collector Cutoff Current ( $V_{CE} = -60 \text{ Vdc}, I_B = 0$ )		I <sub>CES</sub>	-	-0.1	μAdc
Collector Cutoff Current $(V_{CB} = -60 \text{ Vdc}, I_E = 0)$ $(V_{CB} = -80 \text{ Vdc}, I_E = 0)$	MMBTA55 MMBTA56	I <sub>CBO</sub>		-0.1 -0.1	μAdc
ON CHARACTERISTICS				•	
DC Current Gain ( $I_C = -10$ mAdc, $V_{CE} = -1.0$ Vdc) ( $I_C = -100$ mAdc, $V_{CE} = -1.0$ Vdc)		h <sub>FE</sub>	100 100	- -	-
Collector – Emitter Saturation Voltage ( $I_C = -100$ mAdc, $I_B = -10$ mAdc)		V <sub>CE(sat)</sub>	_	-0.25	Vdc
Base – Emitter On Voltage ( $I_C = -100 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}$ )		$V_{BE(on)}$	_	-1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current-Gain – Bandwidth Product (Note 4) ( $I_C = -100 \text{ mAdc}, V_{CE} = -1.0 \text{ Vdc}, f = 100 \text{ MHz}$ )		f <sub>T</sub>	50	-	MHz

3. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

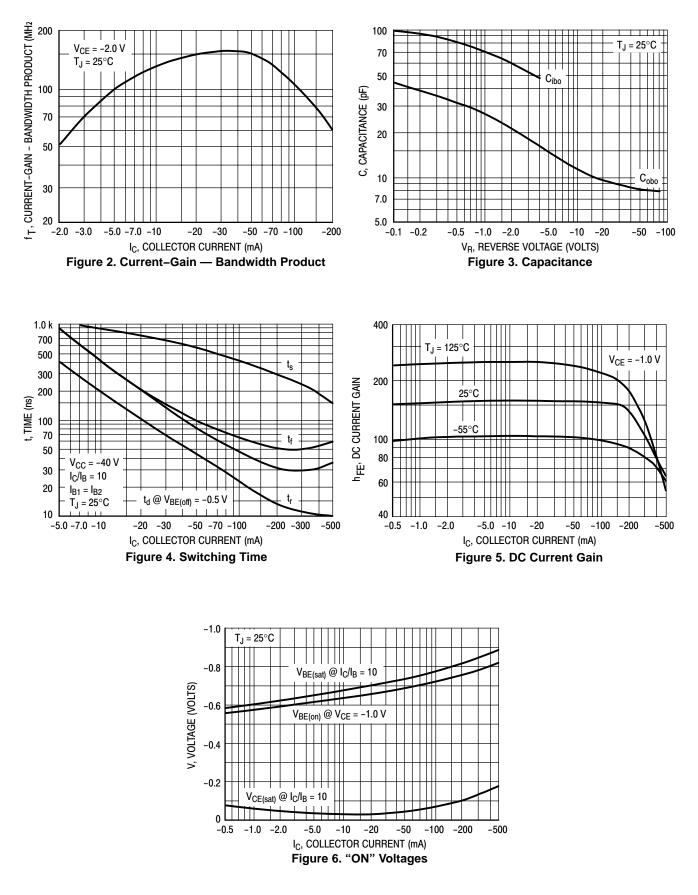
4.  $f_T$  is defined as the frequency at which  $|h_{fe}|$  extrapolates to unity.



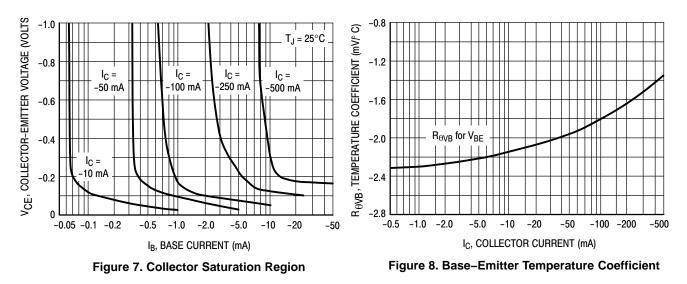
\*Total Shunt Capacitance of Test Jig and Connectors For PNP Test Circuits, Reverse All Voltage Polarities

## Figure 1. Switching Time Test Circuits

## MMBTA55LT1, MMBTA56LT1



## MMBTA55LT1, MMBTA56LT1



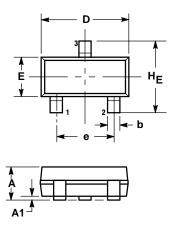
### **ORDERING INFORMATION**

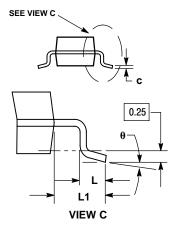
Device Order Number	Package Type	Shipping <sup>†</sup>	
MMBTA55LT1	SOT-23	3,000 / Tape & Reel	
MMBTA55LT1G	SOT-23 (Pb-Free)	3,000 / Tape & Reel	
MMBTA55LT3	SOT-23	10,000 / Tape & Reel	
MMBTA55LT3G	SOT–23 (Pb–Free)	10,000 / Tape & Reel	
MMBTA56LT1	SOT-23	3,000 / Tape & Reel	
MMBTA56LT1G	SOT–23 (Pb–Free)	3,000 / Tape & Reel	
MMBTA56LT3	SOT-23	10,000 / Tape & Reel	
MMBTA56LT3G	SOT-23 (Pb-Free)	10,000 / Tape & Reel	

<sup>†</sup>For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

### PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AN** 





NOTES

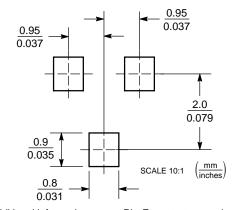
- DIMENSIONING AND TOLERANCING PER ANSI 1. Y14.5M, 1982. CONTROLLING DIMENSION: INCH
- 2. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF 3.
- BASE MATERIAL. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08. 4

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.89	1.00	1.11	0.035	0.040	0.044	
A1	0.01	0.06	0.10	0.001	0.002	0.004	
b	0.37	0.44	0.50	0.015	0.018	0.020	
С	0.09	0.13	0.18	0.003	0.005	0.007	
D	2.80	2.90	3.04	0.110	0.114	0.120	
E	1.20	1.30	1.40	0.047	0.051	0.055	
е	1.78	1.90	2.04	0.070	0.075	0.081	
L	0.10	0.20	0.30	0.004	0.008	0.012	
L1	0.35	0.54	0.69	0.014	0.021	0.029	
HE	2.10	2.40	2.64	0.083	0.094	0.104	

STYLE 6:

PIN 1. BASE 2. EMITTER 3. COLLECTOR

**SOLDERING FOOTPRINT\*** 



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and 💷 are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

#### PUBLICATION ORDERING INFORMATION

#### LITERATURE FULFILLMENT

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA Phone: 480-829-7710 or 800-344-3860 Toll Free USA/Canada Fax: 480-829-7709 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.