

Absolute Maximum Ratings

BYP, EN, IN, OUT, SELA, SELB, $\overline{P\!O\!K}$ to GND, GS to GND, FB,
OUTS to GND-0.3V to +6.0V
Output Short-Circuit DurationContinuous
Continuous Power Dissipation ($T_A = +70^\circ\text{C}$)
TDFN (derate 11.9mW/ $^\circ\text{C}$ above $+70^\circ\text{C}$).....953.5mW

Operating Temperature Range-40 $^\circ\text{C}$ to +125 $^\circ\text{C}$
Junction Temperature Range-40 $^\circ\text{C}$ to +150 $^\circ\text{C}$
Storage Temperature Range-65 $^\circ\text{C}$ to +150 $^\circ\text{C}$
Lead Temperature (soldering, 10s)+300 $^\circ\text{C}$
Soldering Temperature (reflow)+260 $^\circ\text{C}$

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Electrical Characteristics

($V_{IN} = V_{EN} = 5\text{V}$, OUTS = OUT, circuit of Figure 2 (MAX8902A) and Figure 3 (MAX8902B), $T_A = -40^\circ\text{C}$ to $+125^\circ\text{C}$, unless otherwise noted.) (Note 1)

PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
IN						
Input Voltage Range			1.7		5.5	V
Input Undervoltage Lockout	VIN rising, 100mV typical hysteresis		1.5	1.6	1.7	V
OUT						
Output Voltage Range	VIN ≥ VOUT + 0.1V		0.6		5.3	V
Output Voltage Accuracy	VIN = 1.7V to 5.5V for VOUT ≤ 1.4V, VIN = (VOUT + 0.3V) to 5.5V for VOUT > 1.4V, IOUT = 0.1mA to 500mA		-1.5		+1.5	%
Load Regulation	IOUT = 0.1mA to 500mA			0.02		%
Line Regulation	VIN = 1.7V to 5.5V for VOUT ≤ 1.4V, VIN = (VOUT + 0.3V) to 5.5V for VOUT > 1.4V, IOUT = 200mA			0.04		%
Dropout Voltage (Note 2)	IOUT = 500mA	VIN ≥ 3.6V, TA ≤ +85°C		50	100	mV
		VIN ≥ 3.6V, TA ≤ +125°C			120	
		VIN = 1.7V		150		
Current Limit	VOUT = 95% of regulation, VIN = VOUT + 0.5V		600	700	800	mA
Output Noise	IOUT = 100mA, f = 10Hz to 100kHz, CBYP = 0.01μF			16		μVRMS
Power-Supply Rejection Ratio	IOUT = 10mA	f = 5kHz		92		dB
		f = 10kHz		85		
		f = 100kHz		62		
OUTS (MAX8902A only)						
OUTS Input Bias Current	In regulation		0.5		7.0	μA
FB (MAX8902B only)						
FB Threshold Accuracy	VIN = 1.7V to 5.5V, IOUT = 0.1mA to 500mA		0.591	0.600	0.609	V
FB Input Bias Current	VFB = 0.6V	TA = +25°C	-0.1	0.02	+0.1	μA
		TA = -40°C		0.03		
BYP						
BYP Capacitor Range	Regulator remains stable		1		100	nF
BYP Startup Current	From BYP to GND during startup			50		μA

Electrical Characteristics (continued)

($V_{IN} = V_{EN} = 5V$, $O_{UTS} = OUT$, circuit of Figure 2 (MAX8902A) and Figure 3 (MAX8902B), $T_A = -40^{\circ}C$ to $+125^{\circ}C$, unless otherwise noted.) (Note 1)

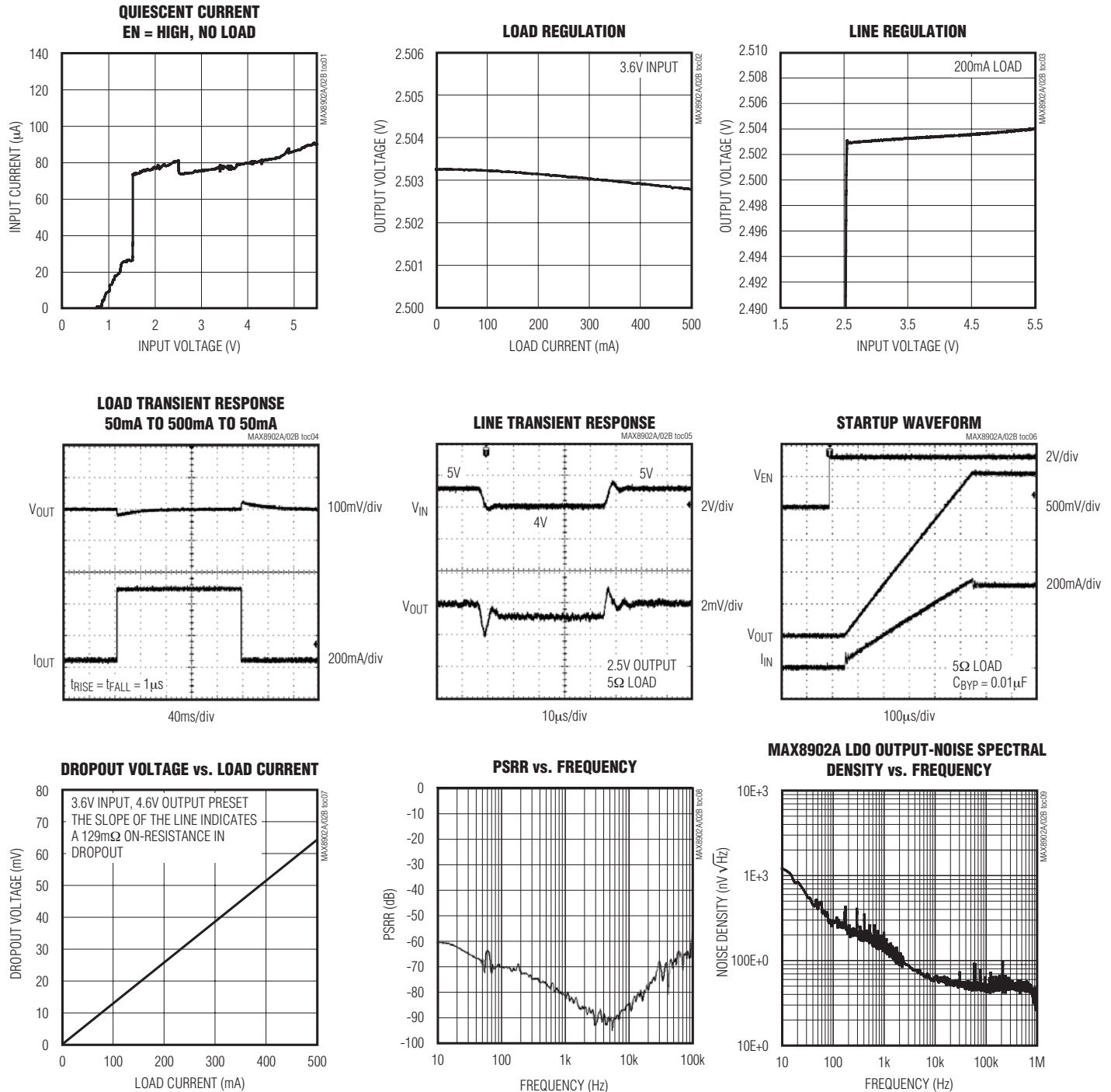
PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
GND						
GND Supply Current	I _{OUT} = 0mA	T _A < +85°C		80	120	μA
		T _A < +125°C			160	
GND Shutdown Current	V _{IN} = 5.5V, EN = 0V	T _A = +25°C		0.001	+1	μA
		T _A = +85°C		0.01		
SELA/SELB (MAX8902A only)						
Select Input Resistance	When shorted to GND or V _{IN}				500	Ω
	When open		1			MΩ
Select Input Capacitance	When open				10	pF
EN						
Enable Input Threshold	V _{IN} = 1.7V to 5.5V	EN rising		0.8	1.2	V
		EN falling, T _A < +85°C	0.4	0.7		
		EN falling, T _A < +125°C	0.38	0.7		
Enable Input Bias Current	V _{EN} = 0V to 5.5V	T _A = +25°C	-1	0.001	+1	μA
		T _A = +85°C		0.01		
POK (MAX8902B only)						
POK Threshold	OUT voltage when POK switches	OUT rising	88	91	94	%
		OUT falling		88		%
POK Voltage, Low	I _{POK} = 1mA			10	100	mV
POK Leakage Current	POK = 5.5V, V _{EN} = 0V	T _A = +25°C	-1	0.001	+1	μA
		T _A = +85°C		0.01		
THERMAL SHUTDOWN						
Thermal Shutdown Threshold	T _J rising			165		°C
	T _J falling			150		
OUTPUT TRANSIENT						
Load Transient	I _{OUT} = 50mA to 500mA to 50mA, t _{RISE} = t _{FALL} = 1μs			25		mV/P-P
Line Transient	V _{IN} = 4V to 5V to 4V, t _{RISE} = t _{FALL} = 5μs, I _{OUT} = 500mA			3		mV/P-P
IN-to-OUT Reverse Voltage Turnoff Threshold	IN falling below OUT			10		mV

Note 1: All devices are production tested at $T_A = +25^{\circ}C$. Specifications over the operating temperature range are guaranteed by design and characterization.

Note 2: The dropout voltage is defined $V_{IN} - V_{OUT}$, when V_{OUT} is 5% lower than the value of V_{OUT} when $V_{IN} = V_{OUT} + 0.5V$.

Typical Operating Characteristics

(MAX8902A, $V_{IN} = 3.6V$, $V_{OUT} = 2.5V$, $T_A = +25^\circ C$, unless otherwise noted.)



Pin Description

PIN	NAME		FUNCTION
	MAX8902A	MAX8902B	
1	IN	IN	Regulator Power Input. Connect IN to a supply from 1.7V to 5.5V. Bypass IN with a 10µF ceramic capacitor to GND.
2	GND	GND	Ground
3	EN	EN	Enable Input. A logic-low drives the output low through a 3k Ω resistor and reduces the supply current to less than 1µA. Drive logic-high or connect to IN for normal operation.
4	SELA	—	Output Voltage Select Input. Connect SELA to GND, IN, or leave unconnected. The states of SELA and SELB are sampled when the regulator turns on and the output voltage is set as shown in Table 2.
	—	GS	Internally Used. Connect GS to GND.
5	SELB	—	Output Voltage Select Input. Connect SELB to GND, IN, or leave unconnected. The states of SELA and SELB are sampled when the regulator turns on and the output voltage is set as shown in Table 2.
	—	$\overline{\text{POK}}$	Power-OK Output. Open-drain output that goes low when the output is above 91% of the nominal regulation voltage. $\overline{\text{POK}}$ is high impedance in shutdown or when the output is below the regulation voltage.
6	OUTS	—	Output Sense Input. Connect OUTS to the load at a point where accurate regulation is required, or connect OUTS directly to OUT.
	—	FB	Feedback Input. Connect FB to the center of a resistor voltage-divider connected between OUT and GND to set the output voltage. V_{FB} regulates to 0.6V.
7	BYP	BYP	Bypass Input. Connect a 0.01µF ceramic capacitor from BYP to OUT to achieve 16µVRMS output noise. Adjust the value of this capacitor to control the output slew rate during startup. Slew Rate = (5V / ms) x (0.01µF / C _{BYP})
8	OUT	OUT	Regulator Output. Sources up to 500mA at the output regulation voltage. Bypass with a 10µF (< 0.03 ESR) capacitor to GND.
—	EP	EP	Exposed Paddle. Connect the exposed paddle to a ground plane to provide heat sinking.

Detailed Description

The MAX8902A/MAX8902B low-noise, low-dropout linear regulators deliver up to 500mA of output current with only 16µVRMS of output noise in a 100kHz bandwidth. These regulators maintain their output voltage over a wide input range, requiring only 100mV of input-to-output headroom at full load.

The MAX8902 maintains a low 80µA typical supply current, independent of the load current and dropout voltage. The regulator control circuitry includes a programmable soft-start circuit and short circuit, reverse current, and thermal-overload protection. Other features include an enable input and a power-OK

($\overline{\text{POK}}$) output (MAX8902B only). A simplified functional diagram is shown in Figure 1.

The MAX8902A output voltage can be set to 1.5V, 1.8V, 2.0V, 2.5V, 3.0V, 3.1V, 3.3V, 4.6V, or 4.7V using the SELA and SELB inputs. The MAX8902B output voltage can be set between 0.6V and 5.3V with an external resistor voltage-divider.

Enable (EN)

The MAX8902A/MAX8902B include an enable input, EN. Pull EN low to shut down the output, or drive EN high to enable the output. If shutdown is not needed, connect EN to IN.

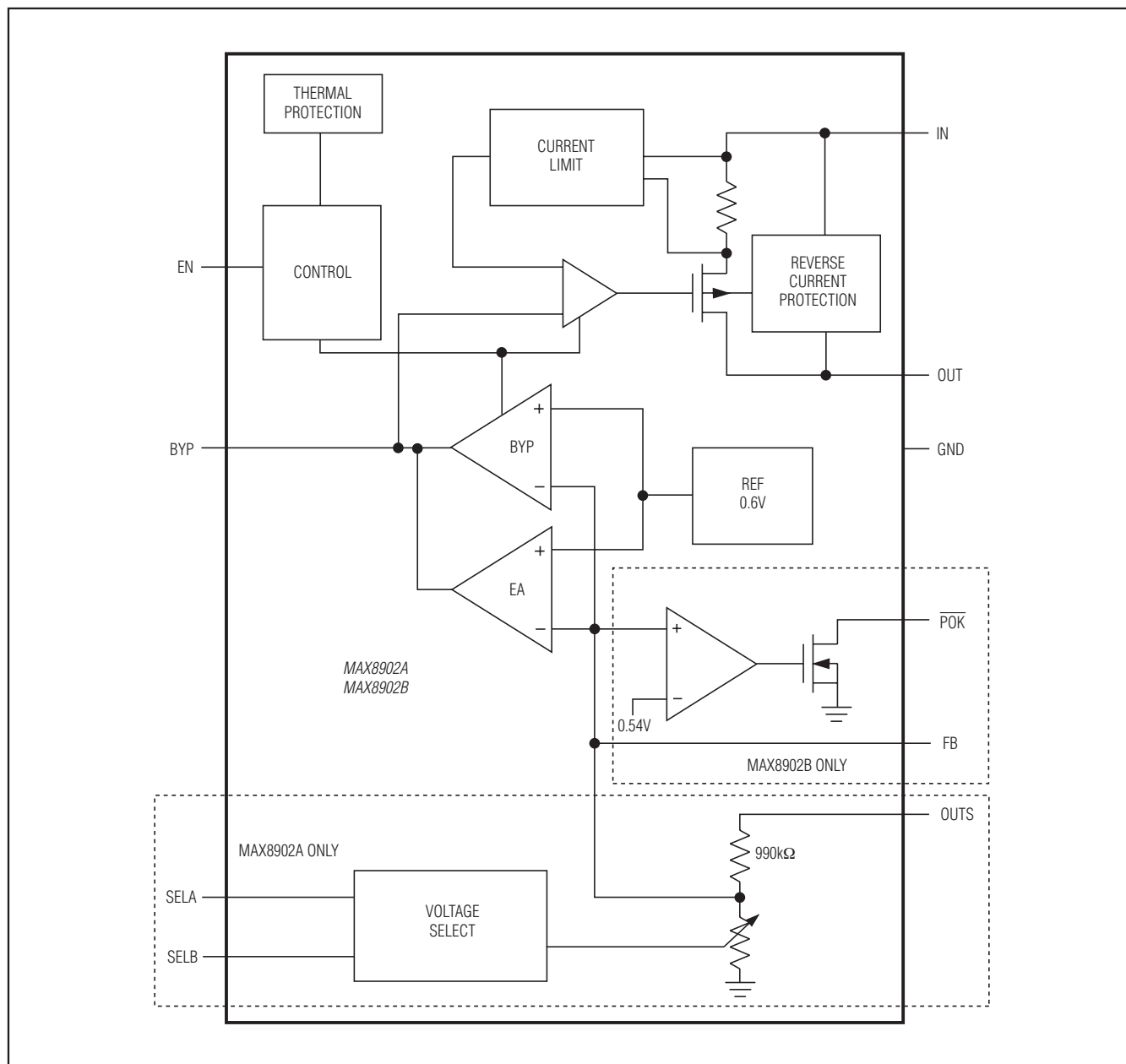


Figure 1. Simplified Functional Diagram

Bypass (BYP)

The capacitor connected from BYP to OUT filters the noise of the reference, feedback resistors, and regulator input stage and provides a high-speed feedback path for improved transient response. A 0.01 μ F capacitor rolls off input noise at approximately 32Hz.

The slew rate of the output voltage during startup is also determined by the BYP capacitor. A 0.01 μ F capacitor sets the slew rate to 5V / ms. This startup rate results in a 50mA slew current drawn from the input at startup to charge the 10 μ F output capacitance.

MAX8902A/MAX8902B

The BYP capacitor value can be adjusted from 0.001μF to 0.1μF to change the startup slew rate according to the following formula:

$$\text{Startup slew rate} = (5V / \text{ms}) \times (0.01\mu F / C_{\text{BYP}})$$

Note that this slew rate applies only at startup, and that recovery from a short circuit occurs at a slew rate approximately 500 times slower.

Also note that, being a low-frequency filter node, BYP is sensitive to leakage. BYP leakage currents above 10nA cause measurable inaccuracy at the output and should be avoided.

Protection Features

The MAX8902A/MAX8902B are fully protected from an output short circuit by a current-limiting and thermal-overload circuit. If the output is shorted to GND, the output current is limited to 700mA (typ). Under these conditions, the part quickly heats up. When the junction temperature reaches +165°C, a thermal-limit circuit shuts off the output device. When the junction cools to +150°C, the output turns back on in an attempt to reestablish regulation. While the fault persists, the output current cycles on and off, as the junction temperature slews between +150°C and +165°C.

The MAX8902A/MAX8902B are also protected against reverse current when the output voltage is higher than the input. In the event that extra output capacitance is used at the output, a power-down transient at the input would normally cause a large reverse current through a conventional regulator. The MAX8902A/MAX8902B include a reverse voltage detector that trips when IN drops 10mV below OUT, shutting off the regulator and opening the pMOS body diode connection, preventing any reverse current.

Thermal Considerations

The MAX8902A/MAX8902B are packaged in an 8-pin, 2mm x 2mm TDFN package with an exposed paddle. The exposed paddle is the main path for heat to leave the IC, and therefore, must be connected to a ground plane with thermal vias to allow heat to dissipate from the device. Thermal properties of the IC package are given in Table 1.

Selecting the Output Voltage (MAX8902A)

The MAX8902A output can be set to one of nine voltages by shorting or opening the SELA and SELB inputs, as shown in Table 2. SELA and SELB should be connected to GND, IN, or left unconnected. Alternatively, they may be driven high, low, or open with external logic; however, the states of SELA and SELB

Low-Noise 500mA LDO Regulators in a 2mm x 2mm TDFN Package

**Table 1. 2mm x 2mm TDFN Package
Thermal Characteristics**

CONTINUOUS POWER DISSIPATION	953.5mW DERATE 11.9mW/°C ABOVE +70°C
θ_{JA}^*	83.9°C/W
θ_{JC}	10.8°C/W

* θ_{JA} is specified according to the JE51 standard with the part mounted on a multilayer PCB.

Table 2. MAX8902A Output Voltages

OUTPUT VOLTAGE (V)	SELA STATE	SELB STATE
1.5	IN	Unconnected
1.8	Unconnected	GND
2.0	Unconnected	IN
2.5	Unconnected	Unconnected
3.0	GND	GND
3.1	GND	IN
3.3	GND	Unconnected
4.6	IN	GND
4.7	IN	IN

are sampled only at startup. The regulation voltage can be set to a different level by cycling EN or IN momentarily to GND.

Setting the Output Voltage (MAX8902B)

The MAX8902B uses external feedback resistors to set the output regulation voltage as shown in Figure 3. The output can be set from 0.6V to 5.3V. Set the lower feedback resistor (R2) to 120kΩ or less to minimize FB input bias current error. Then calculate the value of the upper feedback resistor (R1) as follows:

$$R1 = R2 \times \left(\frac{V_{\text{OUT}}}{V_{\text{FB}}} - 1 \right)$$

where V_{FB} is the feedback regulation voltage of 0.6V.

Power-OK (MAX8902B)

The MAX8902B includes an additional open-drain output, POK, that pulls low to indicate the output voltage is in regulation. During startup, POK is high impedance until the output voltage rises to 91% of its regulation level. If an overload occurs at the output, or the output is shut down, POK is high impedance.

MAX8902A/MAX8902B

Low-Noise 500mA LDO Regulators in a 2mm x 2mm TDFN Package

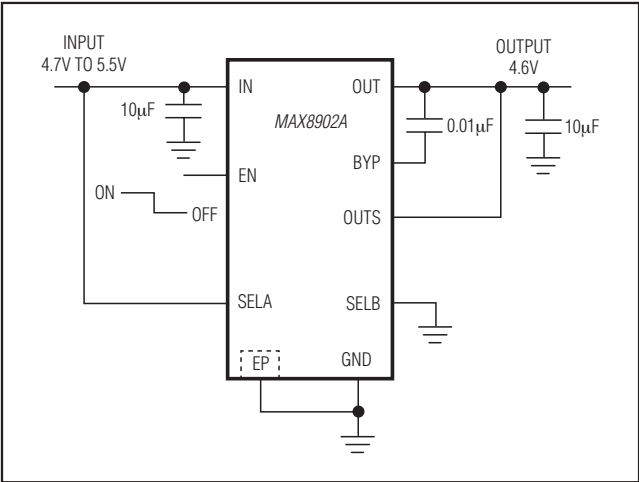


Figure 2. MAX8902A Fixed-Output Application Circuit

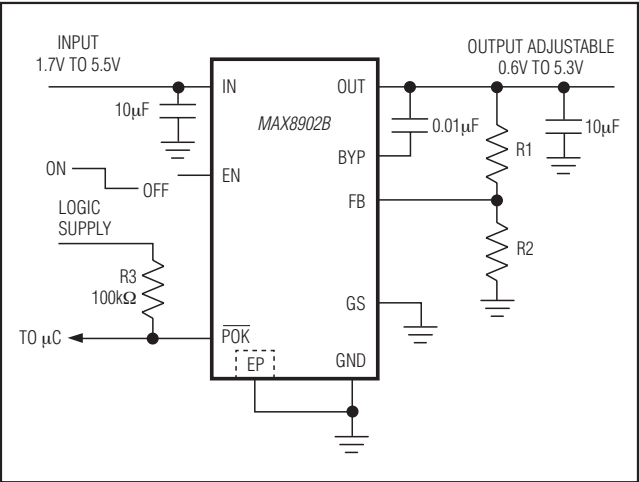


Figure 3. MAX8902B Adjustable-Output Application Circuit

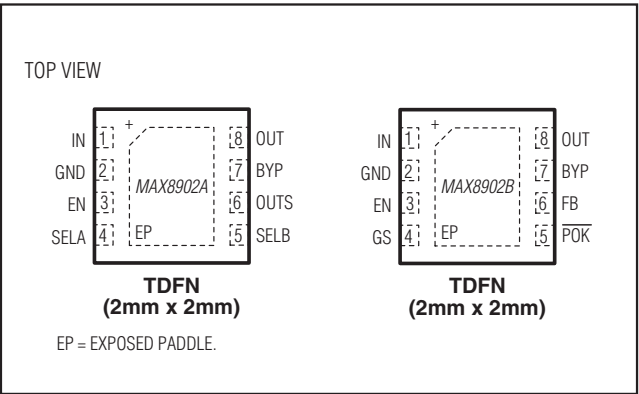
Input Capacitor

A 10µF ceramic capacitor is recommended for the input. Select a capacitor that maintains its capacitance over temperature and DC bias. Capacitors with X5R or X7R temperature characteristics generally perform well.

Output Capacitor

A minimum of 10µF of capacitance is required at OUT to ensure stability. Select a ceramic capacitor that maintains its capacitance over temperature and DC bias. Capacitors with X5R or X7R temperature characteristics generally perform well.

Pin Configurations



Package Information

For the latest package outline information and land patterns (footprints), go to www.maximintegrated.com/packages. Note that a “+”, “#”, or “-” in the package code indicates RoHS status only. Package drawings may show a different suffix character, but the drawing pertains to the package regardless of RoHS status.

PACKAGE TYPE	PACKAGE CODE	OUTLINE NO.	LAND PATTERN NO.
8 TDFN-EP	T822+1	21-0168	90-0064

Chip Information

PROCESS: BiCMOS

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	9/07	Initial release	—
1	1/08	Changed input capacitor value from 4.7 μ F to 10 μ F	1, 5, 8
2	2/08	Changed BP to BYP	2, 4–8
3	5/08	Updated TOC 5 title	4
4	4/11	Added the MAX8902BATA/V+ part to the <i>Ordering Information</i> table	1
5	10/12	Updated θ_{JC} specification in Table 1	7
6	1/15	Added MAX8902AATA/V+ to <i>Ordering Information</i>	1

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