Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

ABSOLUTE MAXIMUM RATINGS

IN, OUT, $\overline{SHDN}$, ADJ to GND .....-0.3V to +6V
Output Short Circuit to GND or IN ..... Continuous
Continuous Power Dissipation (T_A = +70°C)
5-Pin SOT23 (derate 7.1mW/°C above +70°C) 571mW

Operating Temperature Range .....-40°C to +125°C
Junction Temperature .....+150°C
Storage Temperature Range .....-65°C to +150°C
Lead Temperature (soldering, 10s) .....+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—MAX6037_12 (V_{OUT} = 1.25V)

(V_{IN} = $V_{\overline{SHDN}}$ = +3V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V_{OUT}	T_A = +25°C	MAX6037A_12 (0.2%)	1.2475	1.250	1.2525	V
			MAX6037B_12 (0.3%)	1.2462	1.250	1.2538	
			MAX6037C_12 (0.5%)	1.2438	1.250	1.2563	
Output-Voltage Temperature Coefficient (Note 2, 4)	TCV_{OUT}	MAX6037A_12			6	25	ppm/°C
		MAX6037B/C_12			6	50	
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	2.5V ≤ V_{IN} ≤ 5.5V			0.0006	0.0096	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.008	0.072	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.006	0.072	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.025	0.12	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.014	0.12	
OUT Short-Circuit Current	I_{SC}	Short to GND			16		mA
		Short to IN			32		
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				485		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm
DYNAMIC							
Noise Voltage	e_{OUT}	f = 0.1Hz to 10Hz			6		μV_{P-P}
		f = 10Hz to 1kHz			15		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		360		µs
			V_{IN} = 3V, $\overline{SHDN}$ pulled from low to high		75		
Output Impedance when Disabled	Z_{OUT}	V_{IN} = 3V, $V_{\overline{SHDN}}$ = 0V			125		kΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}			0.02		1	µF
INPUT							
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test		2.5		5.5	V
Quiescent Supply Current	I_{IN}				190	275	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V			0.05	500	nA

ELECTRICAL CHARACTERISTICS—MAX6037_12 (V_{OUT} = 1.25V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +3V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
SHUTDOWN ($\overline{SHDN}$)						
Logic-High Input Voltage	V_{ENH}	2.5V ≤ V_{IN} ≤ 5.5V	2.0			V
Logic-Low Input Voltage	V_{ENL}	2.5V ≤ V_{IN} ≤ 5.5V			0.7	V
Logic-High Input Current	I_{ENH}	2.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}	-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	2.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V	-1000	0.05	+1000	nA

ELECTRICAL CHARACTERISTICS—MAX6037_21 (V_{OUT} = 2.048V)

(V_{IN} = $V_{\overline{SHDN}}$ = +3V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V_{OUT}	T_A = +25°C	MAX6037A_21 (0.2%)	2.0439	2.0480	2.0521	V
			MAX6037B_21 (0.3%)	2.0418	2.0480	2.0542	
			MAX6037C_21 (0.5%)	2.0378	2.0480	2.0582	
Output-Voltage Temperature Coefficient (Note 2, 4)	TCV_{OUT}	MAX6037A_21			6	25	ppm/°C
		MAX6037B/C_21			6	50	
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	2.5V ≤ V_{IN} ≤ 5.5V			0.0008	0.0107	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.006	0.044	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.004	0.044	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.02	0.195	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.01	0.195	
OUT Short-Circuit Current	I_{SC}	Short to GND			16		mA
		Short to IN			32		
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				458		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm
DYNAMIC							
Noise Voltage	e_{OUT}	f = 0.1Hz to 10Hz			11		μV_{P-P}
		f = 10Hz to 1kHz			25		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		2.1		ms
			V_{IN} = 3V, $\overline{SHDN}$ pulled from low to high		2		
Output Impedance when Disabled	Z_{OUT}	V_{IN} = 3V, $V_{\overline{SHDN}}$ = 0V			205		kΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}			0.02		1	µF
INPUT							
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test		2.5		5.5	V
Quiescent Supply Current	I_{IN}				190	275	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V			0.05	500	nA

Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

ELECTRICAL CHARACTERISTICS—MAX6037_21 (V_{OUT} = 2.048V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +3V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
SHUTDOWN ($\overline{SHDN}$)						
Logic-High Input Voltage	V_{ENH}	2.5V ≤ V_{IN} ≤ 5.5V	2.0			V
Logic-Low Input Voltage	V_{ENL}	2.5V ≤ V_{IN} ≤ 5.5V			0.7	V
Logic-High Input Current	I_{ENH}	2.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}	-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	2.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V	-1000	0.05	+1000	nA

ELECTRICAL CHARACTERISTICS—MAX6037_25 (V_{OUT} = 2.500V)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V_{OUT}	T_A = +25°C	MAX6037A_25 (0.2%)	2.4950	2.500	2.5050	V
			MAX6037B_25 (0.3%)	2.4925	2.500	2.5075	
			MAX6037C_25 (0.5%)	2.4875	2.500	2.5125	
Output-Voltage Temperature Coefficient (Note 2, 4)	TCV_{OUT}	MAX6037A_25			6	25	ppm/°C
		MAX6037B/C_25			6	50	
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	(V_{OUT} + 0.2V) ≤ V_{IN} ≤ 5.5V			0.0004	0.012	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.005	0.036	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.003	0.036	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.02	0.2	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.01	0.2	
OUT Short-Circuit Current	I_{SC}	Short to GND			33		mA
		Short to IN			32		
Dropout Voltage (Note 5)	V_{IN} - V_{OUT}	I_{SOURCE} = 1mA			40	100	mV
		I_{SOURCE} = 5mA			190	410	
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				514		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm
DYNAMIC							
Noise Voltage	e_{OUT}	f = 0.1Hz to 10Hz			14		μV_{P-P}
		f = 10Hz to 1kHz			30		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		2.2		ms
			V_{IN} = 5V, $\overline{SHDN}$ pulled from low to high		2		
Output Impedance when Disabled	Z_{OUT}	V_{IN} = 5V, $V_{\overline{SHDN}}$ = 0V			250		kΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}			0.02		1	µF

ELECTRICAL CHARACTERISTICS—MAX6037_25 (V_{OUT} = 2.500V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
INPUT						
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test	2.7		5.5	V
Quiescent Supply Current	I_{IN}			210	275	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V		0.05	500	nA
SHUTDOWN ($\overline{SHDN}$)						
Logic-High Input Voltage	V_{ENH}	2.7V ≤ V_{IN} ≤ 5.5V	2.0			V
Logic-Low Input Voltage	V_{ENL}	2.7V ≤ V_{IN} ≤ 5.5V			0.75	V
Logic-High Input Current	I_{ENH}	2.7V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}	-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	2.7V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V	-1000	0.05	+1000	nA

ELECTRICAL CHARACTERISTICS—MAX6037_30 (V_{OUT} = 3.000V)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V_{OUT}	T_A = +25°C	MAX6037A_30 (0.2%)	2.9940	3.000	3.0060	V
			MAX6037B_30 (0.3%)	2.9910	3.000	3.0090	
			MAX6037C_30 (0.5%)	2.9850	3.000	3.0150	
Output-Voltage Temperature Coefficient (Note 2, 4)	TCV_{OUT}	MAX6037A_30			6	25	ppm/°C
		MAX6037B/C_30			6	50	
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	(V_{OUT} + 0.2V) ≤ V_{IN} ≤ 5.5V			0.0004	0.0133	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.005	0.035	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.008	0.03	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.02	0.2	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.01	0.2	
OUT Short-Circuit Current	I_{SC}	Short to GND			33		mA
		Short to IN			32		
Dropout Voltage (Note 5)	V_{IN} - V_{OUT}	I_{SOURCE} = 1mA			40	100	mV
		I_{SOURCE} = 5mA			190	410	
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				501		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm
DYNAMIC							
Noise Voltage	e_{OUT}	f = 0.1Hz to 10Hz			17		μV_{P-P}
		f = 10Hz to 1kHz			40		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		2.4		ms
			V_{IN} = 5V, $\overline{SHDN}$ pulled from low to high		2.1		

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ELECTRICAL CHARACTERISTICS—MAX6037_30 (V_{OUT} = 3.000V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Output Impedance when Disabled	Z_{OUT}	V_{IN} = 5V, $V_{\overline{SHDN}}$ = 0V		300		kΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}		0.02		1	µF
INPUT						
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test	3.2		5.5	V
Quiescent Supply Current	I_{IN}			210	275	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V		0.05	500	nA
SHUTDOWN ($\overline{SHDN}$)						
Logic-High Input Voltage	V_{ENH}	3.2V ≤ V_{IN} ≤ 5.5V	2.0			V
Logic-Low Input Voltage	V_{ENL}	3.2V ≤ V_{IN} ≤ 5.5V			0.8	V
Logic-High Input Current	I_{ENH}	3.2V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}	-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	3.2V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V	-1000	0.05	+1000	nA

ELECTRICAL CHARACTERISTICS—MAX6037_33 (V_{OUT} = 3.300V)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V_{OUT}	T_A = +25°C	MAX6037A_33 (0.2%)	3.2934	3.300	3.3066	V
			MAX6037B_33 (0.3%)	3.2901	3.300	3.3099	
			MAX6037C_33 (0.5%)	3.2855	3.300	3.3165	
Output-Voltage Temperature Coefficient (Note 2, 4)	TCV_{OUT}	MAX6037A_33			6	25	ppm/°C
		MAX6037B/C_33			6	50	
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	(V_{OUT} + 0.2V) ≤ V_{IN} ≤ 5.5V			0.0003	0.0133	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.005	0.027	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.002	0.027	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.02	0.212	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.01	0.212	
OUT Short-Circuit Current	I_{SC}	Short to GND			33		mA
		Short to IN			32		
Dropout Voltage (Note 5)	V_{IN} - V_{OUT}	I_{SOURCE} = 1mA			40	100	mV
		I_{SOURCE} = 5mA			190	410	
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				514		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm

ELECTRICAL CHARACTERISTICS—MAX6037_33 (V_{OUT} = 3.300V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
DYNAMIC							
Noise Voltage	e_{OUT}	f = 0.1Hz to 10Hz			19		μV_{P-P}
		f = 10Hz to 1kHz			45		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		2.6		ms
			V_{IN} = 5V, $\overline{SHDN}$ pulled from low to high		2.4		
Output Impedance when Disabled	Z_{OUT}	V_{IN} = 5V, $V_{\overline{SHDN}}$ = 0V			330		kΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}			0.02		1	µF
INPUT							
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test		3.5		5.5	V
Quiescent Supply Current	I_{IN}				210	275	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V			0.05	500	nA
SHUTDOWN ($\overline{SHDN}$)							
Logic-High Input Voltage	V_{ENH}	3.5V ≤ V_{IN} ≤ 5.5V		2.0			V
Logic-Low Input Voltage	V_{ENL}	3.5V ≤ V_{IN} ≤ 5.5V				0.8	V
Logic-High Input Current	I_{ENH}	3.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}		-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	3.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V		-1000	0.05	+1000	nA

ELECTRICAL CHARACTERISTICS—MAX6037_41 (V_{OUT} = 4.096V)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Output Voltage	V_{OUT}	T_A = +25°C	MAX6037A_41 (0.2%)	4.0878	4.096	4.1042	V
			MAX6037B_41 (0.3%)	4.0837	4.096	4.1083	
			MAX6037C_41 (0.5%)	4.0755	4.096	4.1165	
Output-Voltage Temperature Coefficient (Note 2, 4)	TCV_{OUT}	MAX6037A_41			6	25	ppm/°C
		MAX6037B/C_41			6	50	
Line Regulation	$\Delta V_{OUT}/\Delta V_{IN}$	(V_{OUT} + 0.2V) ≤ V_{IN} ≤ 5.5V			0.0003	0.0105	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.004	0.35	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.002	0.027	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.02	0.212	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.01	0.212	

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ELECTRICAL CHARACTERISTICS—MAX6037_41 (V_{OUT} = 4.096V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, C_{OUT} = 0.1µF, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUT Short-Circuit Current	I_{SC}	Short to GND			33		mA
		Short to IN			32		
Dropout Voltage (Note 5)	V_{IN} - V_{OUT}	I_{SOURCE} = 1mA			40	100	mV
		I_{SOURCE} = 5mA			190	410	
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				524		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm
DYNAMIC							
Noise Voltage	e_{OUT}	f = 0.1Hz to 10Hz			24		μV_{P-P}
		f = 10Hz to 1kHz			50		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		3.2		ms
			V_{IN} = 5V, $\overline{SHDN}$ pulled from low to high		3.2		
Output Impedance when Disabled	Z_{OUT}	V_{IN} = 5V, $V_{\overline{SHDN}}$ = 0			410		kΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}			0.02		1	µF
INPUT							
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test		4.3		5.5	V
Quiescent Supply Current	I_{IN}				210	275	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V			0.05	500	nA
SHUTDOWN ($\overline{SHDN}$)							
Logic-High Input Voltage	V_{ENH}	4.3V ≤ V_{IN} ≤ 5.5V		2.0			V
Logic-Low Input Voltage	V_{ENL}	4.3V ≤ V_{IN} ≤ 5.5V				0.8	V
Logic-High Input Current	I_{ENH}	4.3V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}		-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	4.3V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V		-1000	0.05	+1000	nA

ELECTRICAL CHARACTERISTICS—MAX6037_ADJ (V_{OUT} = 1.184V to 5V)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, T_A = T_{MIN} to T_{MAX}, ADJ shorted to OUT unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
OUTPUT							
Initial Output Voltage (Note 6)	V_{OUT}	T_A = +25°C	MAX6037A_ADJ (0.2%)	1.1816	1.1840	1.1864	V
			MAX6037B_ADJ (0.3%)	1.1805	1.1840	1.1875	
			MAX6037C_ADJ (0.5%)	1.1781	1.1840	1.1899	
Output Voltage Range	V_{OUT}			1.184		V_{IN} - 0.15	V
Output-Voltage Temperature Coefficient (Notes 2, 4, 7)	TCV_{OUT}	MAX6037A_ADJ			6	25	ppm/°C
		MAX6037B/C_ADJ			6	50	
Line Regulation (Note 8)	$\Delta V_{OUT}/\Delta V_{IN}$	2.5V ≤ V_{IN} ≤ 5.5V			0.0008	0.013	%/V
Load Regulation	$\Delta V_{OUT}/\Delta I_{OUT}$	Sourcing: 0 ≤ I_{OUT} ≤ 1mA			0.012	0.078	%/mA
		Sourcing: 1mA ≤ I_{OUT} ≤ 5mA			0.014	0.1	
		Sinking: -1mA ≤ I_{OUT} ≤ 0			0.005	0.12	
		Sinking: -5mA ≤ I_{OUT} ≤ -1mA			0.005	0.12	
OUT Short-Circuit Current	I_{SC}	Short to GND			33		mA
		Short to IN			32		
Dropout Voltage (Notes 5, 9)	V_{IN} - V_{OUT}	I_{SOURCE} = 1mA, V_{OUT} = 5V			40	100	mV
		I_{SOURCE} = 5mA, V_{OUT} = 5V			190	410	
Thermal Hysteresis (Note 3)	ΔV_{OUT}/cycle				421		ppm
Long-Term Stability	ΔV_{OUT}/time	1000h at T_A = +25°C			133		ppm
DYNAMIC							
Noise Voltage (Note 10)	e_{OUT}	f = 0.1Hz to 10Hz			6		μV_{P-P}
		f = 10Hz to 1kHz			15		μV_{RMS}
Turn-On Settling Time	t_R	To V_{OUT} = 0.1% of final value, C_{OUT} = 0.02µF	Initial power-up		360		µs
			V_{IN} = 5V, $\overline{SHDN}$ pulled from low to high		75		
Output Impedance when Disabled (Note 11)	Z_{OUT}	V_{IN} = 5V, $V_{\overline{SHDN}}$ = 0V			>10		MΩ
Capacitive-Load Stability Range (Note 4)	C_{OUT}			0.02		1	µF
INPUT							
Supply Voltage Range	V_{IN}	Guaranteed by line regulation test		2.5		5.5	V
Quiescent Supply Current	I_{IN}				200	250	µA
Shutdown Supply Current	$I_{\overline{SHDN}}$	$V_{\overline{SHDN}}$ = 0V			0.15	500	nA
ADJ Input Bias Current (Note 4)	I_{ADJ}			-50	0.5	+50	nA

Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

ELECTRICAL CHARACTERISTICS—MAX6037_ADJ (V_{OUT} = 1.184V to 5V) (continued)

(V_{IN} = $V_{\overline{SHDN}}$ = +5V, I_{OUT} = 0, T_A = T_{MIN} to T_{MAX}, ADJ shorted to OUT unless otherwise noted. Typical values are at T_A = +25°C.) (Note 1)

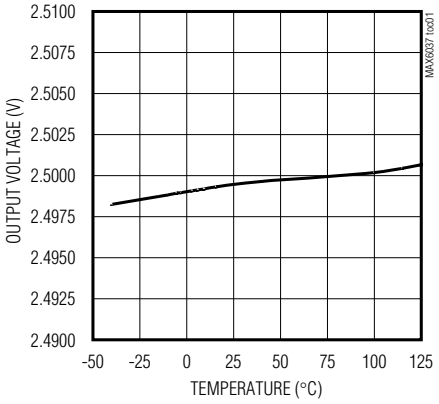
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
SHUTDOWN ($\overline{SHDN}$)						
Logic-High Input Voltage	V_{ENH}	2.5V ≤ V_{IN} ≤ 5.5V	2.0			V
Logic-Low Input Voltage	V_{ENL}	2.5V ≤ V_{IN} ≤ 5.5V			0.7	V
Logic-High Input Current	I_{ENH}	2.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = V_{IN}	-1000	0.15	+1000	nA
Logic-Low Input Current	I_{ENL}	2.5V ≤ V_{IN} ≤ 5.5V, $V_{\overline{SHDN}}$ = 0V	-1000	0.05	+1000	nA

Note 1: All devices are 100% tested at T_A = +25°C and are guaranteed by design for T_A = T_{MIN} to T_{MAX}, as specified.
Note 2: Temperature coefficient is measured by the "box" method, i.e., the maximum ΔV_{OUT} / V_{OUT} is divided by the maximum ΔT.
Note 3: Thermal hysteresis is defined as the change in +25°C output voltage before and after cycling the device from T_{MAX} to T_{MIN}.
Note 4: Not production tested. Guaranteed by design.
Note 5: Dropout voltage is defined as the minimum differential voltage (V_{IN} - V_{OUT}) at which V_{OUT} decreases by 1% from its original value at V_{IN} = +5.0V.
Note 6: V_{OUT} initial accuracy for the MAX6037_ADJ is tested with ADJ shorted to OUT. Actual accuracy will be affected by matching and the temperature coefficient of the external resistors used. Use 1% resistors with low temperature coefficient for best overall accuracy.
Note 7: The temperature coefficient for the MAX6037_ADJ is specified for the case where ADJ is connected to OUT. For the case where an external resistive network is used to set the output voltage, actual change in reference output over temperature will be affected by the temperature coefficient and matching of the external resistors used.
Note 8: The minimum V_{IN} is the greater of +2.5V and (V_{OUT} + 0.2V).
Note 9: V_{OUT} set to +5V with an external resistive-divider.
Note 10: Noise for the MAX6037_ADJ is specified for a +1.25V output. Noise is proportional to V_{OUT} and is greater for higher output voltages. In addition, external resistors used to set the output voltage can contribute to noise.

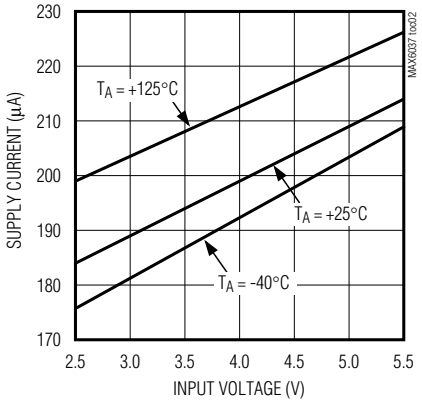
Typical Operating Characteristics

(V_{IN} = +3V for the MAX6037_12 and MAX6037_21; V_{IN} = +5V for the MAX6037_25, MAX6037_30, MAX6037_33, and MAX6037_41; I_{OUT} = 0, C_{OUT} = 0.1µF, C_{IN} = 0.1µF, T_A = +25°C, unless otherwise specified.)

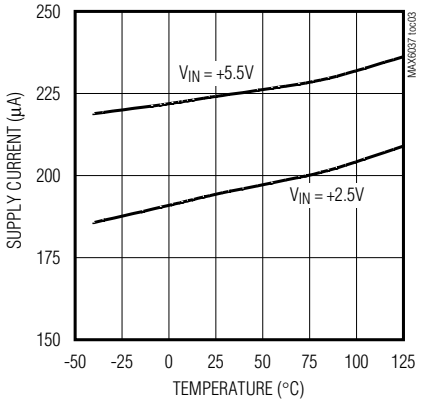
MAX6037_25 OUTPUT VOLTAGE vs. TEMPERATURE (V_{OUT} = 2.500V)



MAX6037_12 SUPPLY CURRENT vs. INPUT VOLTAGE (V_{OUT} = 1.25V)



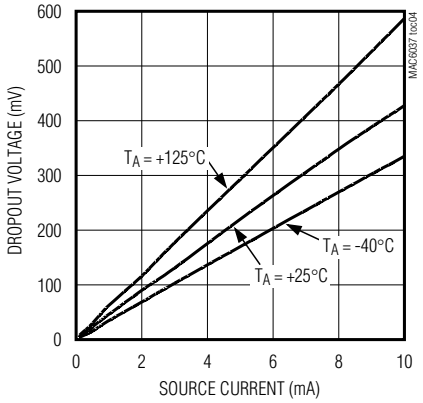
MAX6037_12 SUPPLY CURRENT vs. TEMPERATURE (V_{OUT} = 1.25V)



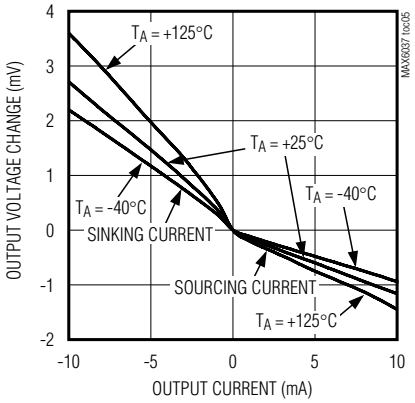
Typical Operating Characteristics (continued)

(V_{IN} = +3V for the MAX6037_12 and MAX6037_21; V_{IN} = +5V for the MAX6037_25, MAX6037_30, MAX6037_33, and MAX6037_41; I_{OUT} = 0, C_{OUT} = 0.1µF, C_{IN} = 0.1µF, T_A = +25°C, unless otherwise specified.)

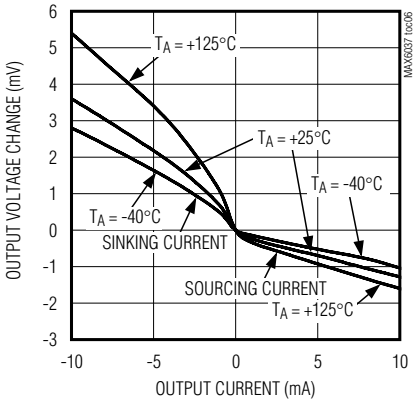
MAX6037_25 DROPOUT VOLTAGE vs. SOURCE CURRENT (V_{OUT} = 2.500V)



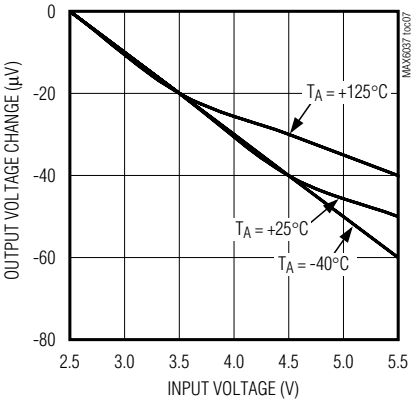
MAX6037_12 LOAD REGULATION (V_{OUT} = 1.25V)



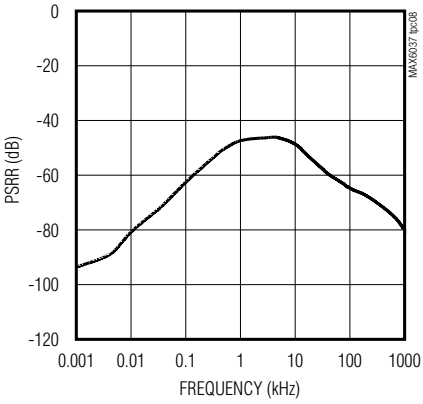
MAX6037_41 LOAD REGULATION (V_{OUT} = 4.096V)



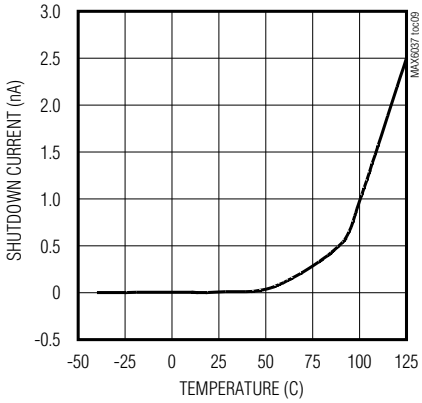
MAX6037_12 LINE REGULATION vs. TEMPERATURE (V_{OUT} = 1.25V)



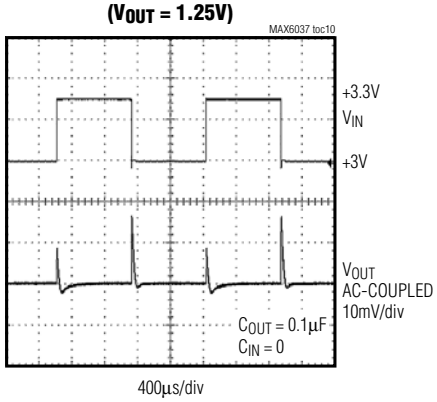
MAX6037_25 POWER-SUPPLY REJECTION RATIO vs. FREQUENCY (V_{OUT} = 2.500V)



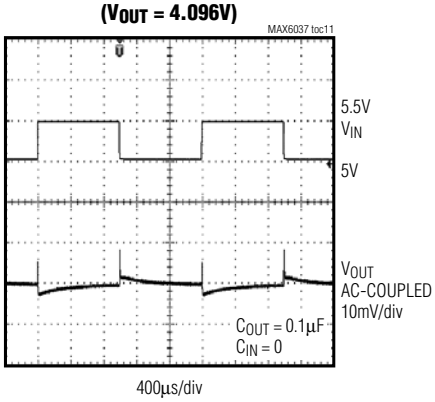
MAX6037_41 SHUTDOWN CURRENT vs. TEMPERATURE (V_{OUT} = 4.096V)



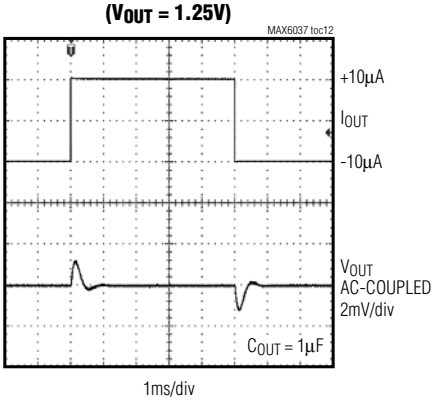
MAX6037_12 LINE TRANSIENT (V_{OUT} = 1.25V)



MAX6037_41 LINE TRANSIENT (V_{OUT} = 4.096V)



MAX6037_12 LOAD TRANSIENT (V_{OUT} = 1.25V)

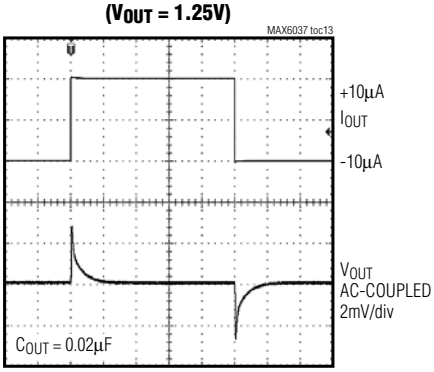


Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

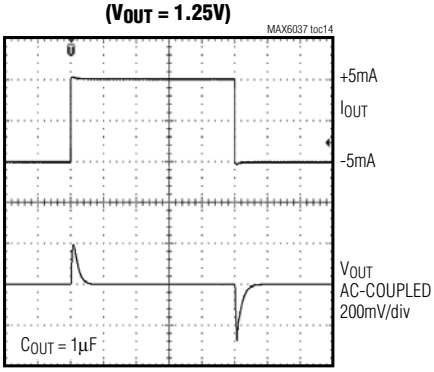
Typical Operating Characteristics (continued)

(V_{IN} = +3V for the MAX6037_12 and MAX6037_21; V_{IN} = +5V for the MAX6037_25, MAX6037_30, MAX6037_33, and MAX6037_41; I_{OUT} = 0, C_{OUT} = 0.1µF, C_{IN} = 0.1µF, T_A = +25°C, unless otherwise specified.)

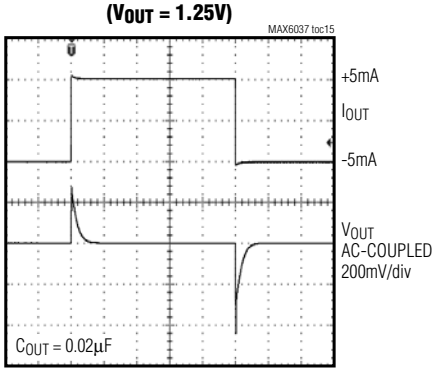
MAX6037_12 LOAD TRANSIENT (V_{OUT} = 1.25V)



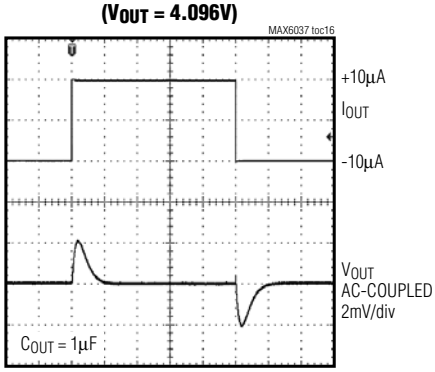
MAX6037_12 LOAD TRANSIENT (V_{OUT} = 1.25V)



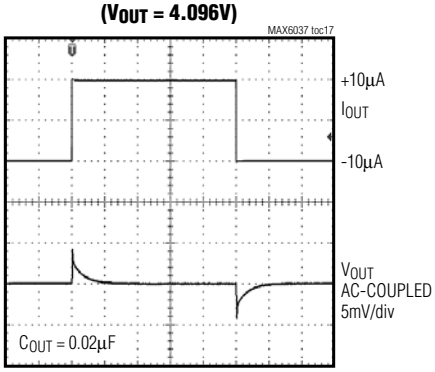
MAX6037_12 LOAD TRANSIENT (V_{OUT} = 1.25V)



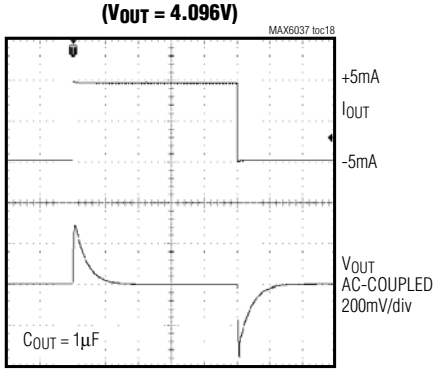
MAX6037_41 LOAD TRANSIENT (V_{OUT} = 4.096V)



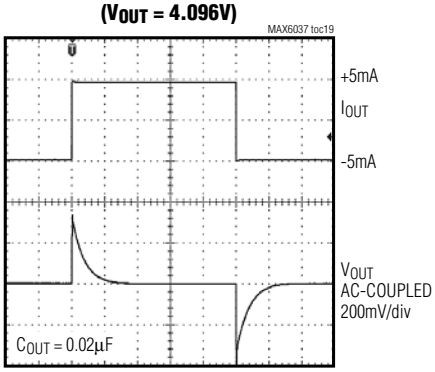
MAX6037_41 LOAD TRANSIENT (V_{OUT} = 4.096V)



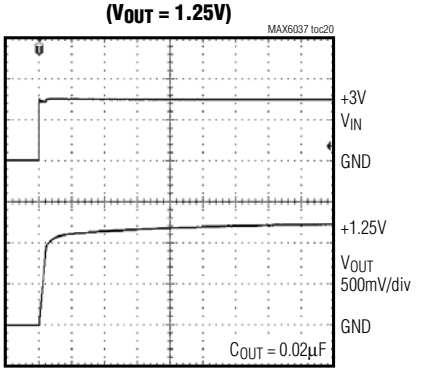
MAX6037_41 LOAD TRANSIENT (V_{OUT} = 4.096V)



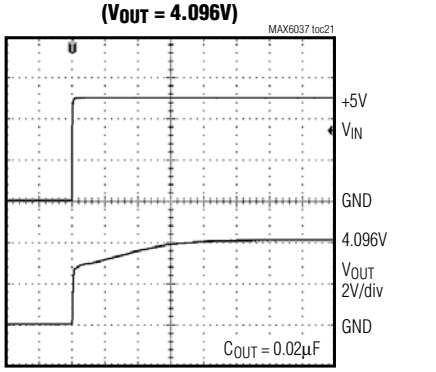
MAX6037_41 LOAD TRANSIENT (V_{OUT} = 4.096V)



MAX6037_12 TURN-ON TRANSIENT (V_{OUT} = 1.25V)



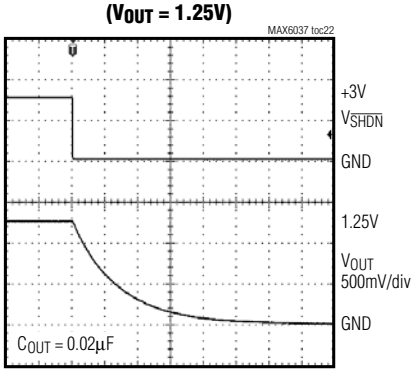
MAX6037_41 TURN-ON TRANSIENT (V_{OUT} = 4.096V)



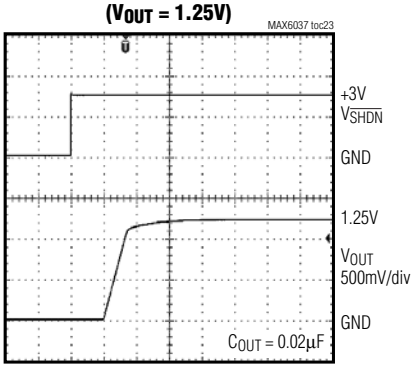
Typical Operating Characteristics (continued)

(V_{IN} = +3V for the MAX6037_12 and MAX6037_21; V_{IN} = +5V for the MAX6037_25, MAX6037_30, MAX6037_33, and MAX6037_41; I_{OUT} = 0, C_{OUT} = 0.1µF, C_{IN} = 0.1µF, T_A = +25°C, unless otherwise specified.)

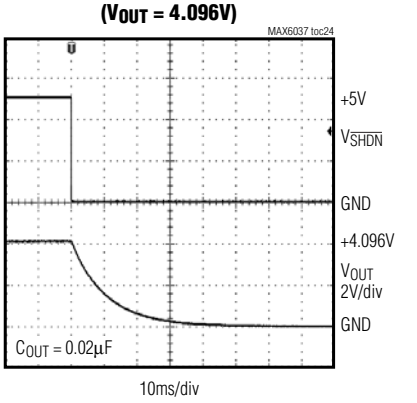
MAX6037_12 SHUTDOWN TRANSIENT (V_{OUT} = 1.25V)



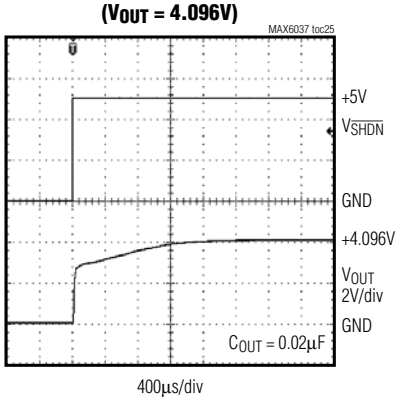
MAX6037_12 EXITING SHUTDOWN TRANSIENT (V_{OUT} = 1.25V)



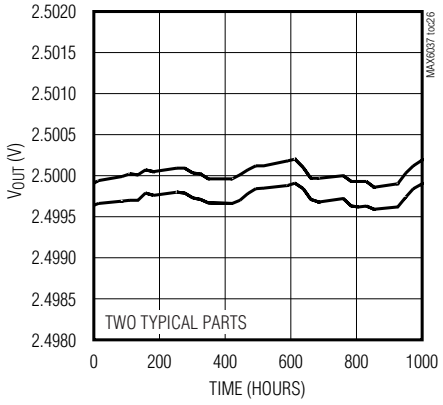
MAX6037_41 SHUTDOWN TRANSIENT (V_{OUT} = 4.096V)



MAX6037_41 EXITING SHUTDOWN TRANSIENT (V_{OUT} = 4.096V)



MAX6037_25 LONG-TERM STABILITY vs. TIME (V_{OUT} = 2.500V)

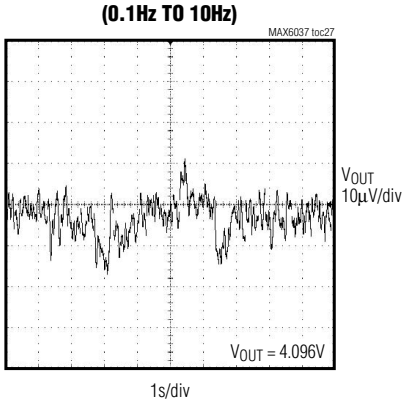


Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

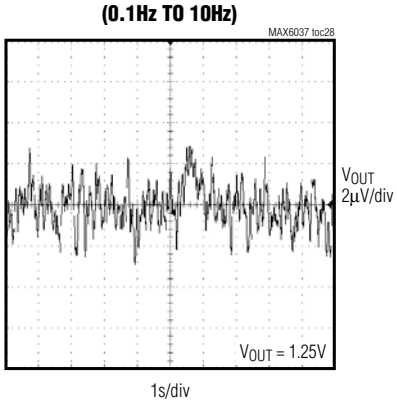
Typical Operating Characteristics (continued)

(V_{IN} = +3V for the MAX6037_12 and MAX6037_21; V_{IN} = +5V for the MAX6037_25, MAX6037_30, MAX6037_33, and MAX6037_41; I_{OUT} = 0, C_{OUT} = 0.1µF, C_{IN} = 0.1µF, T_A = +25°C, unless otherwise specified.)

MAX6037_41 OUTPUT NOISE (0.1Hz TO 10Hz)



MAX6037_12 OUTPUT NOISE (0.1Hz TO 10Hz)

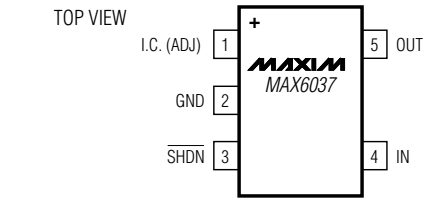


Note 12: Many of the MAX6037 family *Typical Operating Characteristics* are extremely similar. The extremes of these characteristics are found in the MAX6037_12 (1.25V output) and the MAX6037_41 (4.096V output). The *Typical Operating Characteristics* of the remainder of the MAX6037 family typically lie between those two extremes and can be estimated based on their output voltages.

Pin Description

PIN	NAME	FUNCTION
1	I.C.	Internally connected. (All fixed output voltage options.) Do not connect anything to this pin.
	ADJ	Output Voltage Adjustment Connection. Connect a resistor-divider between OUT, ADJ and GND to set the output voltage. (MAX6037_ADJ only).
2	GND	Ground
3	$\overline{SHDN}$	Active-Low Shutdown Input. Pull $\overline{SHDN}$ low to disable the device. Connect $\overline{SHDN}$ to IN for normal operation.
4	IN	Supply Voltage Input. Bypass with a 0.1µF to 1µF capacitor to GND.
5	OUT	Reference-Voltage Output. Connect an output capacitor to GND in the 0.02µF to 1µF range.

Pin Configuration



Detailed Description

The MAX6037 is a family of low-dropout, micropower voltage references. These devices all feature a shutdown mode by forcing $\overline{SHDN}$ low, dropping the quiescent current to less than 500nA. The MAX6037 can sink and source up to 5mA with less than 410mV of dropout voltage, making them attractive for use in low-voltage applications. The MAX6037 is available in six fixed output voltages of 1.25V, 2.048V, 2.5V, 3.0V, 3.3V and 4.096V, and an adjustable output version for voltages between the range of 1.184V and 5V.

Shutdown

The MAX6037 features an active-low shutdown mode. Pulling $\overline{SHDN}$ low disables the output and forces the quiescent current to less than 500nA (typically 50pA). Connect $\overline{SHDN}$ to IN for normal operation.

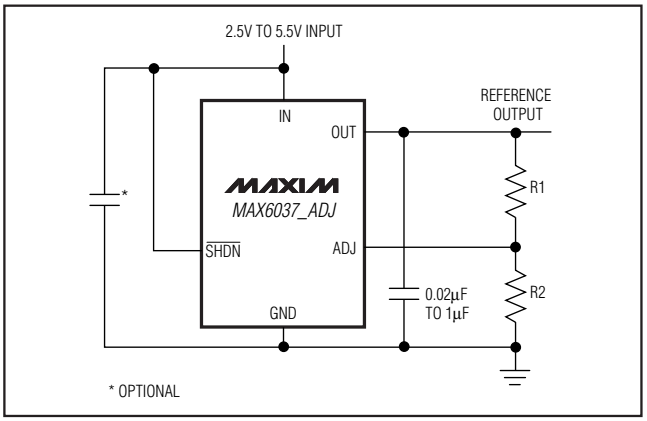


Figure 1. MAX6037_ADJ Typical Operating Circuit

MAX6037_ADJ Adjustable Output Voltage

Set the output voltage on the MAX6037_ADJ by placing a resistor-divider network between OUT, ADJ, and GND (See Figure 1). Use the following formula to calculate the output voltage:

$$V_{OUT} = \left(1 + \frac{R1}{R2}\right)V_{ADJ}$$

where V_{ADJ} = 1.184V. Set R2 = 1MΩ or less. Currents through Resistor R1 and R2 add to the quiescent supply current.

Supply Current

The quiescent supply current of the series-mode MAX6037 family is typically 190µA to 210µA. When the supply voltage is below the minimum-specified input voltage during turn-on, the device can draw up to 250µA beyond the nominal supply current. The input voltage source must be capable of providing this current to ensure reliable turn-on.

Thermal Hysteresis

Output voltage hysteresis is the change of output voltage at T_A = +25°C before and after the device is cycled over its entire operating temperature range. The typical thermal hysteresis value is 500ppm.

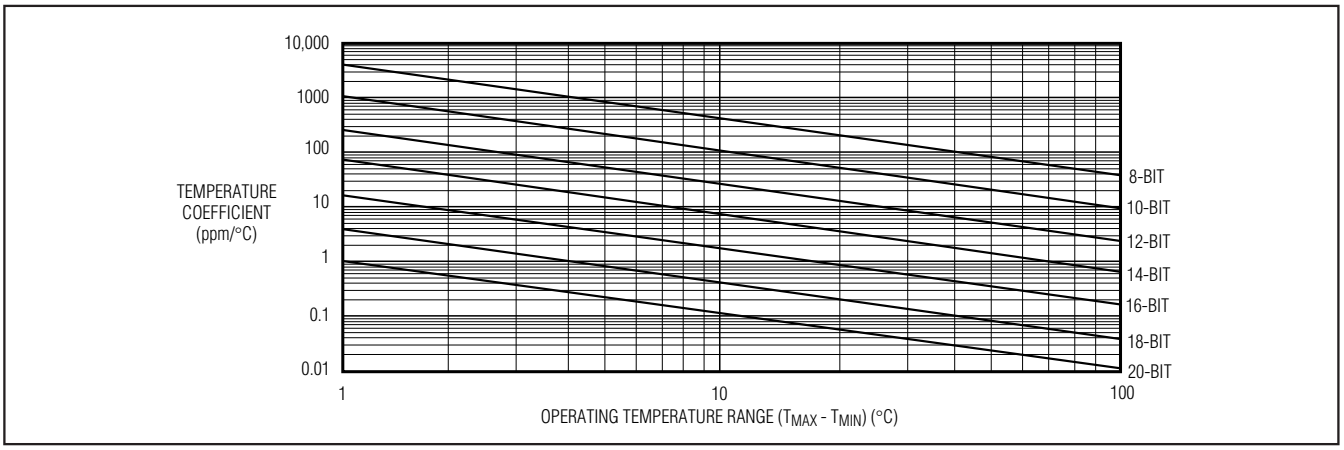


Figure 2. Temperature Coefficient vs. Operating Temperature Range for a 1 LSB Maximum Error

Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

Turn-On Time

These devices typically turn on and settle to within 0.1% of their final value in 360µs to 3.2ms, depending on the device. The turn-on time can increase up to 10ms with the device operating at the minimum dropout voltage and the maximum capacitive load.

Applications Information

Input Bypassing

For the best transient performance, decouple the input with a 0.1µF to 1µF ceramic capacitor as shown in the *Typical Operating Circuit*. Locate the capacitor as close to IN as possible. No capacitor is necessary if transient performance is less important.

Output/Load Capacitance

Devices in the MAX6037 family require an output capacitance in the range of 0.02µF to 1µF for frequency stability.

Temperature Coefficient vs. Operating Temperature Range for a 1 LSB Maximum Error

In a data converter application, the reference voltage of the converter must stay within a certain limit to keep the error in the data converter smaller than the resolution limit throughout the operating temperature range. Figure 2 shows the maximum allowable reference voltage temperature coefficient to keep the conversion error to less than 1 LSB, as a function of the operating temperature range (T_{MAX} - T_{MIN}) with the converter resolution as a parameter. The graph assumes the reference voltage temperature coefficient as the only parameter affecting accuracy.

In reality, the absolute static accuracy of a data converter is dependent on the combination of many parameters such as integral nonlinearity, differential nonlinearity, offset error, gain error, as well as voltage reference changes.

Ordering Information/Selector Guide (continued)

PART	OUTPUT VOLTAGE (V)	INITIAL ACCURACY %	MAX TEMPCO (ppm/°C)	PIN-PACKAGE	PKG CODE	TOP MARK
MAX6037AAUK25+T	2.5	0.2	25	5 SOT23-5	U5-2	AEJB
MAX6037BAUK25+T	2.5	0.3	50	5 SOT23-5	U5-2	AEJC
MAX6037CAUK25+T	2.5	0.5	50	5 SOT23-5	U5-2	AEJD
MAX6037AAUK30+T	3.0	0.2	25	5 SOT23-5	U5-2	AEJE
MAX6037BAUK30+T	3.0	0.3	50	5 SOT23-5	U5-2	AEJF
MAX6037CAUK30+T	3.0	0.5	50	5 SOT23-5	U5-2	AEJG
MAX6037AAUK33+T	3.3	0.2	25	5 SOT23-5	U5-2	AEJH
MAX6037BAUK33+T	3.3	0.3	50	5 SOT23-5	U5-2	AEJI
MAX6037CAUK33+T	3.3	0.5	50	5 SOT23-5	U5-2	AEJJ
MAX6037AAUK41+T	4.096	0.2	25	5 SOT23-5	U5-2	AEJK
MAX6037BAUK41+T	4.096	0.3	50	5 SOT23-5	U5-2	AEJL
MAX6037CAUK41+T	4.096	0.5	50	5 SOT23-5	U5-2	AEJM
MAX6037AAUKADJ+T**	1.184 to 5, adjustable	0.2	25	5 SOT23-5	U5-2	AEIS
MAX6037BAUKADJ+T**	1.184 to 5, adjustable	0.3	50	5 SOT23-5	U5-2	AEIT
MAX6037CAUKADJ+T**	1.184 to 5, adjustable	0.5	50	5 SOT23-5	U5-2	AEIU

***The accuracy of the MAX6037_ADJ is dependent on the accuracy of the external resistors. Use 1% resistors with low temperature coefficient for best overall accuracy.*

+Denotes a lead-free package.

T = Tape and reel.

Note: *All devices are specified over the -40°C to +125°C operating temperature range.*

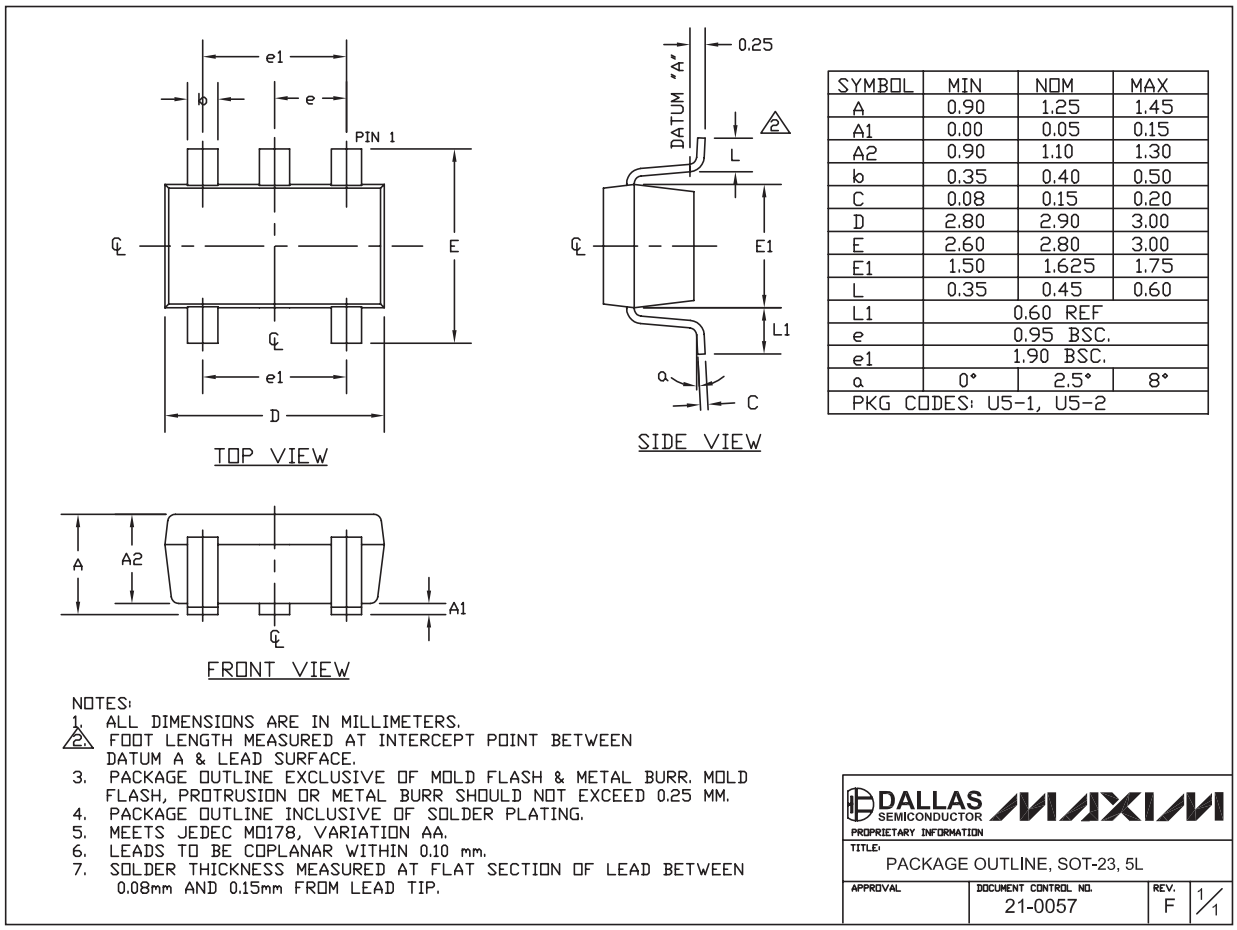
Chip Information

TRANSISTOR COUNT: 372

PROCESS: BiCMOS

Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to **www.maxim-ic.com/packages**.)



SYMBOL	MIN	NOM	MAX
A	0.90	1.25	1.45
A1	0.00	0.05	0.15
A2	0.90	1.10	1.30
b	0.35	0.40	0.50
C	0.08	0.15	0.20
D	2.80	2.90	3.00
E	2.60	2.80	3.00
E1	1.50	1.625	1.75
L	0.35	0.45	0.60
L1	0.60 REF		
e	0.95 BSC.		
e1	1.90 BSC.		
α	0°	2.5°	8°
PKG CODES: U5-1, U5-2			

Low-Power, Fixed and Adjustable Reference with Shutdown in SOT23

Revision History

REVISION NUMBER	REVISION DATE	DESCRIPTION	PAGES CHANGED
0	2/04	Initial release	—
1	12/07	Updating selector table	1–7, 9, 16