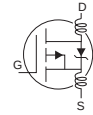


Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

	Parameter	Min.	Typ.	Max.	Units	Conditions
$V_{(BR)DSS}$	Drain-to-Source Breakdown Voltage	-55	—	—	V	$V_{GS} = 0\text{V}$, $I_D = -250\mu\text{A}$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient	—	-0.034	—	V/ $^\circ\text{C}$	Reference to 25°C , $I_D = -1\text{mA}$
$R_{DS(on)}$	Static Drain-to-Source On-Resistance	—	—	0.06	Ω	$V_{GS} = -10\text{V}$, $I_D = -16\text{A}$ ④
$V_{GS(th)}$	Gate Threshold Voltage	-2.0	—	-4.0	V	$V_{DS} = V_{GS}$, $I_D = -250\mu\text{A}$
g_{fs}	Forward Transconductance	8.0	—	—	S	$V_{DS} = -25\text{V}$, $I_D = -16\text{A}$
I_{DSS}	Drain-to-Source Leakage Current	—	—	-25	μA	$V_{DS} = -55\text{V}$, $V_{GS} = 0\text{V}$
		—	—	-250		$V_{DS} = -44\text{V}$, $V_{GS} = 0\text{V}$, $T_J = 150^\circ\text{C}$
I_{GSS}	Gate-to-Source Forward Leakage	—	—	100	nA	$V_{GS} = 20\text{V}$
	Gate-to-Source Reverse Leakage	—	—	-100		$V_{GS} = -20\text{V}$
Q_g	Total Gate Charge	—	—	63	nC	$I_D = -16\text{A}$
Q_{gs}	Gate-to-Source Charge	—	—	13		$V_{DS} = -44\text{V}$
Q_{gd}	Gate-to-Drain ("Miller") Charge	—	—	29		$V_{GS} = -10\text{V}$, See Fig. 6 and 13 ④
$t_{d(on)}$	Turn-On Delay Time	—	14	—	ns	$V_{DD} = -28\text{V}$
t_r	Rise Time	—	66	—		$I_D = -16\text{A}$
$t_{d(off)}$	Turn-Off Delay Time	—	39	—		$R_G = 6.8\Omega$
t_f	Fall Time	—	63	—		$R_D = 1.6\Omega$, See Fig. 10 ④
L_D	Internal Drain Inductance	—	4.5	—	nH	Between lead, 6mm (0.25in.) from package and center of die contact
L_S	Internal Source Inductance	—	7.5	—		
C_{iss}	Input Capacitance	—	1200	—	pF	$V_{GS} = 0\text{V}$
C_{oss}	Output Capacitance	—	520	—		$V_{DS} = -25\text{V}$
C_{rss}	Reverse Transfer Capacitance	—	250	—		$f = 1.0\text{MHz}$, See Fig. 5



Source-Drain Ratings and Characteristics

	Parameter	Min.	Typ.	Max.	Units	Conditions
I_S	Continuous Source Current (Body Diode)	—	—	-31	A	MOSFET symbol showing the integral reverse p-n junction diode.
I_{SM}	Pulsed Source Current (Body Diode) ①	—	—	-110		
V_{SD}	Diode Forward Voltage	—	—	-1.3	V	$T_J = 25^\circ\text{C}$, $I_S = -16\text{A}$, $V_{GS} = 0\text{V}$ ④
t_{rr}	Reverse Recovery Time	—	71	110	ns	$T_J = 25^\circ\text{C}$, $I_F = -16\text{A}$
Q_{rr}	Reverse Recovery Charge	—	170	250	nC	$di/dt = -100\text{A}/\mu\text{s}$ ④

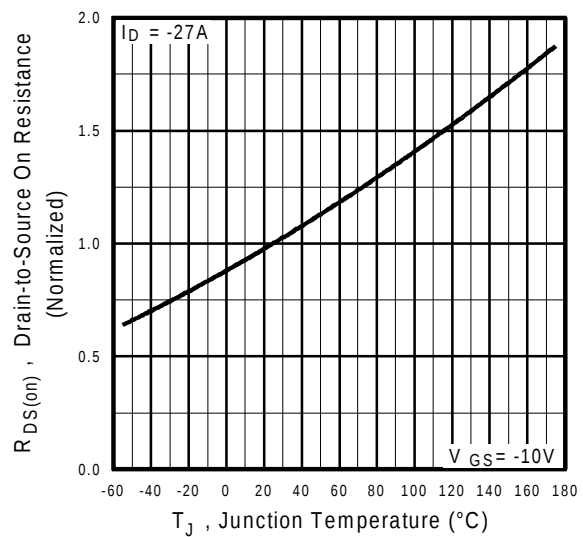
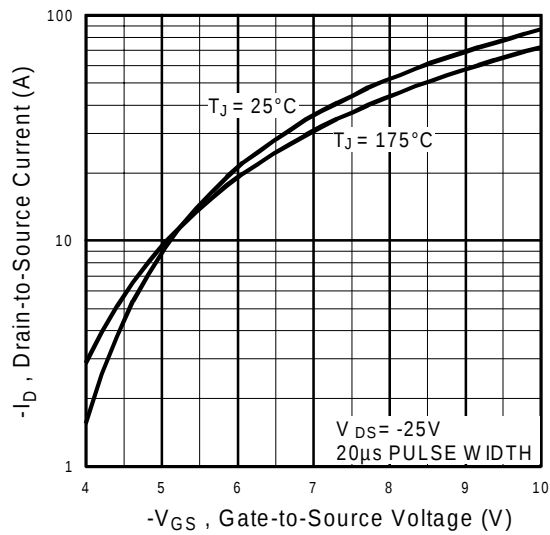
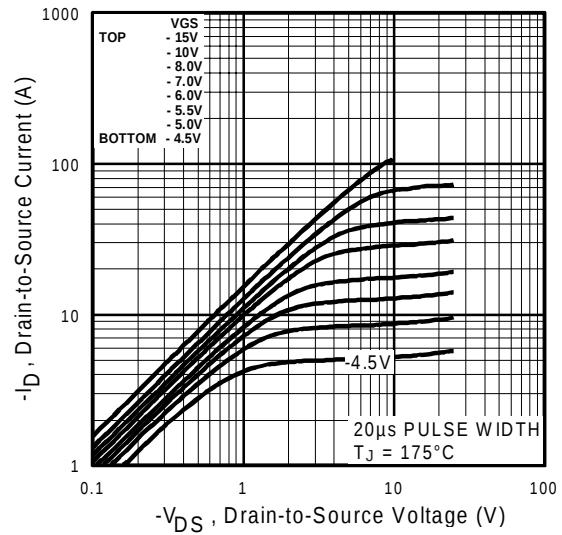
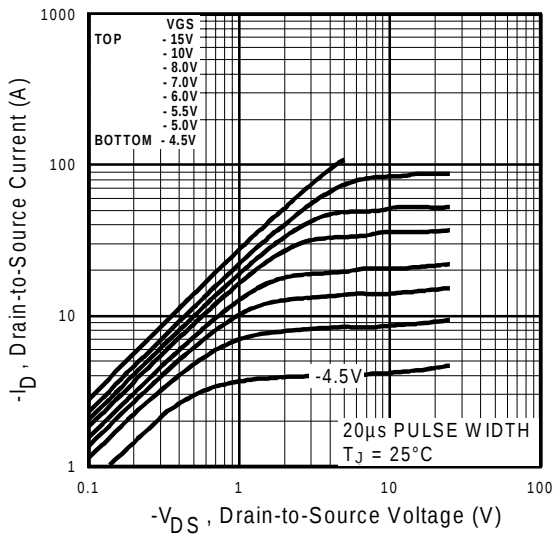
Notes:

① Repetitive rating; pulse width limited by max. junction temperature. (See fig. 11)

② $V_{DD} = -25\text{V}$, starting $T_J = 25^\circ\text{C}$, $L = 2.1\text{mH}$
 $R_G = 25\Omega$, $I_{AS} = -16\text{A}$. (See Figure 12)

③ $I_{SD} \leq -16\text{A}$, $di/dt \leq -280\text{A}/\mu\text{s}$, $V_{DD} \leq V_{(BR)DSS}$,
 $T_J \leq 175^\circ\text{C}$

④ Pulse width $\leq 300\mu\text{s}$; duty cycle $\leq 2\%$.



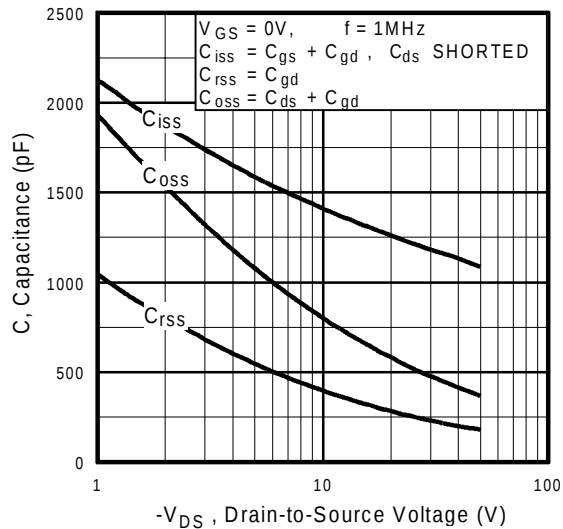


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

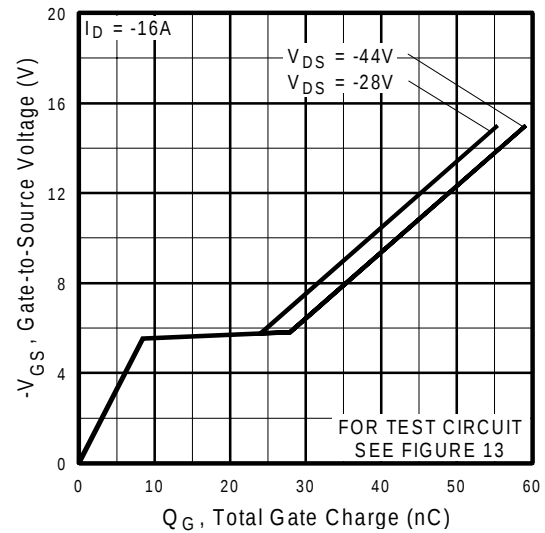


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

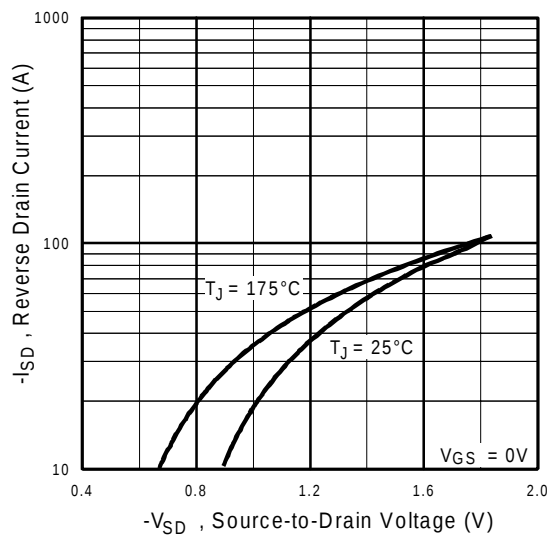


Fig 7. Typical Source-Drain Diode Forward Voltage

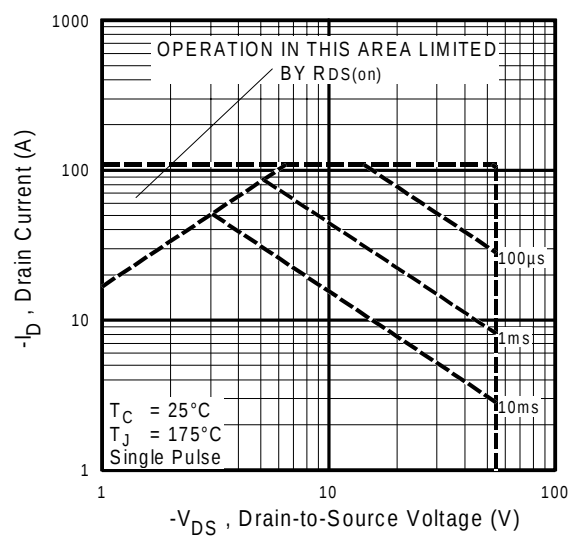


Fig 8. Maximum Safe Operating Area

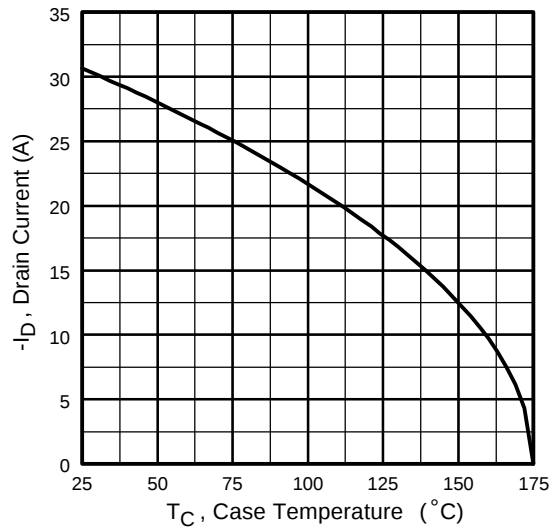


Fig 9. Maximum Drain Current Vs. Case Temperature

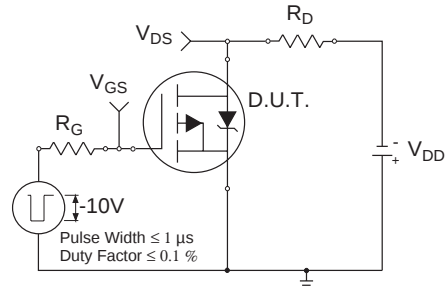


Fig 10a. Switching Time Test Circuit

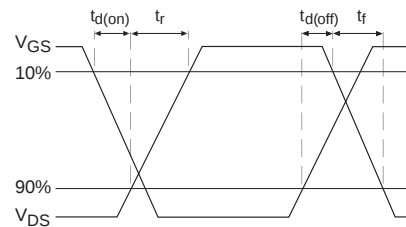


Fig 10b. Switching Time Waveforms

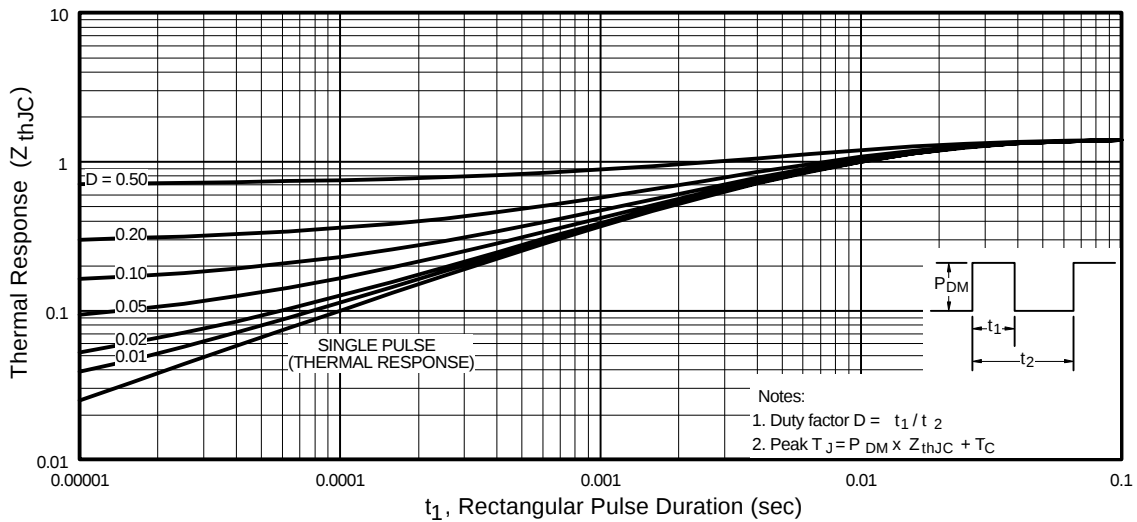


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

IRF5305

International
IOR Rectifier

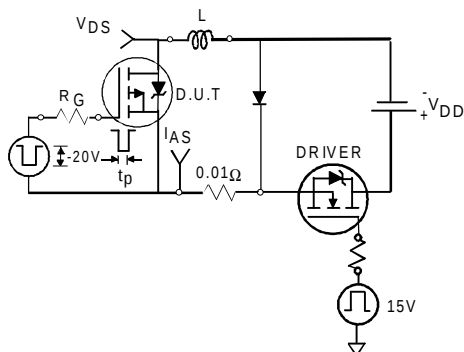


Fig 12a. Unclamped Inductive Test Circuit

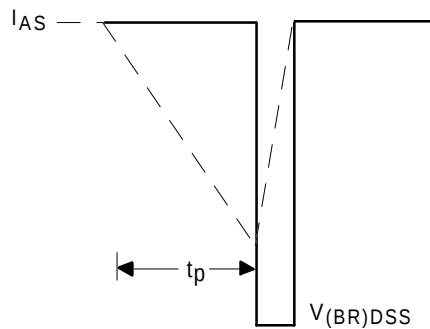


Fig 12b. Unclamped Inductive Waveforms

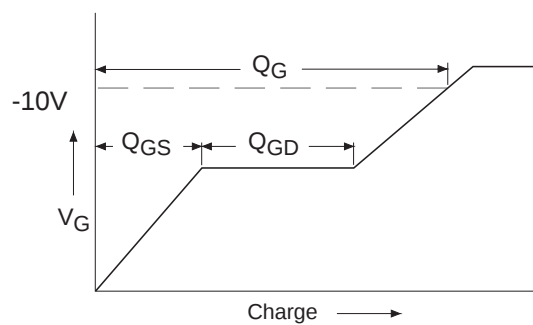


Fig 13a. Basic Gate Charge Waveform

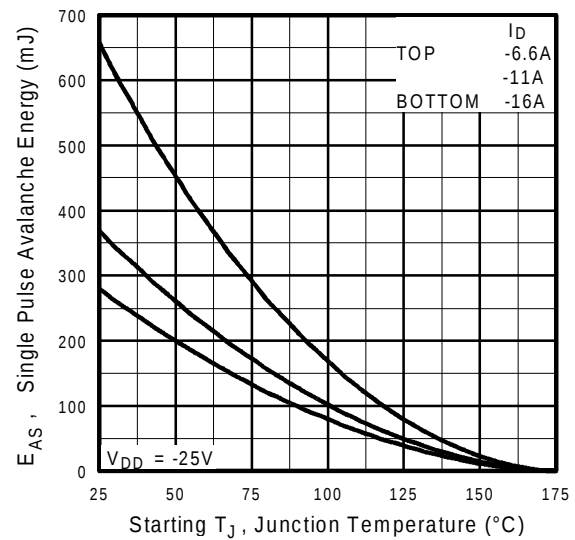


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

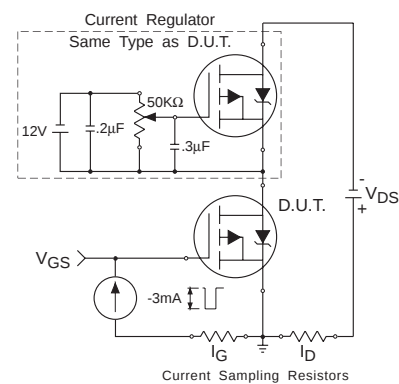
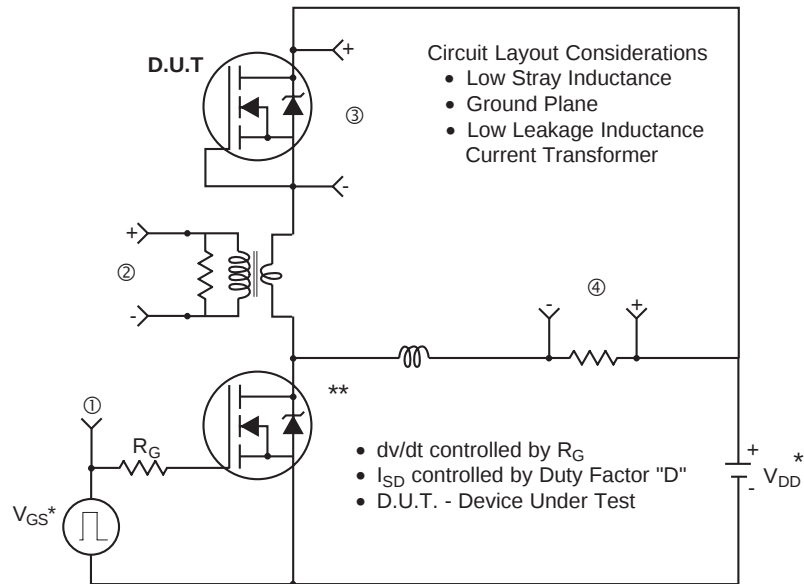


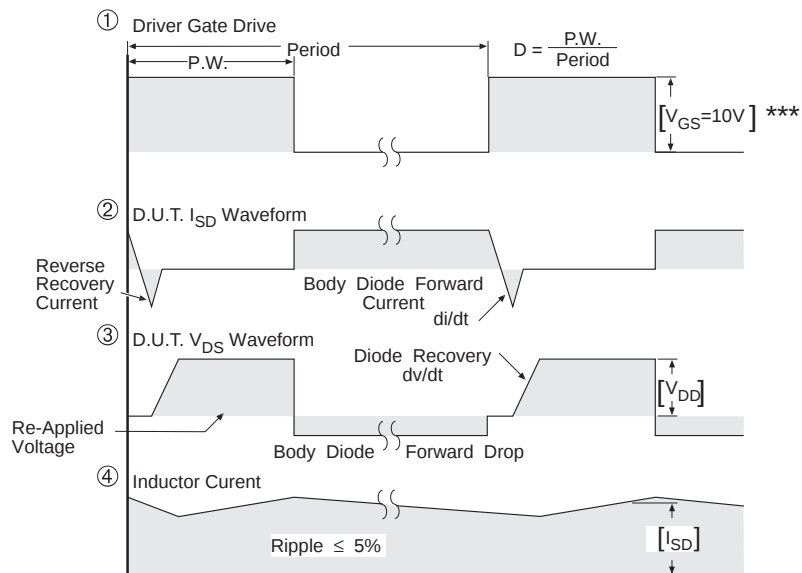
Fig 13b. Gate Charge Test Circuit

Peak Diode Recovery dv/dt Test Circuit



* Reverse Polarity for P-Channel

** Use P-Channel Driver for P-Channel Measurements



*** $V_{GS} = 5.0V$ for Logic Level and 3V Drive Devices

Fig 14. For P-Channel HEXFETS

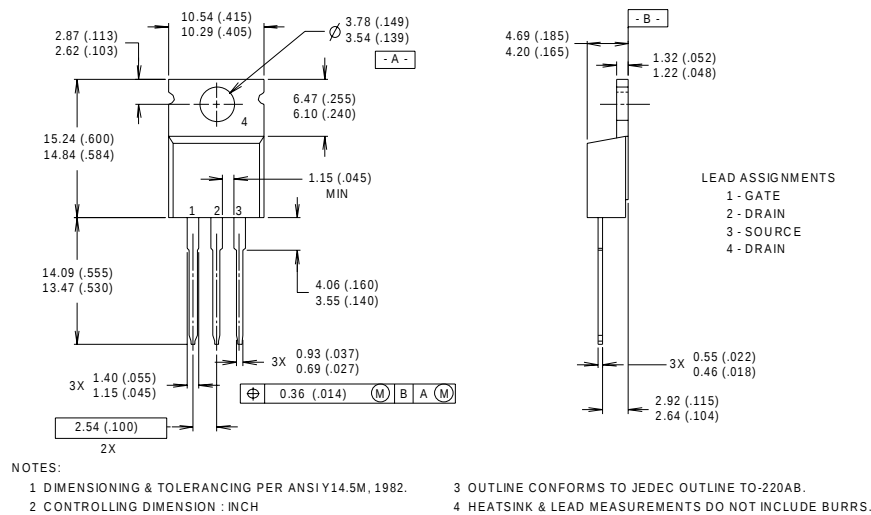
IRF5305

International
IR Rectifier

Package Outline

TO-220AB Outline

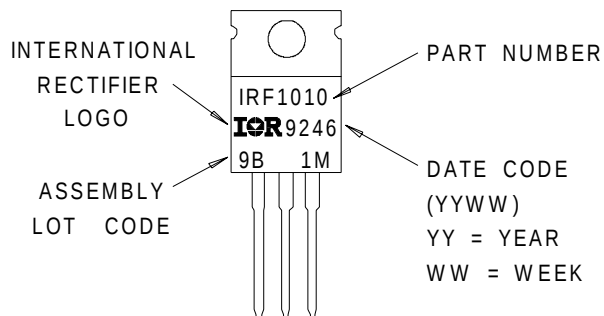
Dimensions are shown in millimeters (inches)



Part Marking Information

TO-220AB

EXAMPLE: THIS IS AN IRF1010
WITH ASSEMBLY
LOT CODE 9B1M



International
IR Rectifier

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IR CANADA: 15 Lincoln Court, Brampton, Ontario L6T3Z2, Tel: (905) 453 2200

IR GERMANY: Saalburgstrasse 157, 61350 Bad Homburg Tel: ++ 49 6172 96590

IR ITALY: Via Liguria 49, 10071 Borgaro, Torino Tel: ++ 39 11 451 0111

IR FAR EAST: K&H Bldg., 2F, 30-4 Nishi-Ikebukuro 3-Chome, Toshima-Ku, Tokyo Japan 171 Tel: 81 3 3983 0086

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<http://www.irf.com/> Data and specifications subject to change without notice. 4/99

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Note: For the most current drawings please refer to the IR website at:
<http://www.irf.com/package/>