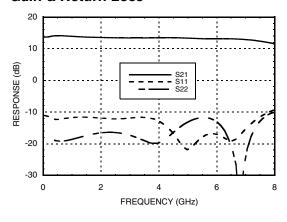




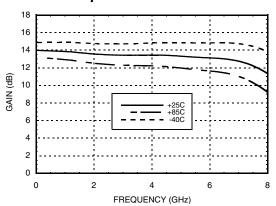
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GAAS MESFET MMIC 1 WATT Power Amplifier, DC - 6 GHz

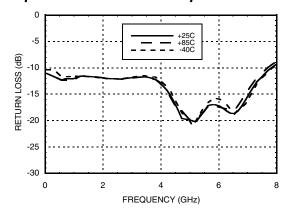
Gain & Return Loss



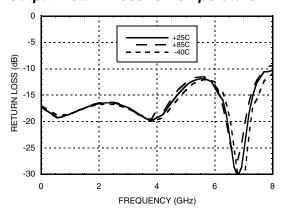
Gain vs. Temperature



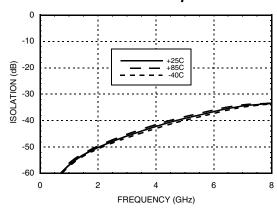
Input Return Loss vs. Temperature



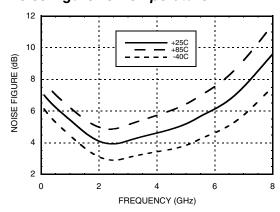
Output Return Loss vs. Temperature



Reverse Isolation vs. Temperature



Noise Figure vs. Temperature



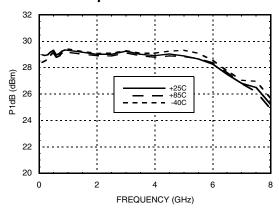




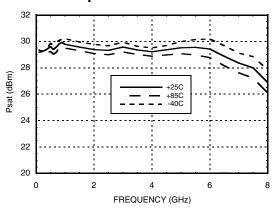
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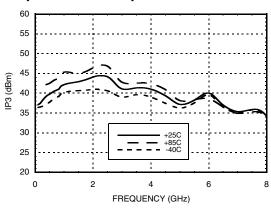
P1dB vs. Temperature



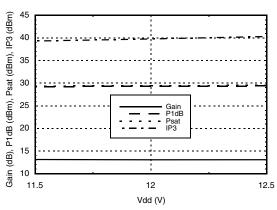
Psat vs. Temperature



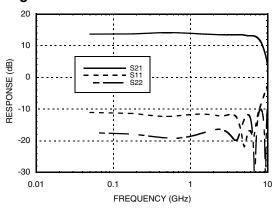
Output IP3 vs. Temperature



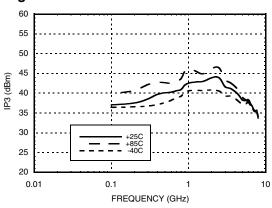
Gain, Power & Output IP3 vs. Supply Voltage @ 3 GHz, Fixed Vgg



Gain & Return Loss vs. Frequency, Log Scale



Output IP3 vs. Temperature, Log Scale







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GAAS MESFET MMIC 1 WATT Power Amplifier, DC - 6 GHz

Absolute Maximum Ratings

Drain Bias Voltage (Vdd)	+14 Vdc
Gate Bias Voltage (Vgg1)	-3 to 0 Vdc
Gate Bias Voltage (Vgg2)	+4 to +7 Vdc
RF Input Power (RFIN)(Vdd = +12 Vdc)	+25 dBm
Channel Temperature	150 °C
Continuous Pdiss (T= 85 °C) (derate 87 mW/°C above 85 °C)	5.7 W
Thermal Resistance (channel to ground paddle)	11.5 °C/W
Storage Temperature	-65 to 150 °C
Operating Temperature	-40 to 85 °C
ESD Sensitivity (HBM)	Class 1A

Typical Supply Current vs. Vdd

Vdd (V)	ldd (mA)
11.5	373
12.0	400
12.5	425



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

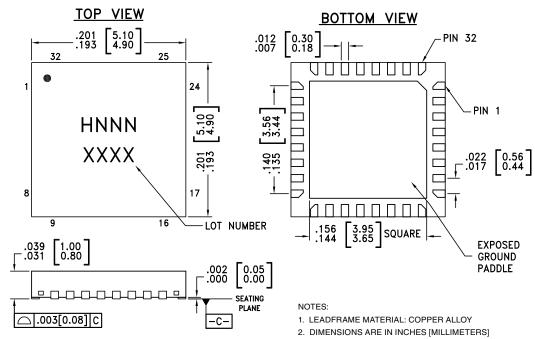




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Outline Drawing



- 3. LEAD SPACING TOLERANCE IS NON-CUMULATIVE
- 4. PAD BURR LENGTH SHALL BE 0.15mm MAXIMUM.
 PAD BURR HEIGHT SHALL BE 0.05mm MAXIMUM.
- 5. PACKAGE WARP SHALL NOT EXCEED 0.05mm.
- ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.
- 7. REFER TO HITTITE APPLICATION NOTE FOR SUGGESTED LAND PATTERN.

Package Information

Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking [3]
HMC637LP5	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 [1]	H637 XXXX
HMC637LP5E	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 [2]	H637 XXXX

- [1] Max peak reflow temperature of 235 °C
- [2] Max peak reflow temperature of 260 $^{\circ}\text{C}$
- [3] 4-Digit lot number XXXX





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Pin Descriptions

Pin Number	Function	Description	Interface Schematic
1, 3, 4, 6 - 12, 14, 17, 18, 19, 20, 22 - 28, 31, 32	N/C	No connection. These pins may be connected to RF ground. Performance will not be affected.	
2	Vgg2	Gate Control 2 for amplifier. +5V should be applied to Vgg2 for nominal operation. Attach bypass capacitor per application circuit herein.	Vgg2
5	RFIN	This pad is DC coupled and matched to 50 Ohms.	RFIN O
13	Vgg1	Gate Control 1 for amplifier. Attach bypass capacitor per application circuit herein. Please follow "MMIC Amplifier Biasing Procedure" Application Note.	Vgg1
15	ACG4	Low frequency termination. Attach bypass capacitor per application circuit herein.	RFIN ACG3
16	ACG3		<u> </u>
21	RFOUT & Vdd	RF output for amplifier. Connect the DC bias (Vdd) network to provide drain current (Idd). See application circuit herein.	ACG1 O-VV RFOUT & Vdd ACG2 O-VV
29	ACG2	Low frequency termination. Attach bypass capacitor per application circuit herein.	
30	ACG1	per approarient enough treferit.	<u> </u>
Ground Paddle	GND	Ground paddle must be connected to RF/DC ground.	⊖ GND =

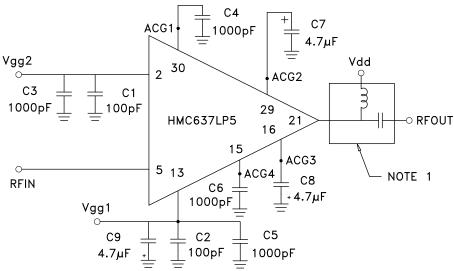




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Application Circuit



NOTE 1: Drain Bias (Vdd) must be applied through a broadband bias tee or external bias network.

NOTE 2: Power Up Bias Sequence

- A) Set Vgg1 to -2V
- B) Set Vdd to +12V
- C) Set Vgg2 to +5V
- D) Adjust Vgg1 to achieve Idd for 400 mA

Power Down Sequence

- A) Remove Vgg2 Bias
- B) Remove Vdd Bias
- C) Remove Vgg1 Bias

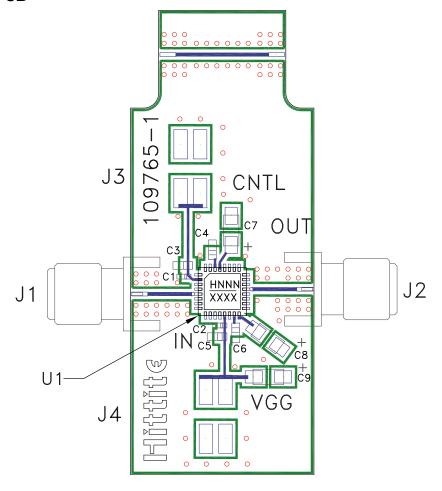




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Evaluation PCB



List of Materials for Evaluation PCB 108347 [1]

Item	Description
J1 - J2	SRI SMA Connector
J3 - J4	2mm Molex Header
C1, C2	100 pF Capacitor, 0402 Pkg.
C3 - C6	1000 pF Capacitor, 0603 Pkg.
C7 - C9	4.7 μF Capacitor, Tantalum
U1	HMC637LP5 / 637LP5E
PCB [2]	109765 Evaluation PCB

^[1] Reference this number when ordering complete evaluation PCB

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and package bottom should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation board should be mounted to an appropriate heat sink. The evaluation circuit board shown is available from Hittite upon request.

^[2] Circuit Board Material: Rogers 4350