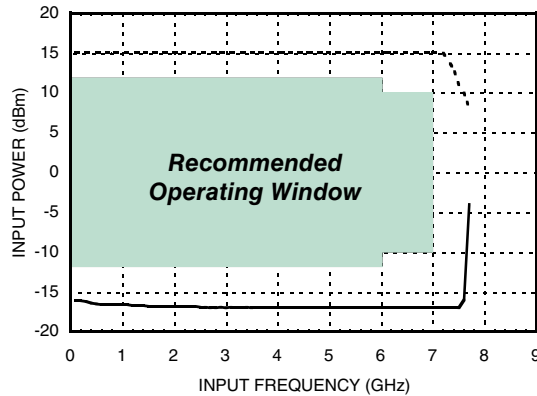




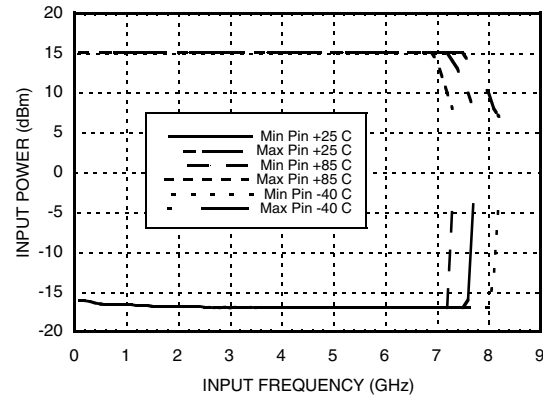
v05.1211

**SMT GaAs HBT MMIC
DIVIDE-BY-3, DC - 7 GHz**

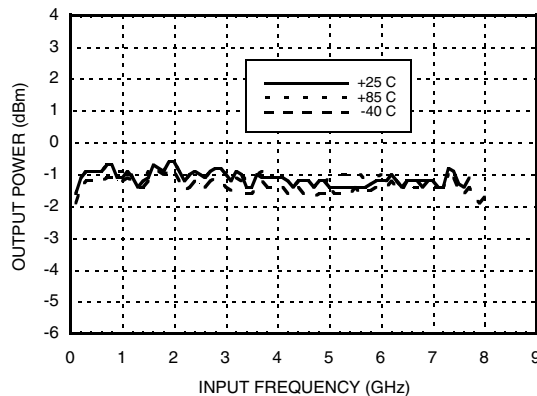
Input Sensitivity Window, $T = 25^\circ\text{C}$



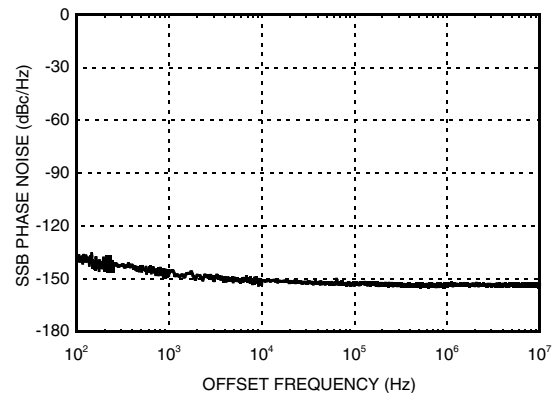
Input Sensitivity Window vs. Temperature



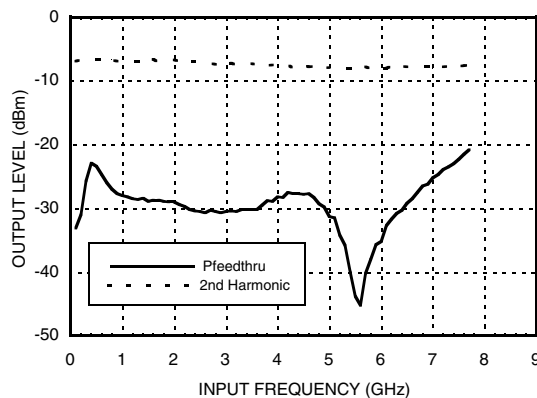
Output Power vs. Temperature



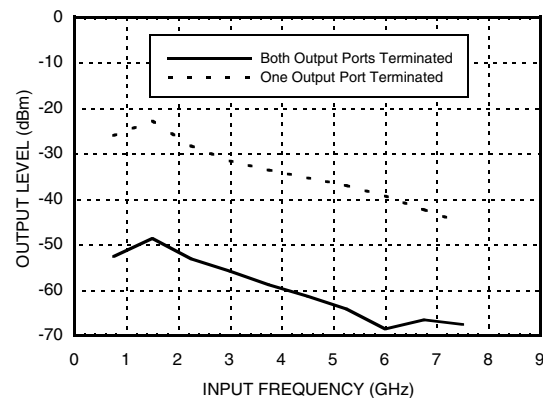
**SSB Phase Noise Performance,
 $P_{in} = 0\text{ dBm}$, $F_{in} = 6\text{ GHz}$, $T = 25^\circ\text{C}$**



Output Harmonic Content, $P_{in} = 0\text{ dBm}$, $T = 25^\circ\text{C}$



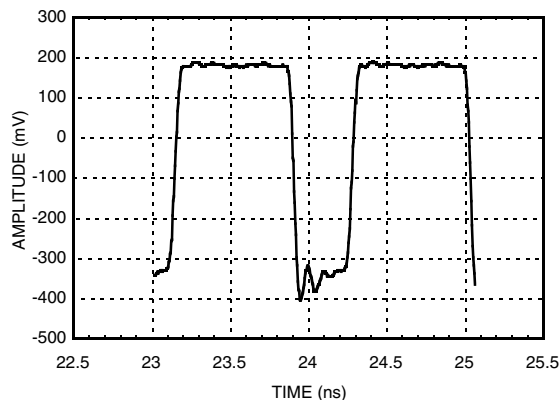
Reverse Leakage, $P_{in} = 0\text{ dBm}$, $T = 25^\circ\text{C}$





SMT GaAs HBT MMIC
DIVIDE-BY-3, DC - 7 GHz

Output Voltage Waveform,
 $P_{in} = 0 \text{ dBm}$, $F_{out} = 882 \text{ MHz}$, $T = 25^\circ\text{C}$



Absolute Maximum Ratings

RF Input Power ($V_{cc} = +5V$)	13 dBm
Nominal +5V Supply to GND	-0.3V to +5.5V
Max Peak Flow Temperature	260 °C
Storage Temperature	-65 to +150 °C
ESD Sensitivity (HBM)	Class 1A

Reliability Information

Junction Temperature to Maintain 1 Million Hour MTTF	135 °C
Nominal Junction Temperature ($T = 85^\circ\text{C}$)	122 °C
Thermal Resistance (Junction to GND Paddle, 5V Supply)	101.5 °C/W
Operating Temperature	-40 to +85 °C

DC blocking capacitors are required at RF input and RF output ports.
Choose value for lowest frequency of operation.



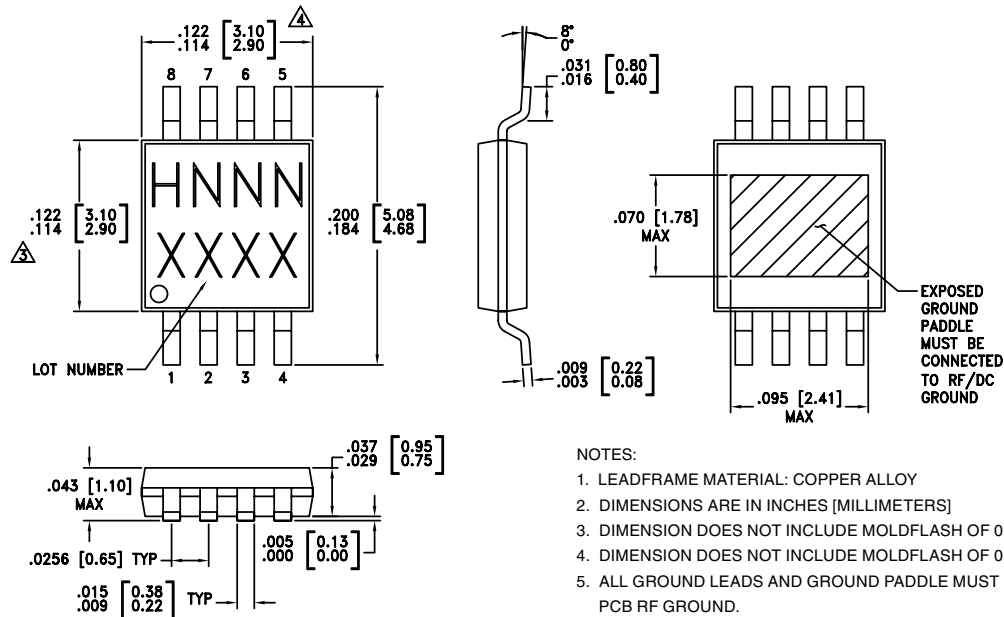
ELECTROSTATIC SENSITIVE DEVICE
OBSERVE HANDLING PRECAUTIONS

Typical Supply Current vs. V_{cc}

V_{cc} (V)	I_{cc} (mA)
4.75	64
5.0	69
5.25	75

Note: Divider will operate over full voltage range shown above

Outline Drawing



Package Information

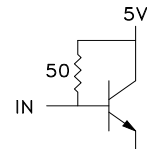
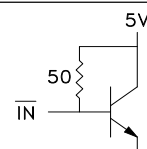
Part Number	Package Body Material	Lead Finish	MSL Rating	Package Marking ^[3]
HMC437MS8G	Low Stress Injection Molded Plastic	Sn/Pb Solder	MSL1 ^[1]	H437 XXXX
HMC437MS8GE	RoHS-compliant Low Stress Injection Molded Plastic	100% matte Sn	MSL1 ^[2]	H437 XXXX

[1] Max peak reflow temperature of 235 °C


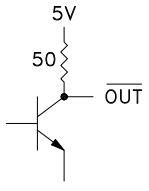
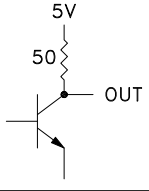
[2] Max peak reflow temperature of 260 °C

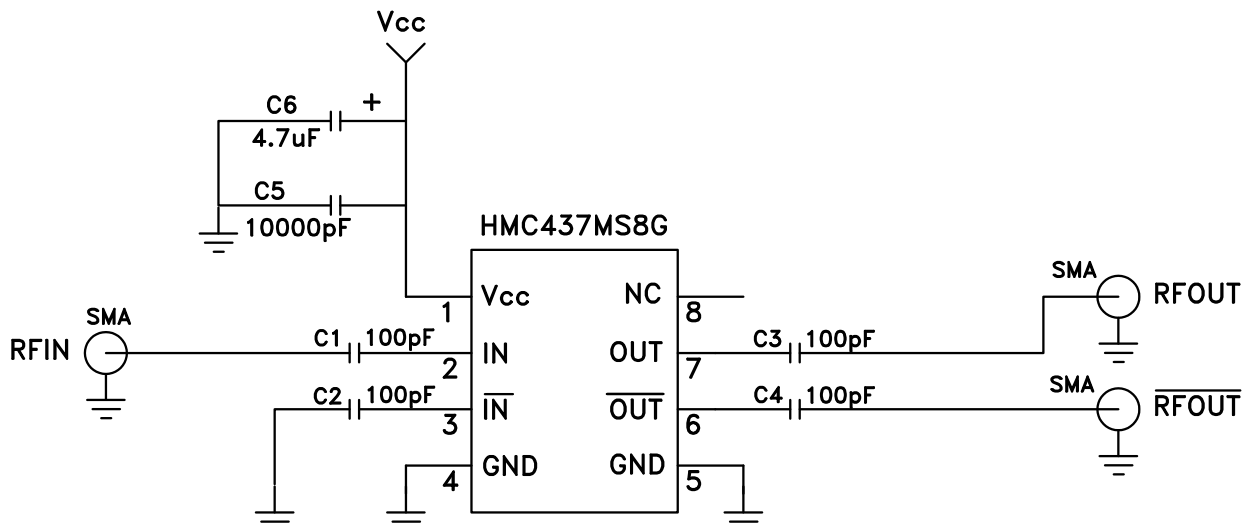
[3] 4-Digit lot number XXXX

Pin Description

Pin Number	Function	Description	Interface Schematic
1	Vcc	Supply voltage 5V ± 0.25V.	
2	IN	RF input must be DC blocked.	
3	IN	RF input 180° out of phase with pin 2 for differential operation. AC ground for single ended operation.	

Pin Description (Continued)

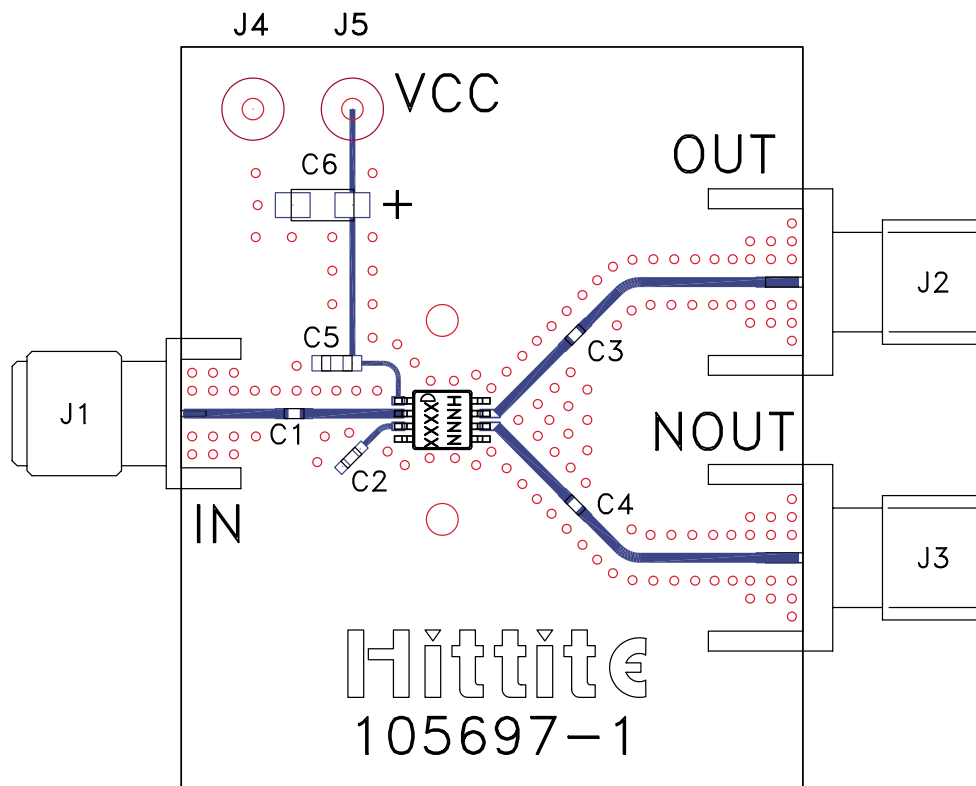
Pin Number	Function	Description	Interface Schematic
4, 5	GND	All ground leads and ground paddle must be soldered to PCB RF/DC ground.	
6	$\overline{\text{OUT}}$	Divided output 180° out of phase with pin 7.	
7	OUT	Divided Output.	
8	N/C	The pins are not connected internally; however, all data shown herein was measured with these pins connected to RF/DC ground externally.	

Application Circuit


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Evaluation PCB



List of Materials for Evaluation PCB 105786 [1]

Item	Description
J1 - J3	PCB Mount SMA RF Connector
J4, J5	DC Pin
C1 - C4	100 pF Capacitor, 0402 Pkg.
C5	10,000 pF Capacitor, 0603 Pkg.
C6	4.7 μ F Tantalum Capacitor
U1	HMC437MS8G / HMC437MS8GE Divide-by-3
PCB [2]	105697 Eval Board

[1] Reference this number when ordering complete evaluation PCB

[2] Circuit Board Material: Rogers 4350

The circuit board used in the application should use RF circuit design techniques. Signal lines should have 50 Ohm impedance while the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request. This evaluation board is designed for single ended input testing. J2 and J3 provide differential output signals.