

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX. UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 µA		1200	-	- V
Maximum forward voltage	V _{FM}	I _F = 8.0 A	See fig. 1	-	2.6	3.3
		I _F = 16 A		-	3.4	4.3
		I _F = 8.0 A, T _J = 125 °C		-	2.4	3.1
Maximum reverse leakage current	I _{RM}	V _R = V _R rated	See fig. 2	-	0.31	10
		T _J = 125 °C, V _R = 0.8 x V _R rated		-	135	1000
Junction capacitance	C _T	V _R = 200 V	See fig. 3	-	11	20 pF
Series inductance	L _S	Measured lead to lead 5 mm from package body		-	8.0	- nH

DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX. UNITS
Reverse recovery time See fig. 5, 10	t _{rr}	I _F = 1.0 A, dI _F /dt = 200 A/µs, V _R = 30 V		-	28	-
	t _{rr1}	T _J = 25 °C	I _F = 8.0 A dI _F /dt = 200 A/µs V _R = 200 V	-	63	95
	t _{rr2}	T _J = 125 °C		-	106	160
Peak recovery current See fig. 6	I _{RRM1}	T _J = 25 °C		-	4.5	8.0
	I _{RRM2}	T _J = 125 °C		-	6.2	11
Reverse recovery charge See fig. 7	Q _{rr1}	T _J = 25 °C		-	140	380
	Q _{rr2}	T _J = 125 °C		-	335	880
Peak rate of recovery current during t _b See fig. 8	dI _(rec) /dt1	T _J = 25 °C		-	133	-
	dI _(rec) /dt2	T _J = 125 °C		-	85	-

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX. UNITS
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s		-	-	300 °C
Thermal resistance, junction to case	R _{thJC}			-	-	1.7
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount		-	-	40
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased		-	0.25	-
Weight				-	6.0	-
				-	0.21	-
Mounting torque				6.0 (5.0)	-	12 (10)
Marking device		Case style TO-247AC modified (JEDEC)		HFA08PB120		

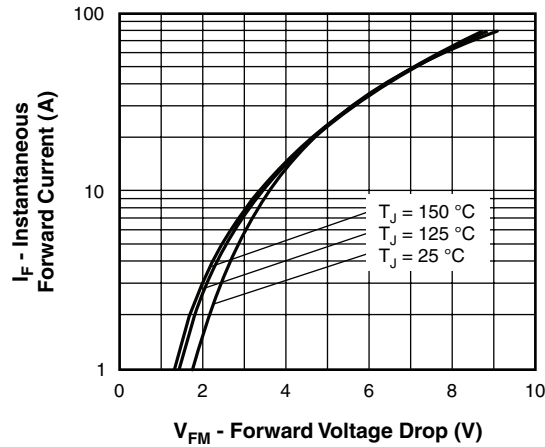


Fig. 1 - Maximum Forward Voltage Drop Characteristics

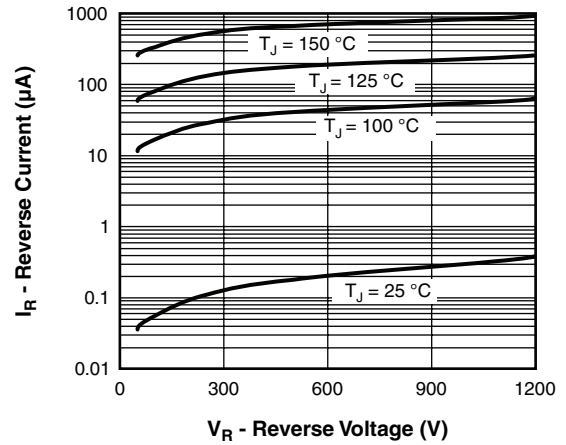


Fig. 2 - Values of Reverse Current vs. Reverse Voltage

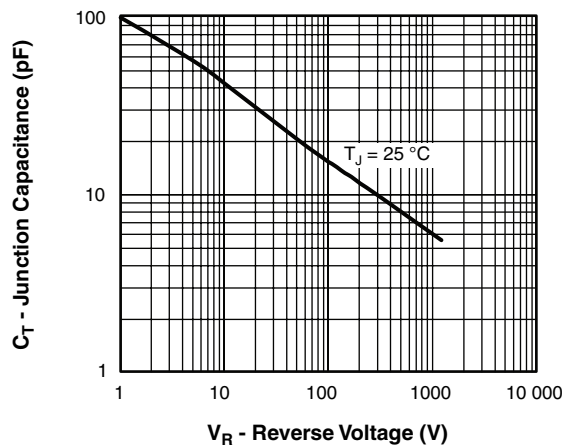


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

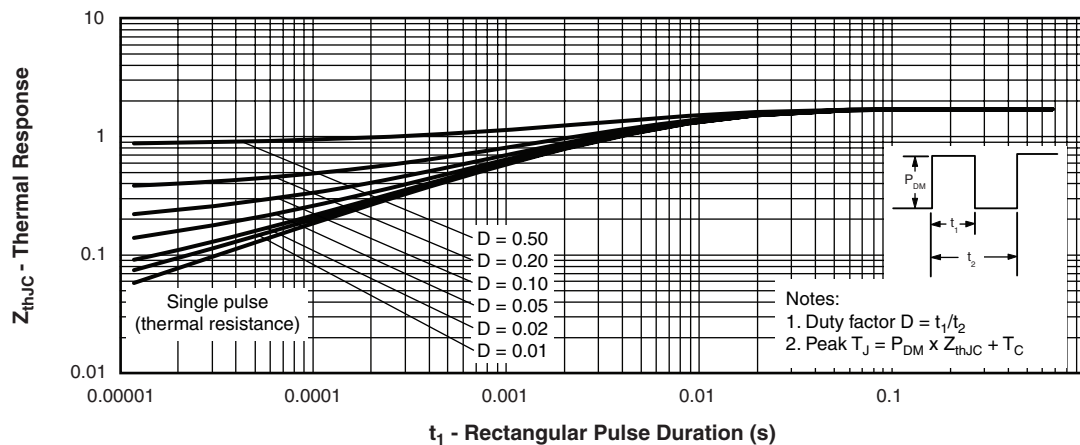
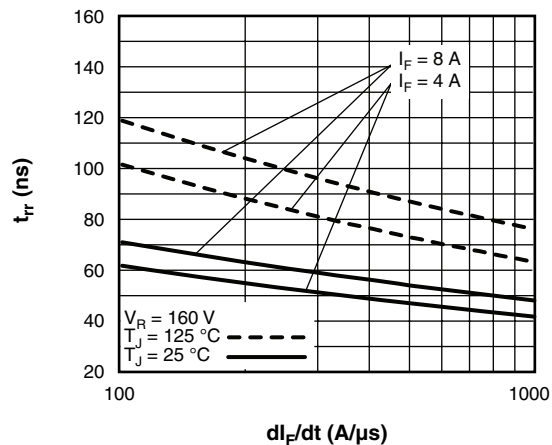
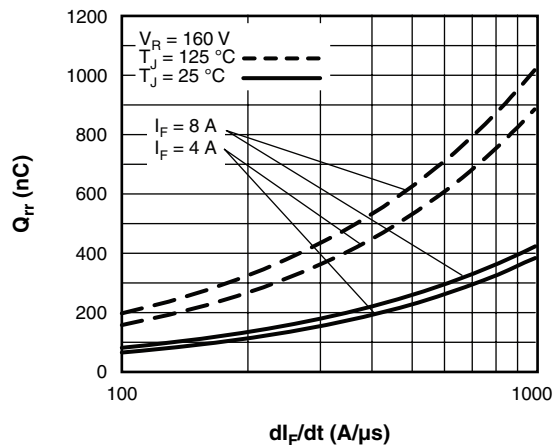
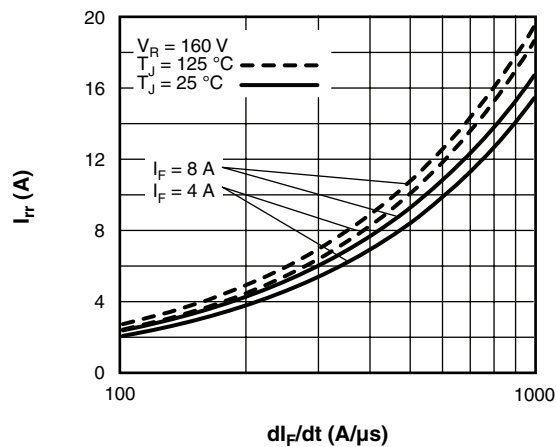
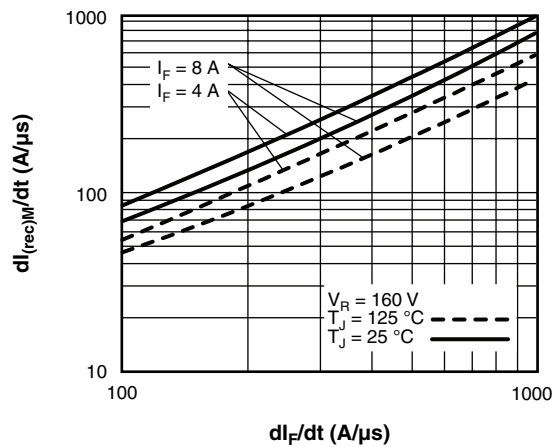


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Fig. 5 - Typical Reverse Recovery Time vs. dI_F/dt Fig. 7 - Typical Stored Charge vs. dI_F/dt Fig. 6 - Typical Recovery Current vs. dI_F/dt Fig. 8 - Typical $dI_{(rec)M}/dt$ vs. dI_F/dt

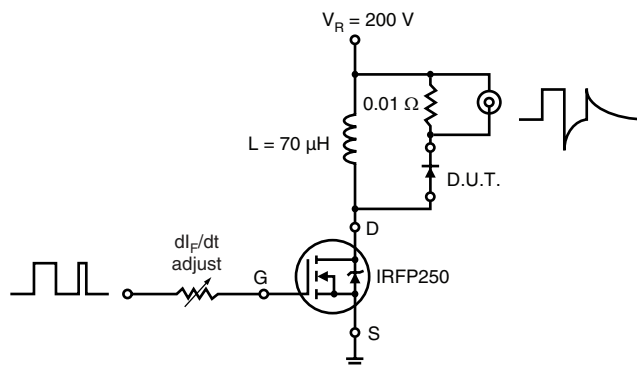


Fig. 9 - Reverse Recovery Parameter Test Circuit

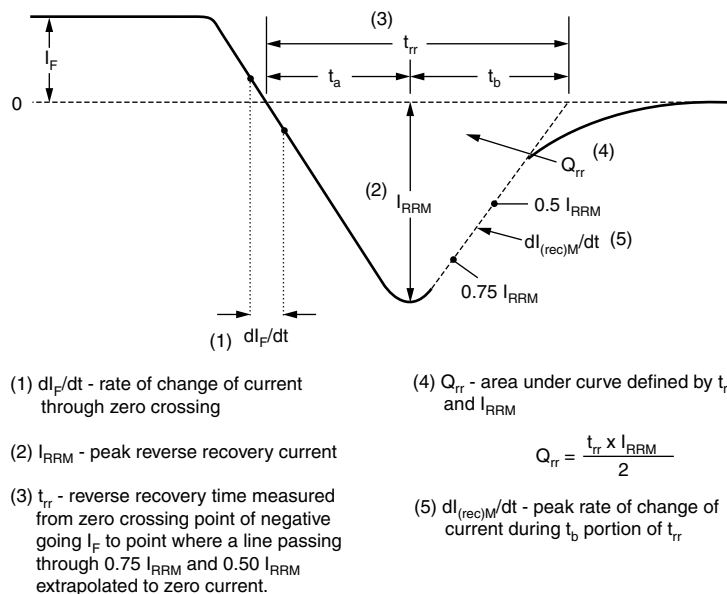


Fig. 10 - Reverse Recovery Waveform and Definitions

**ORDERING INFORMATION TABLE**

Device code	HF	A	08	PB	120	PbF
	1	2	3	4	5	6

- | | | |
|---|---|--|
| 1 | - | HEXFRED® family |
| 2 | - | Process designator: A = Electron irradiated
B = Platinum diffused |
| 3 | - | Current rating (08 = 8 A) |
| 4 | - | Package outline (PB = TO-247, 2 pins) |
| 5 | - | Voltage rating (120 = 1200 V) |
| 6 | - | <ul style="list-style-type: none"> • None = Standard production • PbF = Lead (Pb)-free |

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95253
Part marking information	http://www.vishay.com/doc?95255



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