

Marking Information

BC7 YM

BC7 = Product Type Marking Code
YM = Date Code Marking
Y = Year (ex: 1 = 2021)
M = Month (ex: 9 = September)

BC7 YWX

BC7 = Product Type Marking Code
YWX = Date Code Marking
Y = Year (ex: 1 = 2021)
W = Week
(ex: a=Week 27; z Represents Week 52 and 53)
X = Internal Code (ex: U = Monday)

Date Code Key for YM

Year	2013	...	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	A	...	I	J	K	L	M	N	O	P	R	S
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Date Code Key for YWX

Year	2013	...	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030
Code	3	...	1	2	3	4	5	6	7	8	9	0
Week	1-26				27-52				53			
Code	A-Z				a-z				z			
Internal Code	Sun	Mon	Tue	Wed	Thu	Fri	Sat					
Code	T	U	V	W	X	Y	Z					

Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit	Conditions
Peak Pulse Current, per IEC 61000-4-5	I _{PP}	5.5	A	I/O to V _{SS} , 8/20μs
Peak Pulse Power, per IEC 61000-4-5	P _{PP}	60	W	I/O to V _{SS} , 8/20μs
Operating Voltage (DC)	V _{DC}	6	V	I/O to V _{SS}
ESD Protection – Contact Discharge, per IEC 61000-4-2	V _{ESD_CONTACT}	±14	kV	I/O to V _{SS}
ESD Protection – Air Discharge, per IEC 61000-4-2	V _{ESD_AIR}	±16	kV	I/O to V _{SS}
Operating Temperature	T _{OP}	-55 to +85	°C	—
Storage Temperature	T _{STG}	-55 to +150	°C	—

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Power Dissipation Typical (Note 5)	P _D	350	mW
Thermal Resistance, Junction to Ambient Typical (Note 5)	R _{θJA}	360	°C/W

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Working Voltage	V _{RWM}	—	—	5.5	V	—
Reverse Current	I _R	—	—	0.5	μA	V _R = 5V, I/O to V _{SS}
Reverse Breakdown Voltage	V _{BR}	6	—	—	V	I _R = 1mA, I/O to V _{SS}
Forward Clamping Voltage	V _F	-1.0	-0.85	—	V	I _F = -15mA, I/O to V _{SS}
Holding Voltage	V _H	5.5	—	—	V	—
Reverse Clamping Voltage (Note 6)	V _C	—	9.4	11	V	I _{PP} = 5.5A, I/O to V _{SS} , 8/20μs
Trigger Voltage	V _{TRIG}	—	—	9.5	V	—
ESD Clamping Voltage	V _{ESD}	—	9	—	V	TLP, 10A, t _P = 100ns, I/O to V _{SS}
Dynamic Reverse Resistance	R _{DIF-R}	—	0.25	—	Ω	TLP, 10A, t _P = 100ns, I/O to V _{SS}
Dynamic Forward Resistance	R _{DIF-F}	—	0.25	—	Ω	TLP, 10A, t _P = 100ns, V _{SS} to I/O
Channel Input Capacitance (Note 7)	C _{I/O}	—	0.55	0.65	pF	V _{I/O} = 2.5V, V _{SS} = 0V, f = 1MHz
Delta C _{I/O}	C _{I/OMAX} -C _{I/OMIN}	—	0.04	—	pF	C _{I/OMAX} -C _{I/OMIN}

Notes: 5. Device mounted on FR-4 PCB pad layout (2oz copper) as shown on Diodes Incorporated's suggested pad layout, which can be found on our website at <http://www.diodes.com/package-outlines.html>.

6. Clamping voltage value is based on an 8x20μs peak pulse current (I_{PP}) waveform.

7. C_{I/O1}=C_{PIN1}+C_{PIN10}, C_{I/O2}=C_{PIN2}+C_{PIN9}, C_{I/O3}=C_{PIN4}+C_{PIN7}, C_{I/O4}=C_{PIN5}+C_{PIN6}

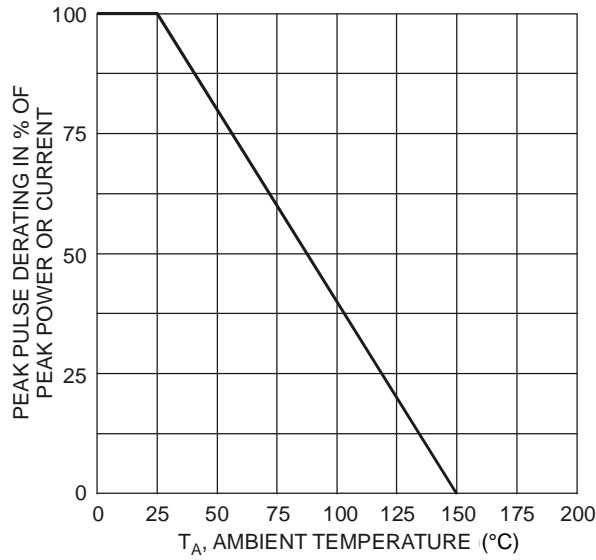


Figure 1 Pulse Derating Curve

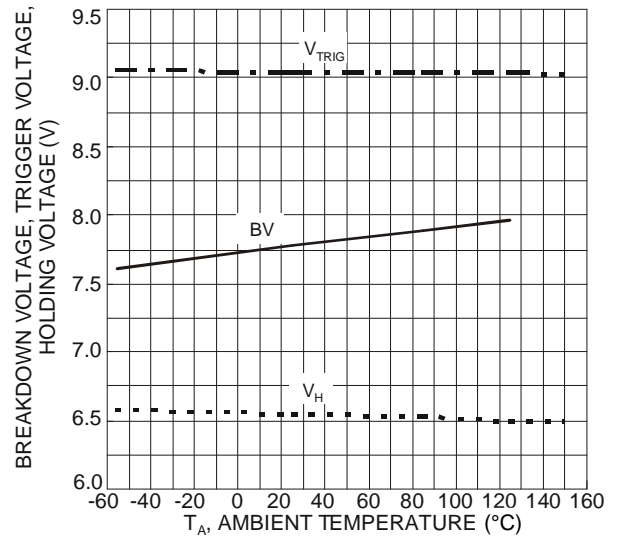


Figure 2 BV, Trigger Voltage, Holding Voltage vs. Ambient Temperature

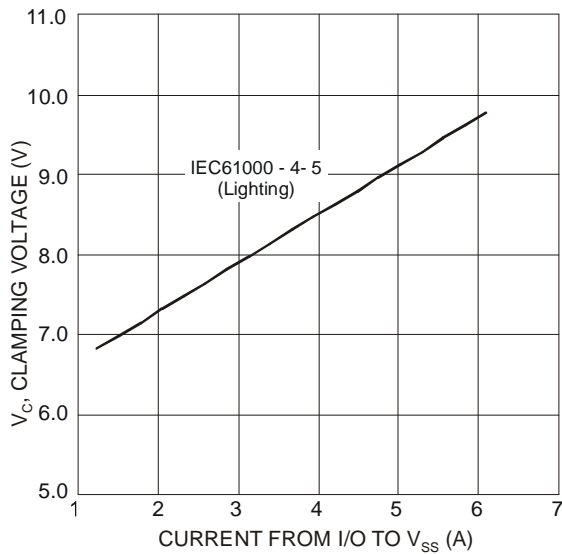


Figure 3 Clamping Voltage Characteristic

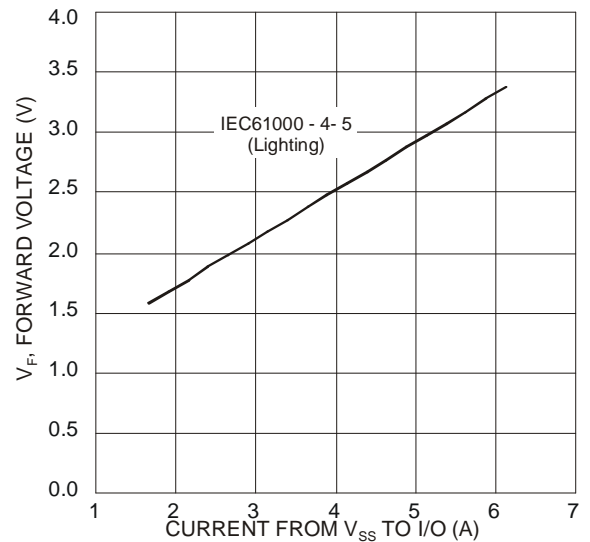
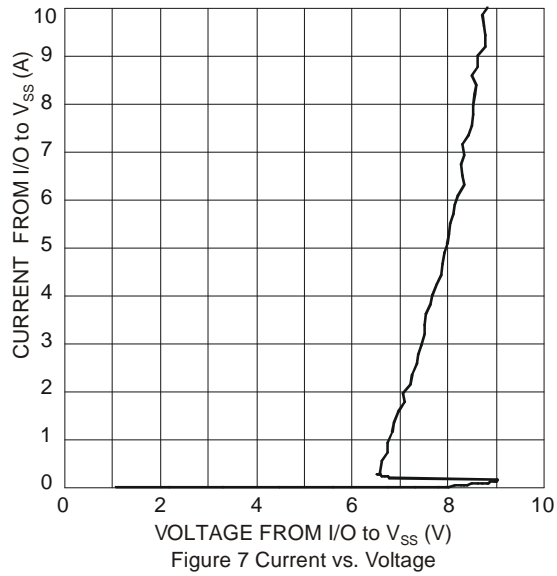
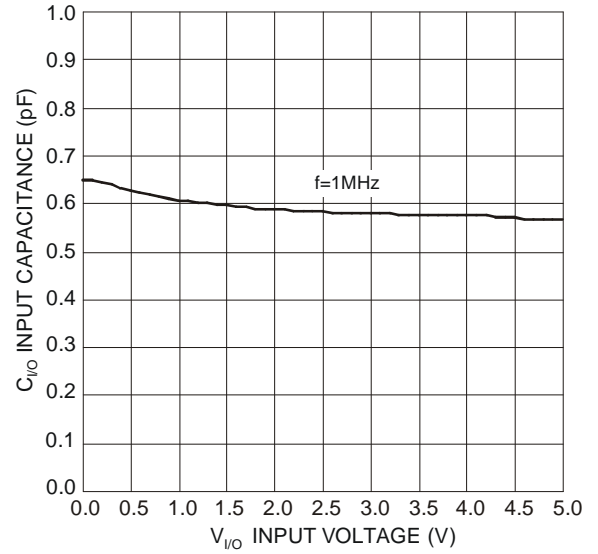
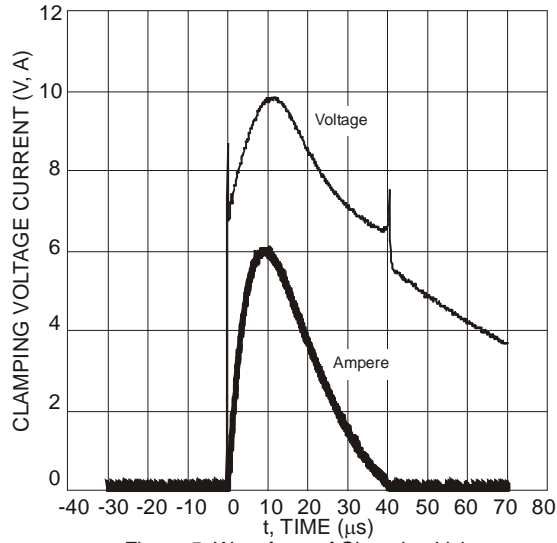


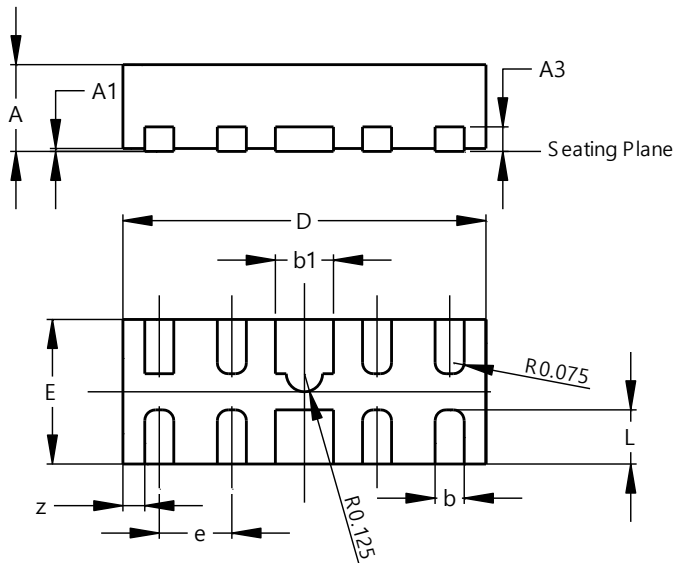
Figure 4 Forward Voltage Characteristic



Package Outline Dimensions

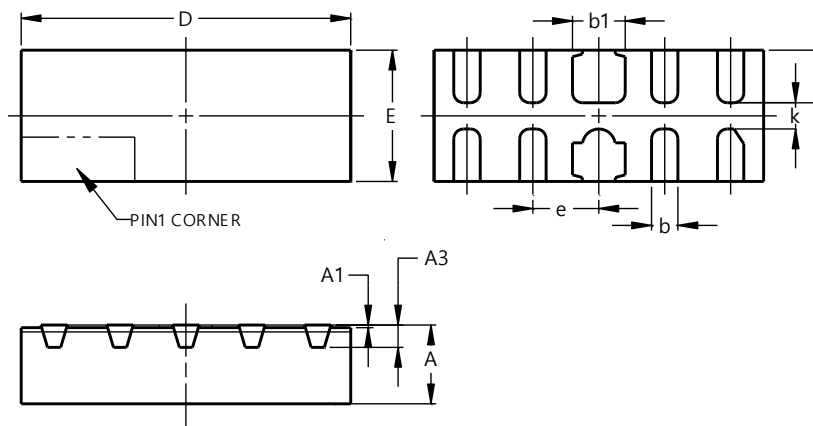
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2510-10



U-DFN2510-10			
Dim	Min	Max	Typ
A	0.545	0.605	0.575
A1	0.00	0.05	0.03
A3	-	-	0.13
b	0.15	0.25	0.20
b1	0.35	0.45	0.40
D	2.450	2.575	2.500
e	-	-	0.50
E	0.950	1.075	1.000
L	0.325	0.425	0.375
z	-	-	0.150
All Dimensions in mm			

U-DFN2510-10 (Type CJ)

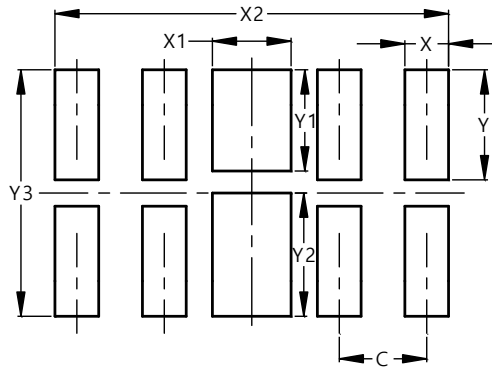


U-DFN2510-10 (Type CJ)			
Dim	Min	Max	Typ
A	0.545	0.605	--
A1	0.00	0.05	--
A3	0.152REF		
b	0.150	0.250	--
b1	0.350	0.450	--
D	2.450	2.575	--
E	0.950	1.075	--
e	--	--	0.500
E	0.950	1.075	1.000
L	0.350	0.450	--
k	0.200REF		
All Dimensions in mm			

Suggested Pad Layout

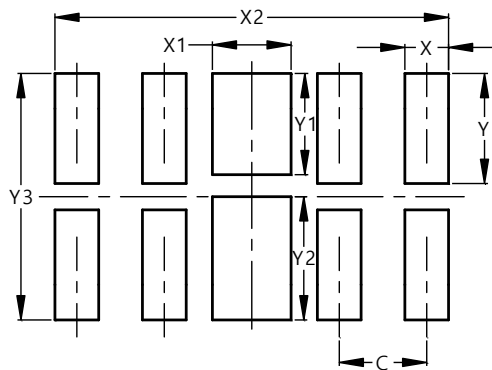
Please see <http://www.diodes.com/package-outlines.html> for the latest version.

U-DFN2510-10



Dimensions	Value (in mm)
C	0.500
X	0.250
X1	0.450
X2	2.250
Y	0.625
Y1	0.575
Y2	0.700
Y3	1.400

U-DFN2510-10 (Type CJ)



Dimensions	Value (in mm)
C	0.500
X	0.250
X1	0.450
X2	2.250
Y	0.625
Y1	0.575
Y2	0.700
Y3	1.400

IMPORTANT NOTICE

1. DIODES INCORPORATED AND ITS SUBSIDIARIES ("DIODES") MAKE NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO ANY INFORMATION CONTAINED IN THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY, FITNESS FOR A PARTICULAR PURPOSE OR NON-INFRINGEMENT OF THIRD PARTY INTELLECTUAL PROPERTY RIGHTS (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).
2. The Information contained herein is for informational purpose only and is provided only to illustrate the operation of Diodes products described herein and application examples. Diodes does not assume any liability arising out of the application or use of this document or any product described herein. This document is intended for skilled and technically trained engineering customers and users who design with Diodes products. Diodes products may be used to facilitate safety-related applications; however, in all instances customers and users are responsible for (a) selecting the appropriate Diodes products for their applications, (b) evaluating the suitability of the Diodes products for their intended applications, (c) ensuring their applications, which incorporate Diodes products, comply the applicable legal and regulatory requirements as well as safety and functional-safety related standards, and (d) ensuring they design with appropriate safeguards (including testing, validation, quality control techniques, redundancy, malfunction prevention, and appropriate treatment for aging degradation) to minimize the risks associated with their applications.
3. Diodes assumes no liability for any application-related information, support, assistance or feedback that may be provided by Diodes from time to time. Any customer or user of this document or products described herein will assume all risks and liabilities associated with such use, and will hold Diodes and all companies whose products are represented herein or on Diodes' websites, harmless against all damages and liabilities.
4. Products described herein may be covered by one or more United States, international or foreign patents and pending patent applications. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks and trademark applications. Diodes does not convey any license under any of its intellectual property rights or the rights of any third parties (including third parties whose products and services may be described in this document or on Diodes' website) under this document.
5. Diodes products are provided subject to Diodes' Standard Terms and Conditions of Sale (<https://www.diodes.com/about/company/terms-and-conditions/terms-and-conditions-of-sales/>) or other applicable terms. This document does not alter or expand the applicable warranties provided by Diodes. Diodes does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel.
6. Diodes products and technology may not be used for or incorporated into any products or systems whose manufacture, use or sale is prohibited under any applicable laws and regulations. Should customers or users use Diodes products in contravention of any applicable laws or regulations, or for any unintended or unauthorized application, customers and users will (a) be solely responsible for any damages, losses or penalties arising in connection therewith or as a result thereof, and (b) indemnify and hold Diodes and its representatives and agents harmless against any and all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim relating to any noncompliance with the applicable laws and regulations, as well as any unintended or unauthorized application.
7. While efforts have been made to ensure the information contained in this document is accurate, complete and current, it may contain technical inaccuracies, omissions and typographical errors. Diodes does not warrant that information contained in this document is error-free and Diodes is under no obligation to update or otherwise correct this information. Notwithstanding the foregoing, Diodes reserves the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes.
8. Any unauthorized copying, modification, distribution, transmission, display or other use of this document (or any portion hereof) is prohibited. Diodes assumes no responsibility for any losses incurred by the customers or users or any third parties arising from any such unauthorized use.

Copyright © 2021 Diodes Incorporated

www.diodes.com