

Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	60	V
Collector-Emitter Voltage	V _{CEO}	50	V
Emitter-Base Voltage	V _{EBO}	5	V
Collector Current – Continuous	lc	100	mA
Peak Pulse Collector Current	I _{CM}	200	mA
Base Current	IB	30	mA

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Unit	
Dower Dissinction	(Note 5)	D	400		
Power Dissipation	(Note 6)	P _D	1000	mW	
Thermal Resistance, Junction to Ambient	(Note 5)	- R _{0JA}	310		
	(Note 6)		120	°C/W	
Thermal Resistance, Junction to Lead	(Note 7)	R _{θJL}	120	°C/W	
Operating and Storage and Temperature Ran	ge	T _J , T _{STG}	-55 to +150	°C	

ESD Ratings (Note 8)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS							
Collector-Base Breakdown Voltage		BV _{CBO}	60	_		V	I _C = 10μA, I _E = 0
Collector-Emitter Breakdown Voltage (N	lote 8)	BV _{CEO}	50	_	_	V	$I_{\rm C} = 1 {\rm mA}, I_{\rm B} = 0$
Emitter-Base Breakdown Voltage		BV _{EBO}	5	_	_	V	I _E = 10μA, I _C = 0
Collector Cut-Off Current		Ісво	_	_	0.1	μA	$V_{CB} = 60V, I_E = 0$
Emitter Cut-Off Current		ІЕВО	—	—	0.1	μA	V _{EB} = 5V, I _C = 0
ON CHARACTERISTICS (Note 9)							
Collector-Emitter Saturation Voltage		V _{CE(SAT)}	_	0.10	0.25	V	I _C = 100mA, I _B = 10mA
DC Current Gain	DN0150ALP4		120	_	240		$V_{CE} = 6V, I_{C} = 2mA$
	DN0150BLP4	h _{FE}	200	—	400		$V_{CE} = 6V, I_C = 2IIIA$
SMALL SIGNAL CHARACTERISTICS							
Transition Frequency		f⊤	60	—	—	MHz	V _{CE} = 10V, I _E = -1mA f = 30MHz
Output Capacitance		C _{ob}	_	1.3	_	pF	V _{CB} = 10V, I _E = 0, f = 1MHz

Notes:

5. For the device mounted on minimum recommended pad layout 1oz copper that is on a single-sided 1.6mm FR4 PCB; device is measured under still air conditions whilst operating in steady state condition. The entire exposed collector pad is attached to the heatsink.

6. Same as Note 5, except the exposed collector pad is mounted on 25mm x 25mm 2oz copper.

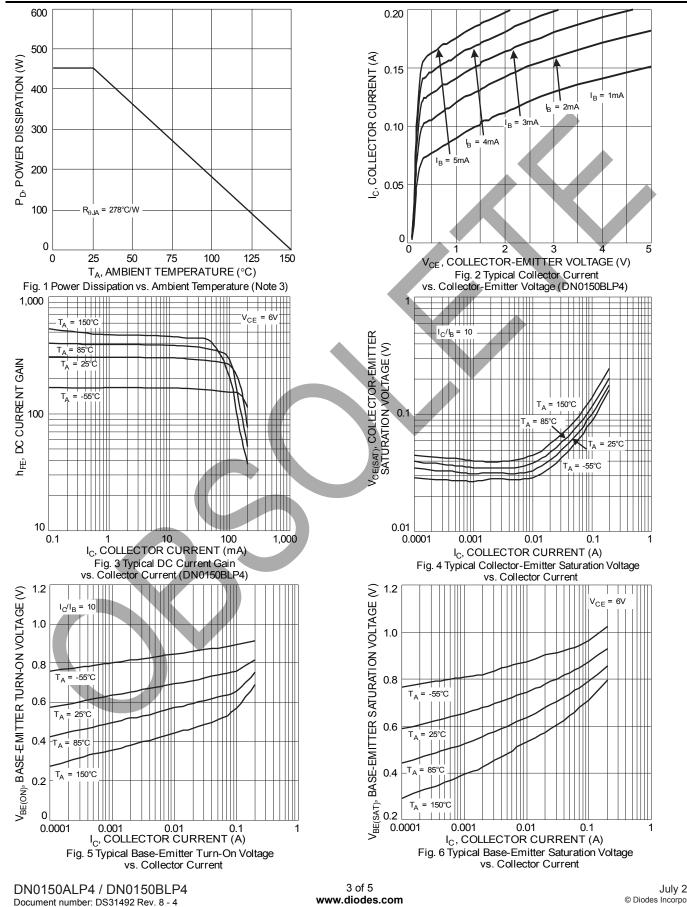
7. Thermal resistance from junction to solder-point (on the exposed collector pad).

8. Refer to JEDEC specification JESD22-A114 and JESD22-A115.

9. Measured under pulsed conditions. Pulse width \leq 300µs. Duty cycle \leq 2%.

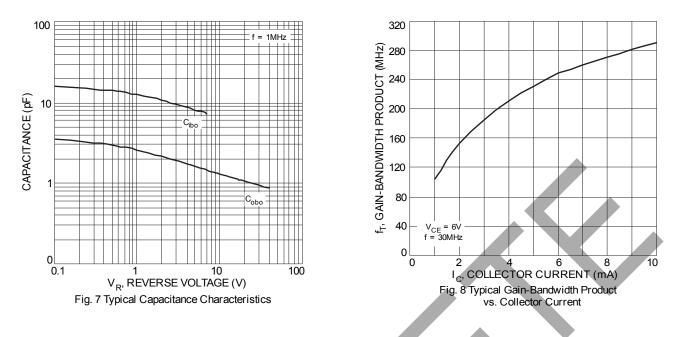


Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)



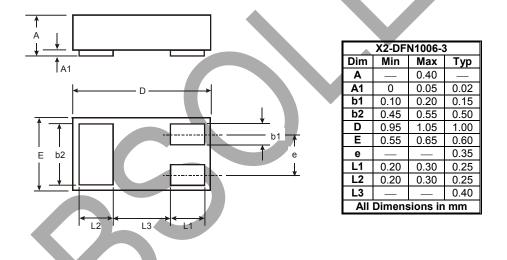


DN0150ALP4 / DN0150BLP4



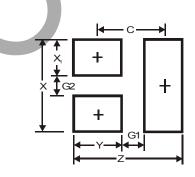
Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.



Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	1.1
G1	0.3
G2	0.2
Х	0.7
X1	0.25
Y	0.4
С	0.7



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