

Maximum Ratings (@ $T_A = +25$ °C, unless otherwise specified.)

Characteristic	Symbol	Value	Units		
Drain-Source Voltage	V _{DSS}	30	V		
Gate-Source Voltage			V _{GSS}	±20	V
Continuous Drain Current (Note 6) V _{GS} = 10V	Steady State	T _A = +25°C T _A = +70°C	I _D	10.8 8.5	А
	t<10s	T _A = +25°C T _A = +70°C	I _D	15.5 12.3	А
Operiting the Desire Operant (National) Vision ASV	Steady State	T _A = +25°C T _A = +70°C	I _D	9.5 7.5	А
Continuous Drain Current (Note 6) V _{GS} = 4.5V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I _D	13.5 10.8	А
Pulsed Drain Current (10µs pulse, duty cycle = 1%)			I _{DM}	70	Α
Avalanche Current (Note 7) L = 0.1mH			I _{AS}	22	Α
Avalanche Energy (Note 7) L = 0.1mH			E _{AS}	24	mJ

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic		Symbol	Value	Units
Total Power Dissipation (Note 5)		P_{D}	1.18	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	D	109	°C/W
Thermal Resistance, Junction to Ambient (Note 3)	t<10s	$R_{\theta JA}$	49	°C/W
Total Power Dissipation (Note 6)		P_{D}	2.75	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	D	46	°C/W
Thermal Nesistance, Junction to Ambient (Note 0)	t<10s	$R_{\theta JA}$	24	°C/W
Thermal Resistance, Junction to Case (Note 6)		R ₀ JC	4.5	°C/W
Operating and Storage Temperature Range		$T_{J_1}T_{STG}$	-55 to +150	°C

Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 8)							
Drain-Source Breakdown Voltage	BV _{DSS}	30	ı	-	V	$V_{GS} = 0V, I_D = 250\mu A$	
Zero Gate Voltage Drain Current	I _{DSS}	-	=	1	μA	$V_{DS} = 30V, V_{GS} = 0V$	
Gate-Source Leakage	I _{GSS}	-	-	±100	nA	$V_{GS} = \pm 20V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 8)							
Gate Threshold Voltage	V _{GS(th)}	1.4	-	2.0	V	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	
Static Drain-Source On-Resistance		-	8.5	12	mΩ	$V_{GS} = 10V, I_D = 20A$	
Static Drain-Source On-Resistance	R _{DS} (ON)	-	10.5	16		$V_{GS} = 4.5V, I_D = 20A$	
Diode Forward Voltage	V_{SD}	-	0.7	1.0	V	$V_{GS} = 0V$, $I_S = 1A$	
DYNAMIC CHARACTERISTICS (Note 9)							
Input Capacitance	C _{iss}	-	1415	-	рF		
Output Capacitance	Coss	-	119	-	pF	$V_{DS} = 15V, V_{GS} = 0V,$ - f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	82	-	pF	71 = 1.0ivin2	
Gate Resistance	R_g	-	3.0	-	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V _{GS} = 4.5V)	Qg	-	11.3	-	nC		
Total Gate Charge (V _{GS} = 10V)	Qg	-	25.1	-	nC	\/ 15\/ 120	
Gate-Source Charge	Q_{gs}	-	3.5	-	nC	$V_{DS} = 15V, I_D = 12A$	
Gate-Drain Charge	Q_{gd}	-	3.6	-	nC]	
Turn-On Delay Time	t _{D(ON)}	-	4.8	-	ns		
Turn-On Rise Time	t _R	-	16.5	-	ns	$V_{DD} = 15V, V_{GS} = 10V,$ $R_{L} = 1.25\Omega, R_{G} = 3\Omega$	
Turn-Off Delay Time	t _{D(OFF)}	-	26.1	-	ns		
Turn-Off Fall Time	t _F	-	5.6	-	ns		
Reverse Recovery Time	t _{RR}	-	12.3	-	ns	I _F = 12A, di/dt = 500A/µs	
Reverse Recovery Charge	Q _{rr}	-	10.4	-	nC		

5. Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.

6. Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.

7. I_{AS} and E_{AS} rating are based on low frequency and duty cycles to keep $T_J = +25 ^{\circ} C$.

8. Short duration pulse test used to minimize self-heating effect.

9. Guaranteed by design. Not subject to product testing.

= 25°C

-55°C

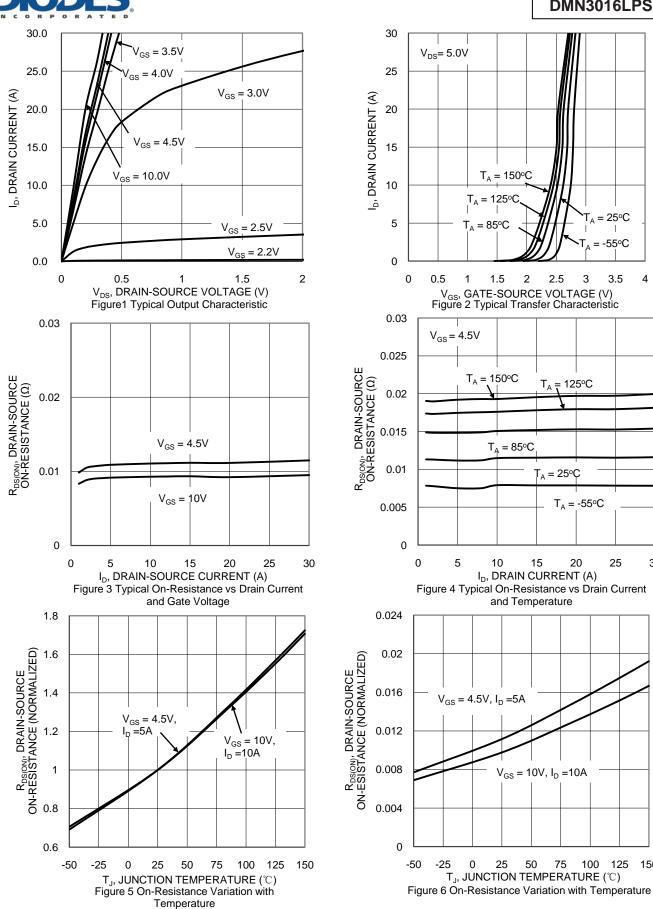
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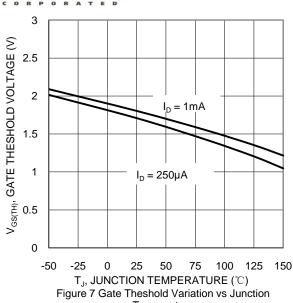
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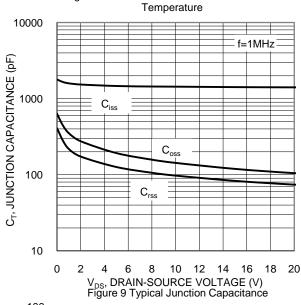
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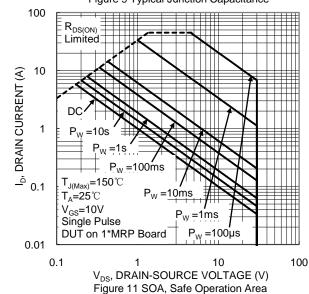
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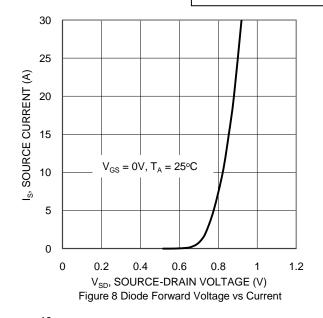


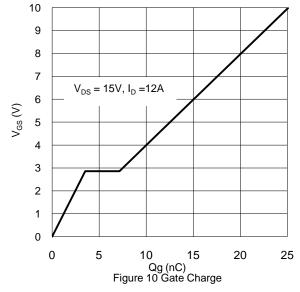
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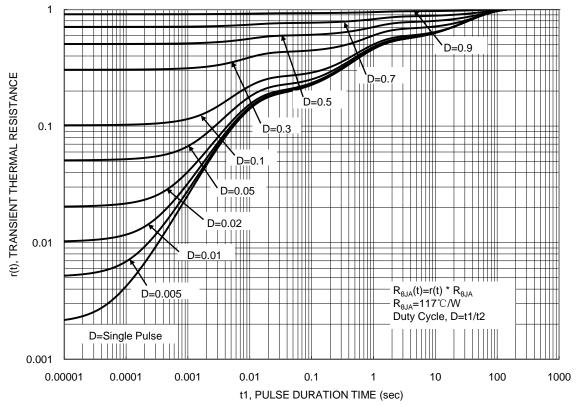


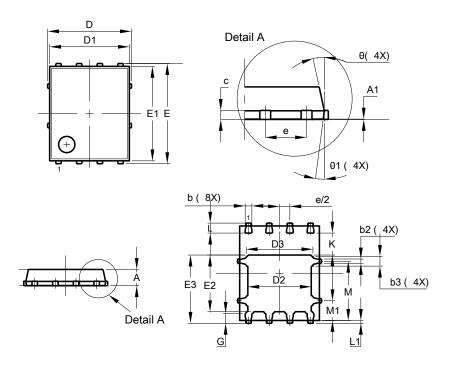
Figure 12 Transient Thermal Resistance



Package Outline Dimensions

Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for the latest version.

(1) Package Type: POWERDI®5060-8

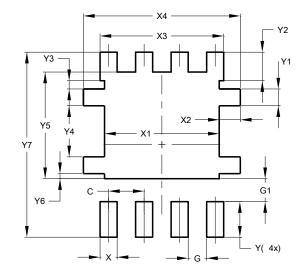


POWERDI [®] 5060-8						
Dim	Min	Max	Тур			
Α	0.90	1.10	1.00			
A1	0.00	0.05				
b	0.33	0.51	0.41			
b2	0.200	0.350	0.273			
b3	0.40	0.80	0.60			
С	0.230	0.330	0.277			
D	5.15 BSC					
D1	4.70	5.10	4.90			
D2	3.70	4.10	3.90			
D3	3.90	4.30	4.10			
Е	6.15 BSC					
E1	5.60	6.00	5.80			
E2	3.28	3.68	3.48			
E3	3.99	4.39	4.19			
е	1.27 BSC					
G	0.51	0.71	0.61			
K	0.51					
L	0.51	0.71	0.61			
L1	0.100	0.200	0.175			
М	3.235	4.035	3.635			
M1	1.00	1.40	1.21			
Θ	10°	12º	11º			
Θ1	6º	80	7º			
All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.

(1) Package Type: POWERDI®5060-8



C 1.270 G 0.660 G1 0.820 X 0.610 X1 4.100 X2 0.755 X3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180 Y7 6.610	Dimensions	Value (in mm)
G1 0.820 X 0.610 X1 4.100 X2 0.755 X3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	С	1.270
X 0.610 X1 4.100 X2 0.755 X3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	G	0.660
X1 4.100 X2 0.755 X3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	G1	0.820
X2 0.755 X3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	X	0.610
X3 4.420 X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	X1	4.100
X4 5.610 Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	X2	0.755
Y 1.270 Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	Х3	4.420
Y1 0.600 Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	X4	5.610
Y2 1.020 Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	Υ	1.270
Y3 0.295 Y4 1.825 Y5 3.810 Y6 0.180	Y1	0.600
Y4 1.825 Y5 3.810 Y6 0.180	Y2	1.020
Y5 3.810 Y6 0.180	Y3	0.295
Y6 0.180	Y4	1.825
	Y5	3.810
Y7 6.610	Y6	0.180
5.5.5	Y7	6.610



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