

#### NOT RECOMMENDED FOR NEW DESIGN USE DMP3018SSS

**DMG4407SSS** 

## **Maximum Ratings** (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic			Symbol	Value	Unit
Drain-Source Voltage			$V_{DSS}$	-30	V
Gate-Source Voltage			V <sub>GSS</sub>	±25	V
Continuous Pusin Courset (Alata C) V 2001	Steady State	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-9.9 -7.9	А
Continuous Drain Current (Note 6) V <sub>GS</sub> = -20V	t<10s	$T_A = +25^{\circ}C$ $T_A = +70^{\circ}C$	I <sub>D</sub>	-12.5 -10.0	Α
Continuous Prain Current (Note 6) // 61/	Steady State	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-8.2 -6.5	Α
Continuous Drain Current (Note 6) V <sub>GS</sub> = -6V	t<10s	$T_A = +25$ °C $T_A = +70$ °C	I <sub>D</sub>	-11.0 -8.7	Α
Maximum Continuous Body Diode Forward Current (Note 6)			I <sub>S</sub>	-3.0	Α
Pulsed Drain Current (10µs Pulse, Duty Cycle = 1%)			I <sub>DM</sub>	-80	Α

# Thermal Characteristics ( $@T_A = +25^{\circ}C$ , unless otherwise specified.)

Characteristic		Symbol	Value	Unit
Total Power Dissipation (Note 5)		PD	1.45	W
Thermal Resistance, Junction to Ambient (Note 5)	Steady State	Paus	88	°C/W
Thermal Resistance, Junction to Ambient (Note 5)	t<10s	Reja	50	°C/W
Total Power Dissipation (Note 6)		$P_{D}$	1.82	W
Thermal Resistance, Junction to Ambient (Note 6)	Steady State	R <sub>0JA</sub>	70	°C/W
Thermal Resistance, Junction to Ambient (Note o)	t<10s	Көда	41	°C/W
Thermal Resistance, Junction to Case (Note 6)		$R_{\theta JC}$	7.6	°C/W
Operating and Storage Temperature Range		TJ, T <sub>STG</sub>	-50 to +155	°C

# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition	
OFF CHARACTERISTICS (Note 7)							
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	-30		_	V	$V_{GS} = 0V, I_D = -250\mu A$	
Zero Gate Voltage Drain Current	IDSS	7	_	-1	μΑ	$V_{DS} = -30V, V_{GS} = 0V$	
Gate-Source Leakage	I <sub>GSS</sub>		_	±10	μΑ	$V_{GS} = \pm 25V, V_{DS} = 0V$	
ON CHARACTERISTICS (Note 7)							
Gate Threshold Voltage	$V_{GS(TH)}$	-1.7		-3.0	V	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	
		I	9	11		$V_{GS} = -20V, I_D = -12A$	
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	-	10	13	mΩ	$V_{GS} = -10V, I_{D} = -10A$	
		_	12.7	17		$V_{GS} = -6V, I_D = -10A$	
Forward Transfer Admittance	Y <sub>fs</sub>	_	21	_	S	$V_{DS} = -5V, I_{D} = -10A$	
Diode Forward Voltage	V <sub>SD</sub>	_	-0.7	-1.0	V	V <sub>GS</sub> = 0V, I <sub>S</sub> = -1A	
DYNAMIC CHARACTERISTICS (Note 8)	•						
Input Capacitance	Ciss		2246	_	pF	15)/ )/	
Output Capacitance	Coss	I	352	_	pF	$V_{DS} = -15V, V_{GS} = 0V,$ 1f = 1.0MHz	
Reverse Transfer Capacitance	Crss	-	294	_	pF	71 = 1.010102	
Gate Resistance	$R_g$	-	5.1	_	Ω	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	
Total Gate Charge (V <sub>GS</sub> = -4.5V)	Qg	_	20.5	_	nC	V <sub>GS</sub> = -10V, V <sub>DS</sub> = -15V, I <sub>D</sub> = -12A	
Total Gate Charge (V <sub>GS</sub> = -10V)	Qg	_	41	_	nC		
Gate-Source Charge	Q <sub>gs</sub>	_	7.6	_	nC		
Gate-Drain Charge	$Q_{gd}$	_	8.0	_	nC		
Turn-On Delay Time	t <sub>D(ON)</sub>	_	11.3	_	ns	$V_{DD} = -15V, V_{GS} = -10V,$ $R_L = 1.25\Omega, R_G = 3\Omega$	
Turn-On Rise Time	t <sub>R</sub>	_	15.4	_	ns		
Turn-Off Delay Time	t <sub>D(OFF)</sub>	_	38.0	_	ns		
Turn-Off Fall Time	t <sub>F</sub>	_	22.0	_	ns		

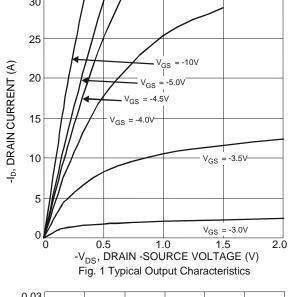
 Device mounted on FR-4 substrate PC board, 2oz copper, with minimum recommended pad layout.
 Device mounted on FR-4 substrate PC board, 2oz copper, with 1inch square copper plate.
 Short duration pulse test used to minimize self-heating effect. Notes:

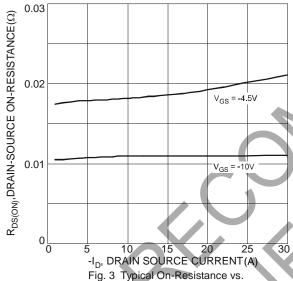
8. Guaranteed by design. Not subject to product testing.



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Drain Current and Gate Voltage

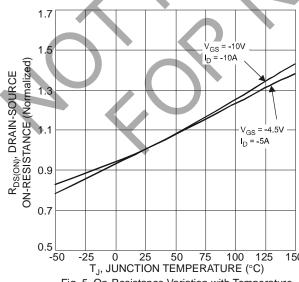
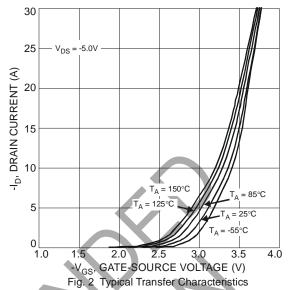
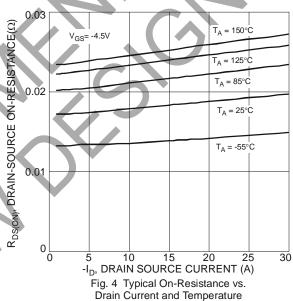
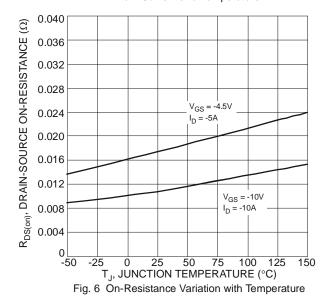


Fig. 5 On-Resistance Variation with Temperature







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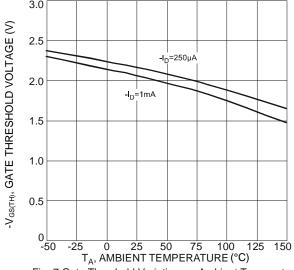
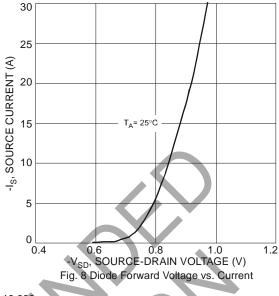
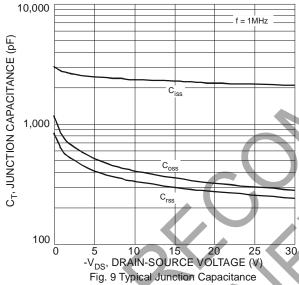
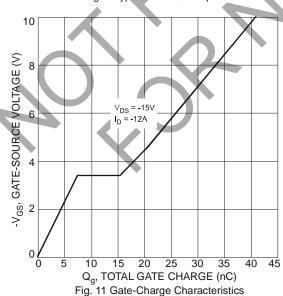


Fig. 7 Gate Threshold Variation vs. Ambient Temperature







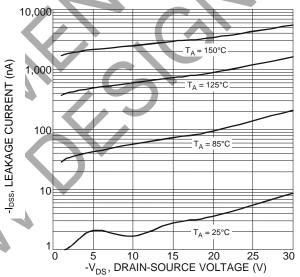
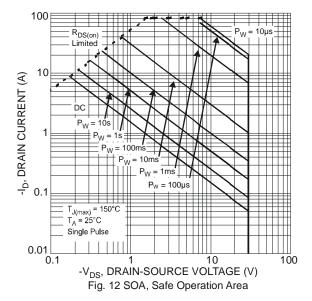
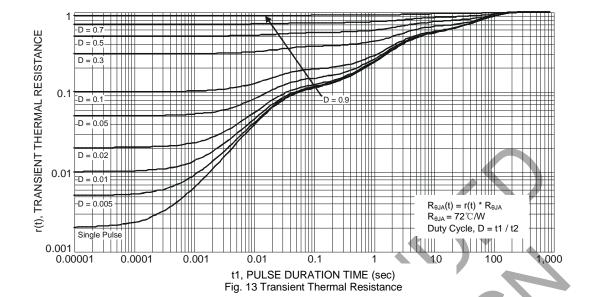


Fig. 10 Typical Drain-Source Leakage Current vs. Voltage





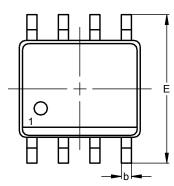


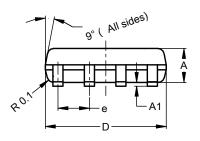


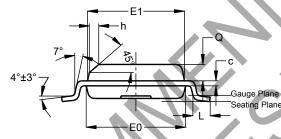
## **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

SO-8





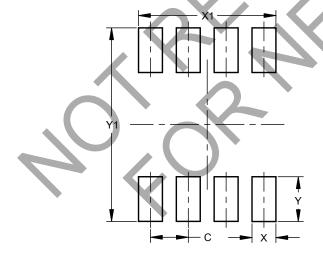


SO-8						
Dim	Min	Max	Тур			
Α	1.40	1.50	1.45			
A1	0.10	0.20	0.15			
b	0.30	0.50	0.40			
С	0.15	0.25	0.20			
D	4.85	4.95	4.90			
Е	5.90	6.10	6.00			
E1	3.80	3.90	3.85			
E0	3.85	3.95	3.90			
е			1.27			
h	6	1	0.35			
L	0.62	0.82	0.72			
Q	0.60	0.70	0.65			
All Dimensions in mm						

### **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

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<b>Dimensions</b>	Value (in mm)		
С	1.27		
Х	0.802		
X1	4.612		
Y	1.505		
Y1	6.50		



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