● Reference Data

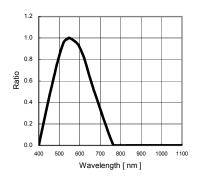


Fig.1 Spectral Response

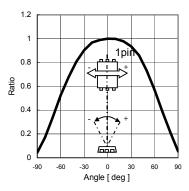


Fig.4 Directional Characteristics 1

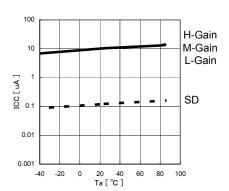


Fig.7 Ta - ICC (0 lx)

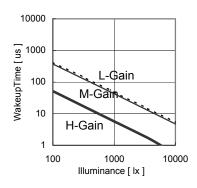


Fig.10 Illuminance - Wake up Time

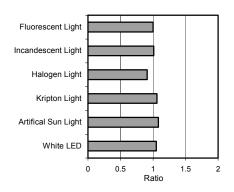


Fig.2 Light Source Dependency (Fluorescent Light is set to '1')

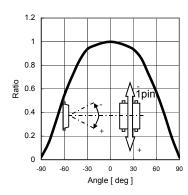


Fig.5 Directional Characteristics 2

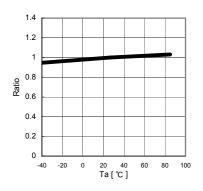


Fig.8 IOUT Temperature dependency (100 lx)

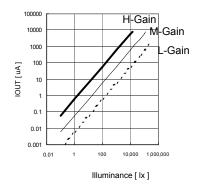


Fig.3 Illuminance – IOUT Characteristics

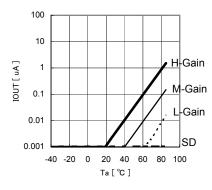


Fig.6 Ta - IOUT (0 lx)

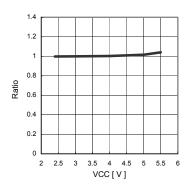
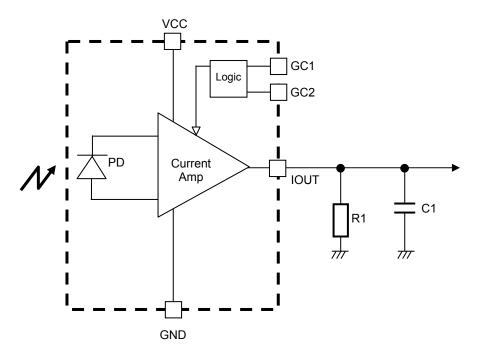


Fig.9 IOUT VCC dependency

●Block Diagram



Block Diagram Descriptions

•PD

Photo diode close to human eyes sensitivity.

●Current AMP

To amplify Photo diode current (H-Gain / M-Gain / L-Gain) Gain controllable in 3 steps by input voltage from GC1 and GC2.

Logic

Logic block for mode setting by input voltage from GC1 and GC2

■Mode Setting

GC2	GC1	Mode	
0	0	Shutdown	
0	1	H-Gain Mode	
1	0	M-Gain Mode	
1	1	L-Gain Mode	

3/8

External parts Setting

1) Gain setting of BH1603FVC

Please select the best gain controlled by 5 and 6pin based on the required illuminance range.

The reference is as follows.

Illuminance detection range [lx]	Gain Mode	
~1,000	H-Gain Mode	
~10,000	M-Gain Mode	
~100,000	L-Gain Mode	

This device will be mounted under the optical window in actual designing. Therefore, there is a possibility that the illuminace to ALS(Ambient Light Sensor) will be less than the illuminance on the final product surface.

Please consider the attenuation of light through the optical window.

Please set output resistance value(R1) within the range of 1 k Ω ~1M Ω which needs to be smaller than the input impedance of the next circuit.

2) Approximate formula of IOUT output voltage in each Gain Mode

(1) H-Gain mode

The output voltage is calculated as below.

Viout= $0.6 \times 10^{-6} \times Ev \times R1$

Viout is IOUT output voltage [V]. Ev is an illuminance of the ALS surface [lx].

R1 is IOUT output resistor[Ω].

(For example) In case you want to convert the illuminance value up to 500 lx by ADC.

If the maximum voltage of ADC input is 2V, output resistor value will be as below.

R1 = Viout/(0.6 x 10⁻⁶ x Ev)
= 2 /(0.6 x 10⁻⁶ x 500) = 6667[
$$\Omega$$
]
 \Rightarrow 6.8[k Ω]

(2) M-Gain mode

The output voltage is calculated as follows. Viout= 0.06 x 10⁻⁶ x Ev x R1

(3) L-Gain mode

The output voltage is calculated as follows.

Viout= 0.006 x 10⁻⁶ x Ev x R1

3) C1

(1) To reject the flicker light..

In case IOUT output is R1 only and an ALS receives the artificial lights such as fluorescent lamps and incandescent lamps synchronized with 50/60 Hz of AC power supplies, the output current has a ripple. If you want to reject this ripple, please add C1 to R1 in parallel. Please set it to C1 x R1 = about 0.1 as a time constant.

(2) To control backlight smoothly by using illuminance value.

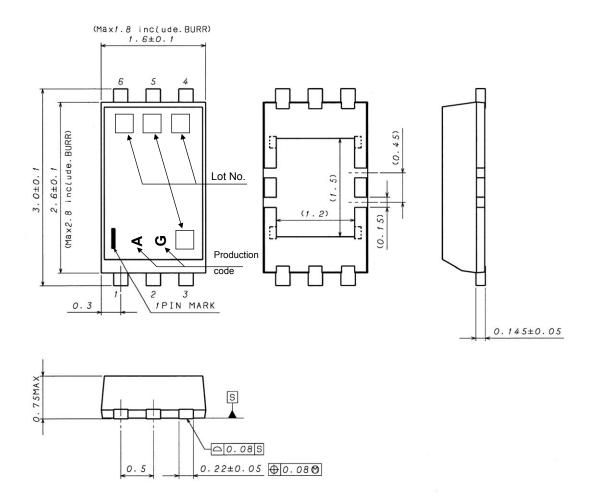
C1 is effective to control backlight smoothly for a rapid changing of the illuminance. In this case, please set it to C1 x R1 = about 1 ~ 10 as a time constant. It is not necessary if you average illuminance value with software to change backlight smoothly.

Please note that the rise time becomes slow at power-on and recovery from shutdown mode to operation mode.

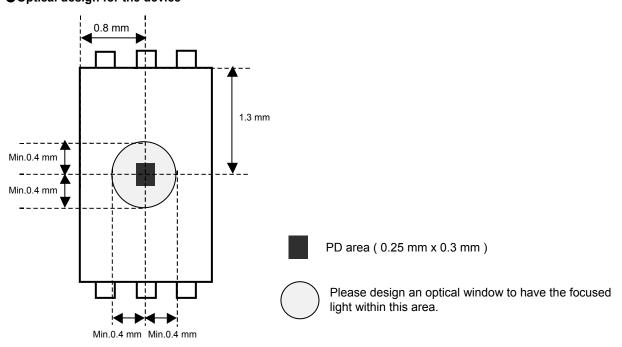
Terminal Descriptions

erminal Descriptions					
Pin No.	Pin Name	Equivalent Circuit	Function		
1	IOUT	VCC VCC	This terminal outputs current depending on illuminance level. Use this pin by putting resistor between GND.		
2	GND		GND Terminal		
3	VCC		Power Supply Terminal		
4	NC		NC(Non connection)Terminal Open or short to GND		
5	GC1	vcc ///////////////////////////////////	Mode Setting Terminal 1		
6	GC2	VCCC W-VCC	Mode Setting Terminal 2		

●Package Outlines



Optical design for the device



●Notes for use

1) Absolute Maximum Ratings

An excess in the absolute maximum ratings, such as supply voltage (Vmax), temperature range of operating conditions (Topr), etc., can break down devices, thus making impossible to identify breaking mode such as a short circuit or an open circuit. If any special mode exceeding the absolute maximum ratings is assumed, consideration should be given to take physical safety measures including the use of fuses, etc.

2) GND voltage

Make setting of the potential of the GND terminal so that it will be maintained at the minimum in any operating state. Furthermore, check to be sure no terminals are at a potential lower than the GND voltage including an actual electric transient.

3) Short circuit between terminals and erroneous mounting

In order to mount ICs on a set PCB, pay thorough attention to the direction and offset of the ICs. Erroneous mounting can break down the ICs. Furthermore, if a short circuit occurs due to foreign matters entering between terminals or between the terminal and the power supply or the GND terminal, the ICs can break down.

4) Operation in strong electromagnetic field

Be noted that using ICs in the strong electromagnetic field can malfunction them.

5) Inspection with set PCB

On the inspection with the set PCB, if a capacitor is connected to a low-impedance IC terminal, the IC can suffer stress. Therefore, be sure to discharge from the set PCB by each process. Furthermore, in order to mount or dismount the set PCB to/from the jig for the inspection process, be sure to turn OFF the power supply and then mount the set PCB to the jig. After the completion of the inspection, be sure to turn OFF the power supply and then dismount it from the jig. In addition, for protection against static electricity, establish a ground for the assembly process and pay thorough attention to the transportation and the storage of the set PCB.

6) Input terminals

In terms of the construction of IC, parasitic elements are inevitably formed in relation to potential. The operation of the parasitic element can cause interference with circuit operation, thus resulting in a malfunction and then breakdown of the input terminal. Therefore, pay thorough attention not to handle the input terminals; such as to apply to the input terminals a voltage lower than the GND respectively, so that any parasitic element will operate. Furthermore, do not apply a voltage to the input terminals when no power supply voltage is applied to the IC. In addition, even if the power supply voltage is applied, apply to the input terminals a voltage lower than the power supply voltage or within the guaranteed value of electrical characteristics.

7) Thermal design

Perform thermal design in which there are adequate margins by taking into account the permissible dissipation (pd) in actual states of use.

8) Treatment of package

Dusts or scratch on the photo detector may affect the optical characteristics. Please handle it with care.

9) Rush current

When power is first supplied to this IC, rush current may flow instantaneously. Because it is possible that the charge current to the parasitic capacitance of internal photo diode or the internal logic may be unstable. Therefore, give special consideration to power coupling capacitance, power wiring, width of GND wiring, and routing of connections.

10) The exposed central pad on the back side of the package

There is an exposed central pad on the back side of the package. But please do it non connection.

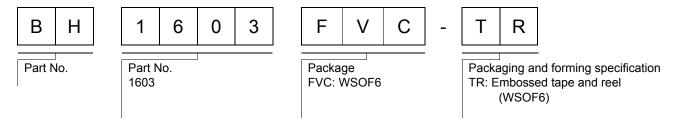
(Don't solder, and don't do electrical connection)

Please mount by Footprint dimensions described in the Jisso Information for WSOF6.

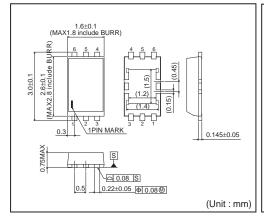
This pad is GND level, therefore there is a possibility that LSI malfunctions and heavy-current is generated.

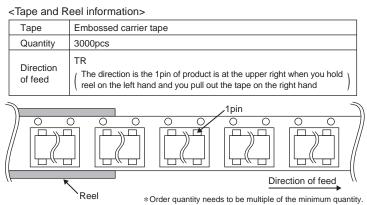
Downloaded from Arrow.com.

Ordering part number



WSOF6





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JÁPAN	USA	EU	CHINA
CLASSⅢ	CLACCIII	CLASS II b	CL ACCIII
CLASSIV	CLASSⅢ	CLASSⅢ	CLASSⅢ

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 - [b] Use of our Products outdoors or in places where the Products are exposed to direct sunlight or dust
 - [c] Use of our Products in places where the Products are exposed to sea wind or corrosive gases, including Cl₂, H₂S, NH₃, SO₂, and NO₂
 - [d] Use of our Products in places where the Products are exposed to static electricity or electromagnetic waves
 - [e] Use of our Products in proximity to heat-producing components, plastic cords, or other flammable items
 - [f] Sealing or coating our Products with resin or other coating materials
 - [g] Use of our Products without cleaning residue of flux (even if you use no-clean type fluxes, cleaning residue of flux is recommended); or Washing our Products by using water or water-soluble cleaning agents for cleaning residue after soldering
 - [h] Use of the Products in places subject to dew condensation
- 4. The Products are not subject to radiation-proof design.
- 5. Please verify and confirm characteristics of the final or mounted products in using the Products.
- 6. In particular, if a transient load (a large amount of load applied in a short period of time, such as pulse. is applied, confirmation of performance characteristics after on-board mounting is strongly recommended. Avoid applying power exceeding normal rated power; exceeding the power rating under steady-state loading condition may negatively affect product performance and reliability.
- 7. De-rate Power Dissipation (Pd) depending on Ambient temperature (Ta). When used in sealed area, confirm the actual ambient temperature.
- 8. Confirm that operation temperature is within the specified range described in the product specification.
- 9. ROHM shall not be in any way responsible or liable for failure induced under deviant condition from what is defined in this document.

Precaution for Mounting / Circuit board design

- 1. When a highly active halogenous (chlorine, bromine, etc.) flux is used, the residue of flux may negatively affect product performance and reliability.
- 2. In principle, the reflow soldering method must be used; if flow soldering method is preferred, please consult with the ROHM representative in advance.

For details, please refer to ROHM Mounting specification

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This Product is electrostatic sensitive product, which may be damaged due to electrostatic discharge. Please take proper caution in your manufacturing process and storage so that voltage exceeding the Products maximum rating will not be applied to Products. Please take special care under dry condition (e.g. Grounding of human body / equipment / solder iron, isolation from charged objects, setting of lonizer, friction prevention and temperature / humidity control).

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- 1. Product performance and soldered connections may deteriorate if the Products are stored in the places where:
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 - [b] the temperature or humidity exceeds those recommended by ROHM
 - the Products are exposed to direct sunshine or condensation
 - [d] the Products are exposed to high Electrostatic
- 2. Even under ROHM recommended storage condition, solderability of products out of recommended storage time period may be degraded. It is strongly recommended to confirm solderability before using Products of which storage time is exceeding the recommended storage time period.
- 3. Store / transport cartons in the correct direction, which is indicated on a carton with a symbol. Otherwise bent leads may occur due to excessive stress applied when dropping of a carton.
- Use Products within the specified time after opening a humidity barrier bag. Baking is required before using Products of which storage time is exceeding the recommended storage time period.

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bh1603fvc - Web Page

Distribution Inventory

Part Number	bh1603fvc
Package	WSOF6
Unit Quantity	3000
Minimum Package Quantity	3000
Packing Type	Taping
Constitution Materials List	inquiry
RoHS	Yes