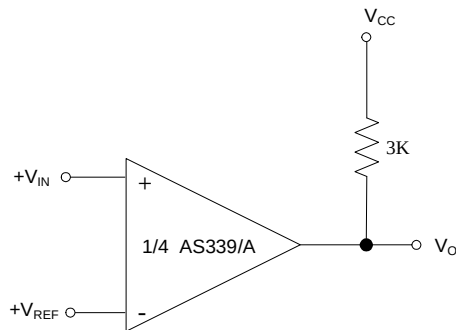
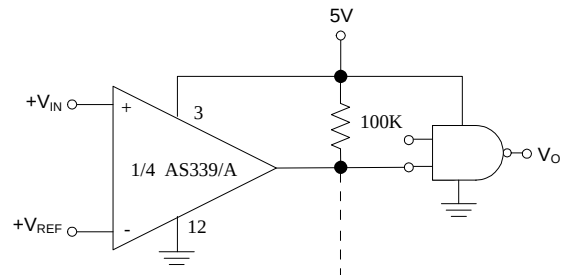


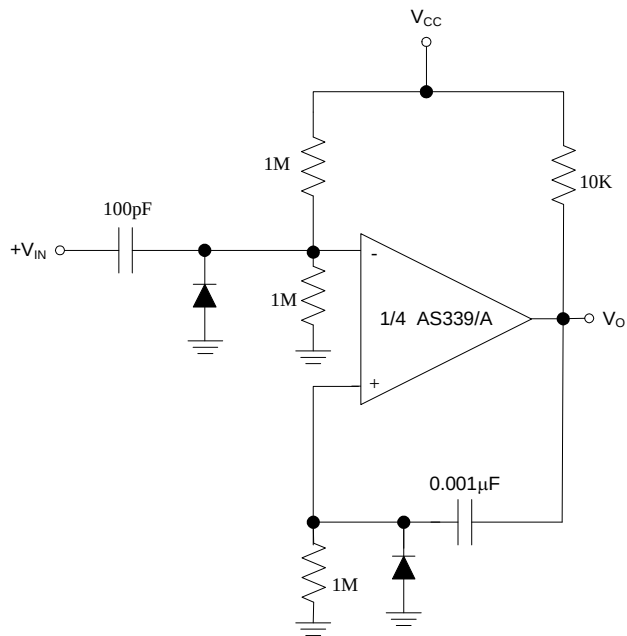
Typical Applications Circuit



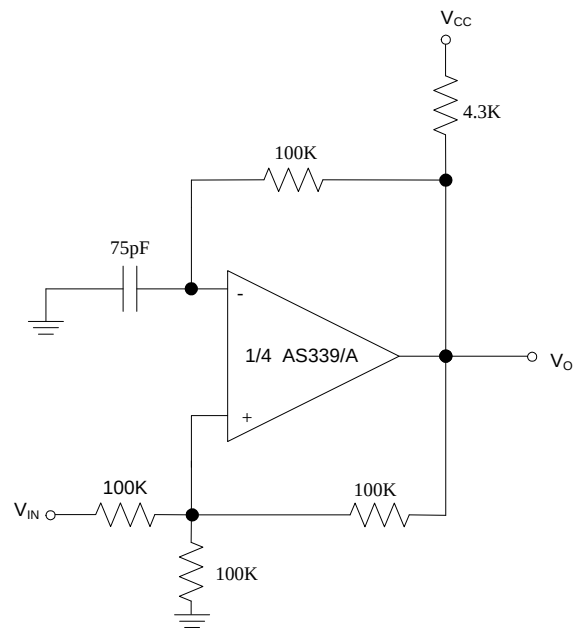
Basic Comparator



Driving CMOS

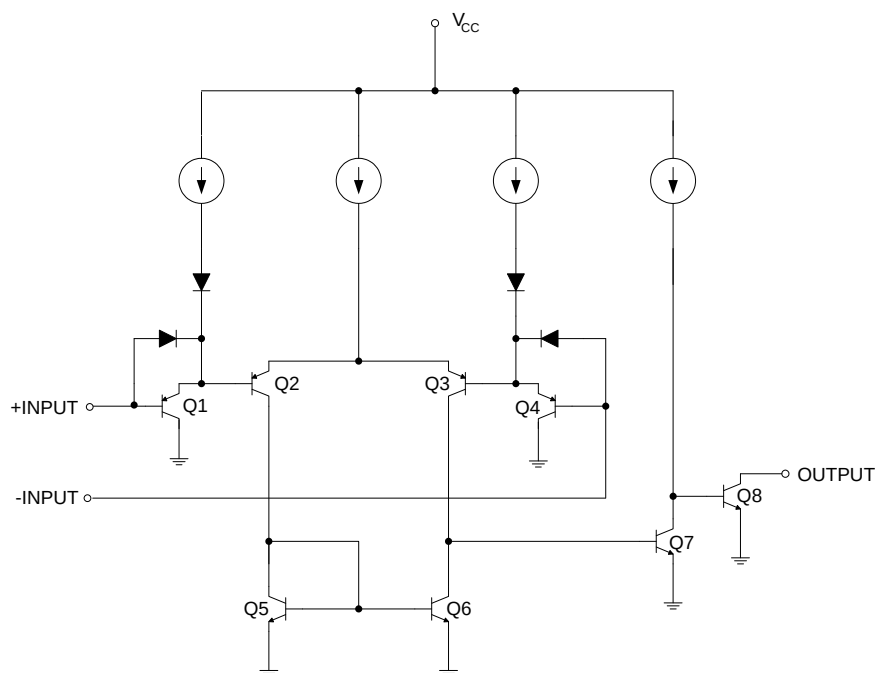


One Shot Multivibrator



Squarewave Oscillator

Functional Block Diagram



Absolute Maximum Ratings (Note 4)

Symbol	Parameter	Rating		Unit
V_{CC}	Supply Voltage	40		V
V_{ID}	Differential Input Voltage	40		V
V_{IN}	Input Voltage	-0.3 to 40		V
I_{IN}	Input Current ($V_{IN} < -0.3V$) (Note 5)	50		mA
—	Output Short-Circuit to Ground	Continuous		—
P_D	Power Dissipation ($T_A = +25^\circ C$)	SO-14	890	mW
		TSSOP-14	790	
T_J	Operating Junction Temperature	+150		$^\circ C$
T_{STG}	Storage Temperature Range	-65 to +150		$^\circ C$
T_{LEAD}	Lead Temperature (Soldering, 10 Seconds)	+260		$^\circ C$

Notes: 4. Stresses greater than those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "Recommended Operating Conditions" is not implied. Exposure to "Absolute Maximum Ratings" for extended periods may affect device reliability.

5. This input current will only exist when the voltage at any of the input leads is driven negative. It is due to the collector-base junction of the input PNP transistors becoming forward biased and thereby acting as input diode clamps. In addition to this diode action, there is also lateral NPN parasitic transistor action on the IC chip. This transistor action can cause the output voltages of the comparators to go to the $V+$ voltage level (or to ground for a large overdrive) for the time duration that an input is driven negative. This is not destructive and normal output states will re-establish when the input voltage, which was negative, again returns to a value greater than $-0.3 V_{DC}$ (at $+25^\circ C$).

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	Supply Voltage	2	36	V
T_A	Operating Temperature Range	-40	+85	°C

Electrical Characteristics (Limits in standard typeface are for $T_A = +25^{\circ}\text{C}$, **bold** typeface applies over $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$ (Note 6), $V_{CC} = 5\text{V}$, $\text{GND} = 0\text{V}$, unless otherwise specified.)

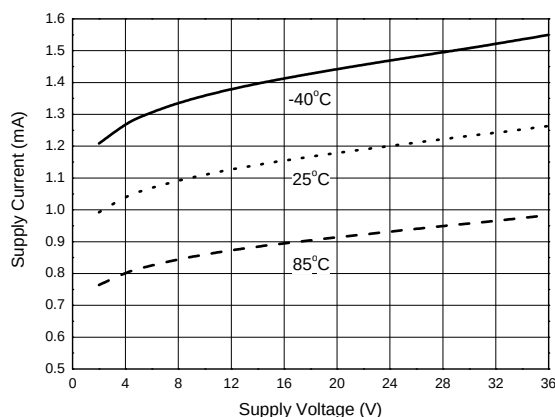
Parameter	Conditions		Min	Typ	Max	Unit
Input Offset Voltage	$V_O = 1.4V$, $R_S = 0\Omega$, V_{CC} from 5V to 30V	AS339	—	2	5	mV
			—	—	7	
		AS339A	—	2	3	
			—	—	5	
Input Bias Current	I_{IN+} or I_{IN-} with output in Linear Range, $V_{CM} = 0V$		—	25	250	nA
			—	—	400	
Input Offset Current	$I_{IN+} - I_{IN-}$, $V_{CM} = 0V$		—	5.0	50	nA
			—	—	200	
Input Common Mode Voltage Range (Note 7)	$V_{CC} = 30V$		0	—	$V_{CC}-1.5$	V
Supply Current	$R_L = \infty$	$V_{CC} = 5V$	—	0.9	2.0	mA
			—	—	3.0	
		$V_{CC} = 30V$	—	1.2	2.5	
			—	—	3.5	
Voltage Gain	$R_L \geq 15k\Omega$, $V_{CC} = 15V$, $V_O = 1V$ to 11V		50	200	—	V/mV
Large Signal Response Time	$V_{IN} = \text{TTL Logic Swing}$, $V_{REF} = 1.4V$ $V_{RL} = 5V$, $R_L = 5.1k\Omega$		—	200	—	ns
Response Time	$V_{RL} = 5V$, $R_L = 5.1k\Omega$		—	1.3	—	μs
Output Sink Current	$V_{IN-} = 1V$, $V_{IN+} = 0$, $V_O = 1.5V$		6.0	16	—	mA
Output Leakage Current	$V_{IN-} = 0V$, $V_{IN+} = 1V$, $V_O = 5V$		—	0.1	—	nA
	$V_{IN-} = 0V$, $V_{IN+} = 1V$, $V_O = 30V$		—	—	1	μA
Saturation Voltage	$V_{IN-} = 1V$, $V_{IN+} = 0$, $I_{SINK} \leq 4mA$		—	200	400	mV
			—	—	500	
Thermal Resistance (Junction to Case)	SO-14		—	15	—	$^{\circ}C/W$
	TSSOP-14		—	6	—	
Thermal Resistance (Junction to Ambient)	SO-14		—	89	—	
	TSSOP-14		—	125	—	

Notes: 6. Limits over the full temperature are guaranteed by design, but not tested in production.

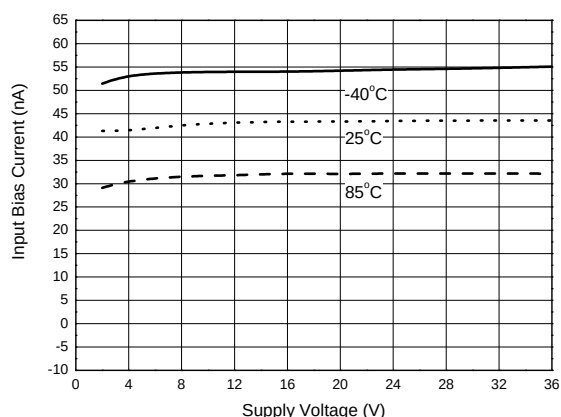
7. The input common-mode voltage of either input signal voltage should not be allowed to go negatively by more than 0.3V (at $+25^{\circ}\text{C}$). The upper end of the common-mode voltage range is $V_{CC}-1.5\text{V}$ (at $+25^{\circ}\text{C}$), but either or both inputs can go to $+36\text{V}$ without damages, independent of the magnitude of the V_{CC} .

Performance Characteristics

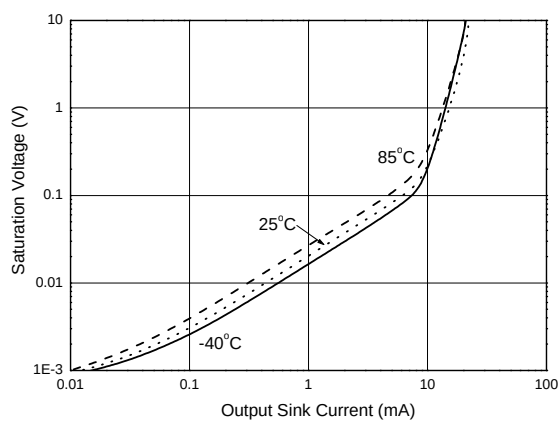
Supply Voltage vs. Supply Current



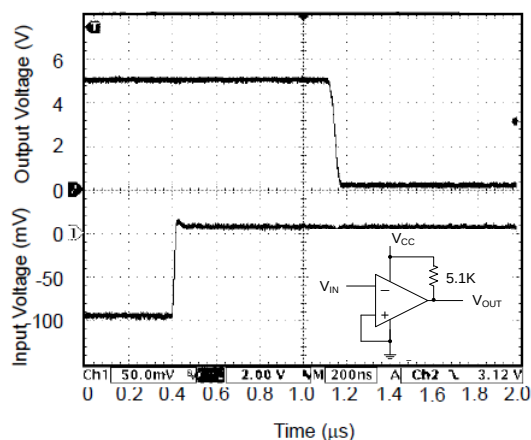
Supply Voltage vs. Input Bias Current



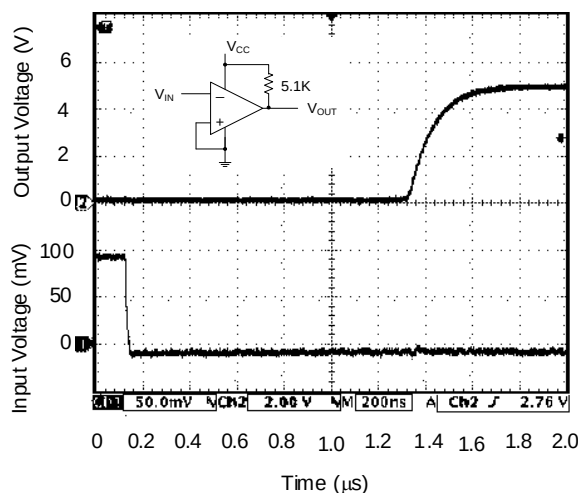
Output Sink Current vs. Saturation Voltage



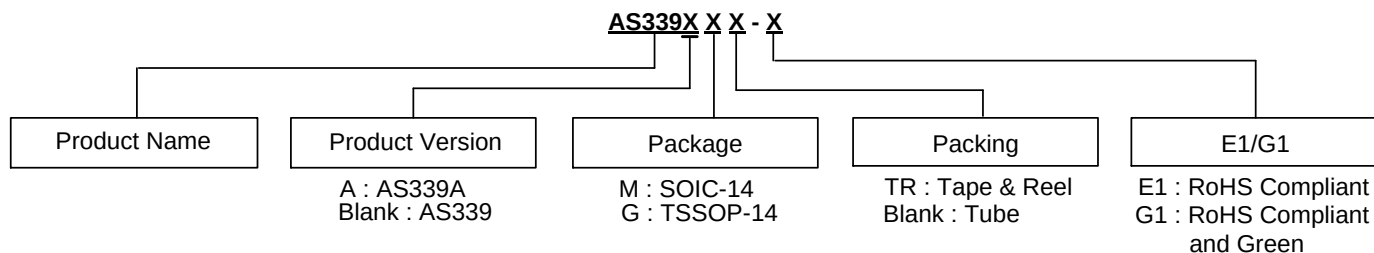
Response Time for 5mV Input Overdrive - Negative Transition



Response Time for 5mV Input Overdrive - Positive Transition



Ordering Information

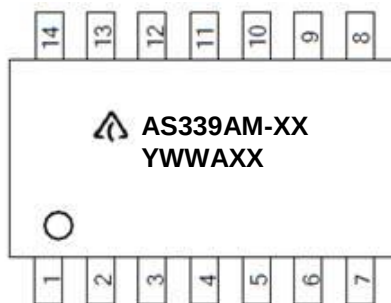
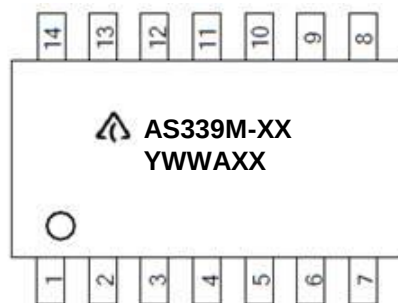


	Part Number	Package (Note 9)	RoHS Compliant Lead Free / Green	Marking ID	Packing	Quantity	Status (Note 8)	Alternative
 Lead-Free	AS339M-E1	SO-14	Lead Free	AS339M-E1	Tube	NA	End of Life	AS339MTR-G1
 Lead-Free	AS339MTR-E1	SO-14	Lead Free	AS339M-E1	Tape & Reel	2500	NRND	AS339MTR-G1
 Lead-Free	AS339AM-E1	SO-14	Lead Free	AS339AM-E1	Tube	NA	End of Life	AS339AMTR-G1
 Lead-Free	AS339AMTR-E1	SO-14	Lead Free	AS339AM-E1	Tape & Reel	2500	NRND	AS339AMTR-G1
 Lead-Free Green	AS339M-G1	SO-14	Green	AS339M-G1	Tube	NA	End of Life	AS339MTR-G1
 Lead-Free Green	AS339MTR-G1	SO-14	Green	AS339M-G1	Tape & Reel	2500	In Production	—
 Lead-Free Green	AS339AM-G1	SO-14	Green	AS339AM-G1	Tube	NA	End of Life	AS339AMTR-G1
 Lead-Free Green	AS339AMTR-G1	SO-14	Green	AS339AM-G1	Tape & Reel	2500	In Production	—
 Lead-Free	AS339GTR-E1	TSSOP-14	Lead Free	EGS339	Tape & Reel	2500	NRND	AS339GTR-G1
 Lead-Free Green	AS339GTR-G1	TSSOP-14	Green	GG339	Tape & Reel	2500	In Production	—

- Notes:
8. All variants in Tube packing with package SO-14 are End of Life.
All variants with package DIP-14 are End of Life without replacements.
NRND: Not Recommended for New Design.
 9. For packaging details, go to our website at: <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

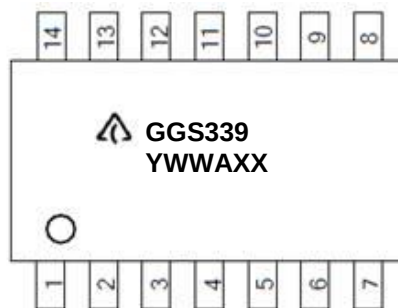
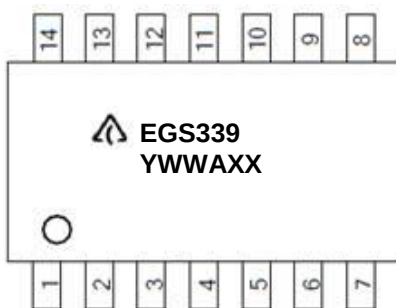
Marking information

(1) SO-14



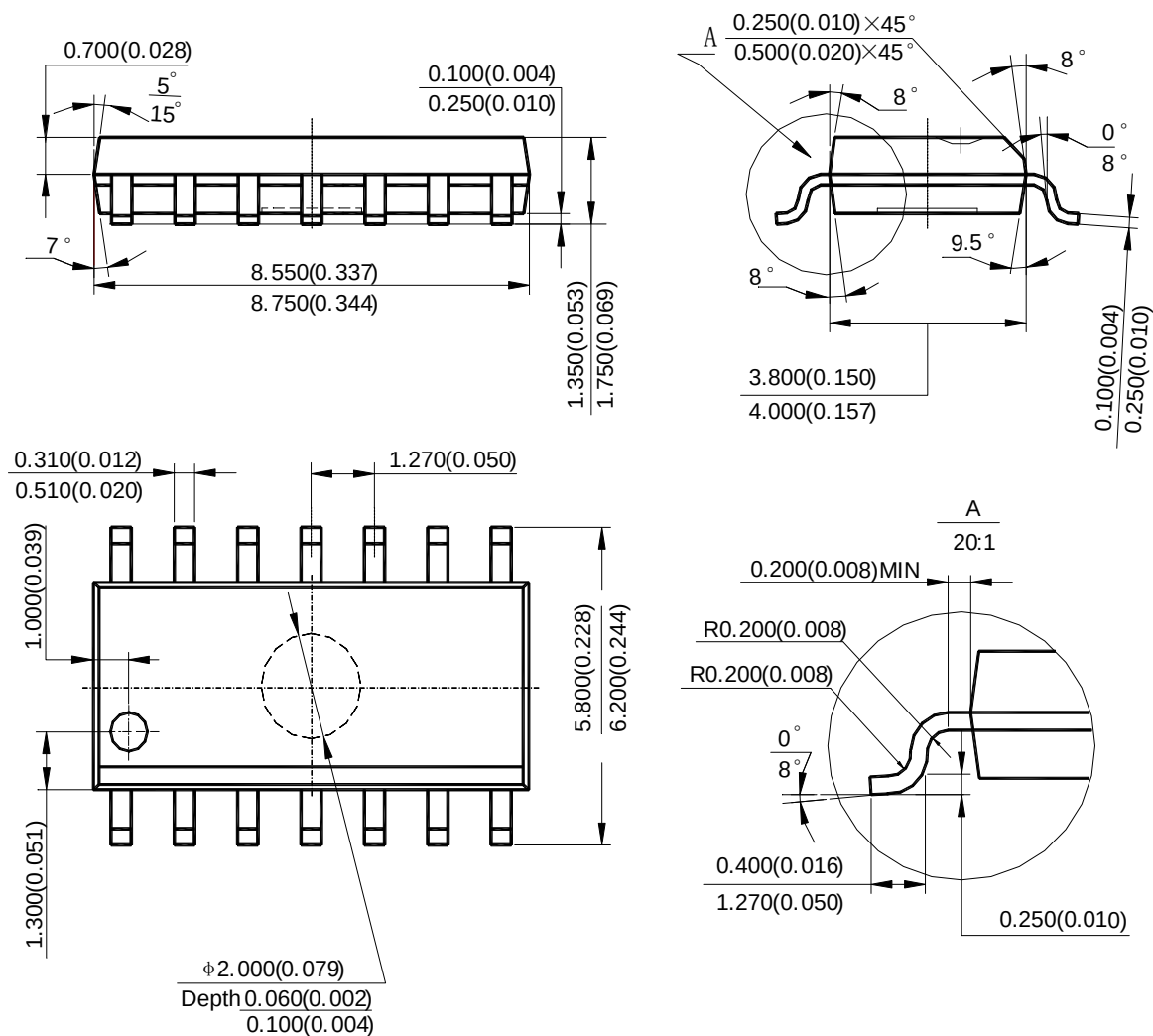
First Line: Logo and Marking ID
(See Ordering Information)
Second Line: Date Code
Y: Year
WW: Work Week of Molding
A: Assembly House Code
XX: 7th and 8th Digits of Batch Number

(2) TSSOP14



First Line: Logo and Marking ID
(See Ordering Information)
Second Line: Date Code
Y: Year
WW: Work Week of Molding
A: Assembly House Code
XX: 7th and 8th Digits of Batch Number

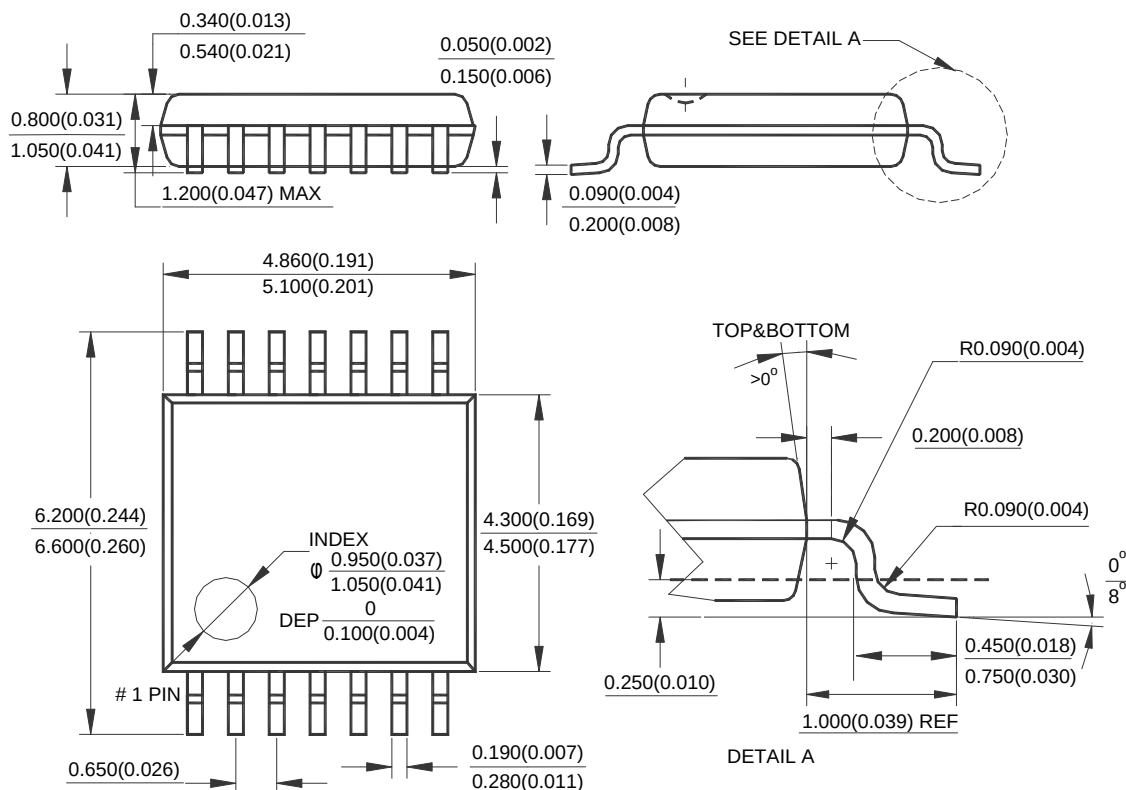
(1) Package Type: SO-14



Note: Eject hole, oriented hole and mold mark is optional.

Package Outline Dimensions (Cont. All dimensions in mm(inch).)

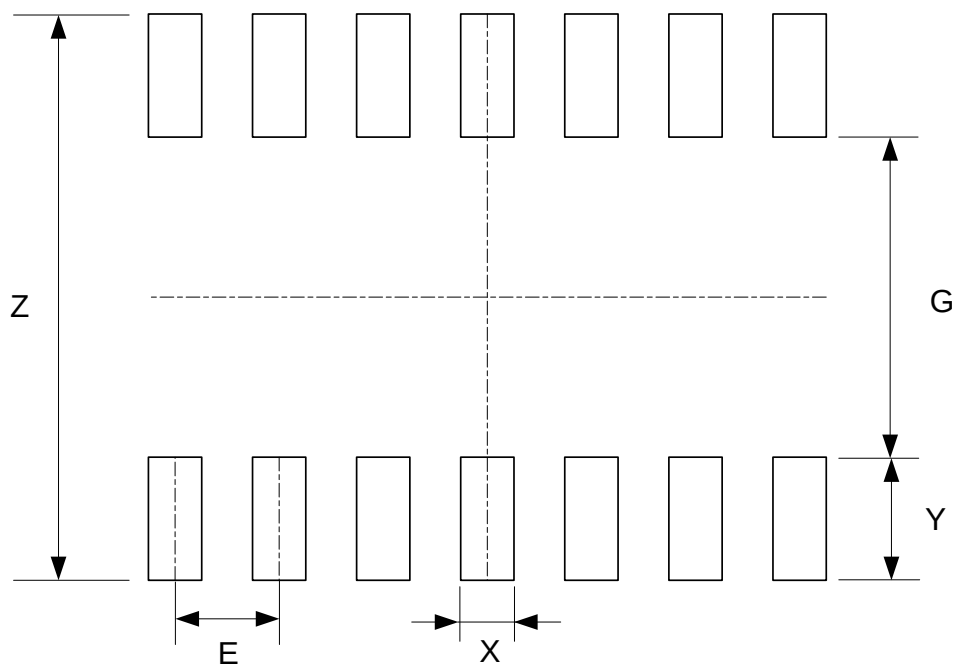
(2) Package Type: TSSOP-14



Note: Eject hole, oriented hole and mold mark is optional.

Suggested Pad Layout

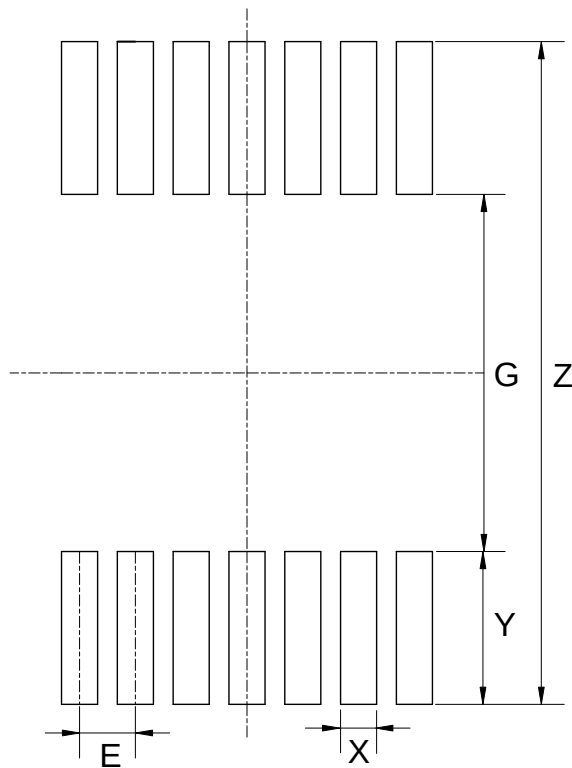
(1) Package Type: SO-14



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)
Value	6.900/0.272	3.900/0.154	0.650/0.026	1.500/0.059	1.270/0.050

Suggested Pad Layout (Cont.)

(2) Package Type: TSSOP-14



Dimensions	Z (mm)/(inch)	G (mm)/(inch)	X (mm)/(inch)	Y (mm)/(inch)	E (mm)/(inch)
Value	7.720/0.304	4.160/0.164	0.420/0.017	1.780/0.070	0.650/0.026

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