

# ADG511/ADG512/ADG513—SPECIFICATIONS<sup>1</sup>

## Dual Supply ( $V_{DD} = +5\text{ V} \pm 10\%$ , $V_{SS} = -5\text{ V} \pm 10\%$ , $GND = 0\text{ V}$ , unless otherwise noted)

| Parameter                                            | B Versions               |                      | T Version                |                      | Unit                                   | Test Conditions/Comments                                                                                                       |
|------------------------------------------------------|--------------------------|----------------------|--------------------------|----------------------|----------------------------------------|--------------------------------------------------------------------------------------------------------------------------------|
|                                                      | 25°C                     | -40°C to +85°C       | 25°C                     | -55°C to +125°C      |                                        |                                                                                                                                |
| <b>ANALOG SWITCH</b>                                 |                          |                      |                          |                      |                                        |                                                                                                                                |
| Analog Signal Range                                  |                          | $V_{DD}$ to $V_{SS}$ |                          | $V_{DD}$ to $V_{SS}$ | V                                      |                                                                                                                                |
| $R_{ON}$                                             | 30                       | 50                   | 30                       | 50                   | $\Omega$ typ<br>$\Omega$ max           | $V_D = \pm 3.5\text{ V}$ , $I_S = -10\text{ mA}$ ;<br>$V_{DD} = +4.5\text{ V}$ , $V_{SS} = -4.5\text{ V}$                      |
| <b>LEAKAGE CURRENTS</b>                              |                          |                      |                          |                      |                                        |                                                                                                                                |
| Source OFF Leakage $I_S$ (OFF)                       | $\pm 0.025$<br>$\pm 0.1$ | $\pm 2.5$            | $\pm 0.025$<br>$\pm 0.1$ | $\pm 2.5$            | nA typ<br>nA max                       | $V_{DD} = +5.5\text{ V}$ , $V_{SS} = -5.5\text{ V}$<br>$V_D = \pm 4.5\text{ V}$ , $V_S = \mp 4.5\text{ V}$ ;<br>Test Circuit 2 |
| Drain OFF Leakage $I_D$ (OFF)                        | $\pm 0.025$<br>$\pm 0.1$ | $\pm 2.5$            | $\pm 0.025$<br>$\pm 0.1$ | $\pm 2.5$            | nA typ<br>nA max                       | $V_D = \pm 4.5\text{ V}$ , $V_S = \mp 4.5\text{ V}$ ;<br>Test Circuit 2                                                        |
| Channel ON Leakage $I_D$ , $I_S$ (ON)                | $\pm 0.05$<br>$\pm 0.2$  | $\pm 5$              | $\pm 0.05$<br>$\pm 0.2$  | $\pm 5$              | nA typ<br>nA max                       | $V_D = V_S = \pm 4.5\text{ V}$ ;<br>Test Circuit 3                                                                             |
| <b>DIGITAL INPUTS</b>                                |                          |                      |                          |                      |                                        |                                                                                                                                |
| Input High Voltage, $V_{INH}$                        |                          | 2.4                  |                          | 2.4                  | V min                                  |                                                                                                                                |
| Input Low Voltage, $V_{INL}$                         |                          | 0.8                  |                          | 0.8                  | V max                                  |                                                                                                                                |
| Input Current<br>$I_{INL}$ or $I_{INH}$              | 0.005                    | $\pm 0.1$            | 0.005                    | $\pm 0.1$            | $\mu\text{A}$ typ<br>$\mu\text{A}$ max | $V_{IN} = V_{INL}$ or $V_{INH}$                                                                                                |
| <b>DYNAMIC CHARACTERISTICS<sup>2</sup></b>           |                          |                      |                          |                      |                                        |                                                                                                                                |
| $t_{ON}$                                             | 200                      | 375                  | 200                      | 375                  | ns typ<br>ns max                       | $R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$ ;<br>$V_S = \pm 3\text{ V}$ ; Test Circuit 4                                        |
| $t_{OFF}$                                            | 120                      | 150                  | 120                      | 150                  | ns typ<br>ns max                       | $R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$ ;<br>$V_S = \pm 3\text{ V}$ ; Test Circuit 4                                        |
| Break-Before-Make Time<br>Delay, $t_D$ (ADG513 Only) | 100                      |                      | 100                      |                      | ns typ                                 | $R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$ ;<br>$V_{S1} = V_{S2} = 3\text{ V}$ ; Test Circuit 5                                |
| Charge Injection                                     | 11                       |                      | 11                       |                      | pC typ                                 | $V_S = 0\text{ V}$ , $R_S = 0\ \Omega$ , $C_L = 10\text{ nF}$ ;<br>Test Circuit 6                                              |
| OFF Isolation                                        | 68                       |                      | 68                       |                      | dB typ                                 | $R_L = 50\ \Omega$ , $C_L = 5\text{ pF}$ , $f = 1\text{ MHz}$ ;<br>Test Circuit 7                                              |
| Channel-to-Channel Crosstalk                         | 85                       |                      | 85                       |                      | dB typ                                 | $R_L = 50\ \Omega$ , $C_L = 5\text{ pF}$ , $f = 1\text{ MHz}$ ;<br>Test Circuit 8                                              |
| $C_S$ (OFF)                                          | 9                        |                      | 9                        |                      | pF typ                                 | $f = 1\text{ MHz}$                                                                                                             |
| $C_D$ (OFF)                                          | 9                        |                      | 9                        |                      | pF typ                                 | $f = 1\text{ MHz}$                                                                                                             |
| $C_D$ , $C_S$ (ON)                                   | 35                       |                      | 35                       |                      | pF typ                                 | $f = 1\text{ MHz}$                                                                                                             |
| <b>POWER REQUIREMENTS</b>                            |                          |                      |                          |                      |                                        |                                                                                                                                |
| $V_{DD}$                                             |                          | +4.5/5.5             |                          | +4.5/5.5             | V min/max                              |                                                                                                                                |
| $V_{SS}$                                             |                          | -4.5/-5.5            |                          | -4.5/-5.5            | V min/max                              |                                                                                                                                |
| $I_{DD}$                                             | 0.0001                   | 1                    | 0.0001                   | 1                    | $\mu\text{A}$ typ<br>$\mu\text{A}$ max | $V_{DD} = +5.5\text{ V}$ , $V_{SS} = -5.5\text{ V}$<br>Digital Inputs = 0 V or 5 V                                             |
| $I_{SS}$                                             | 0.0001                   | 1                    | 0.0001                   | 1                    | $\mu\text{A}$ typ<br>$\mu\text{A}$ max |                                                                                                                                |

### NOTES

<sup>1</sup>Temperature ranges are as follows: B Versions -40°C to +85°C; T Version -55°C to +125°C.

<sup>2</sup>Guaranteed by design, not subject to production test.

Specifications subject to change without notice.

**Single Supply** ( $V_{DD} = 5\text{ V} \pm 10\%$ ,  $V_{SS} = 0\text{ V}$ ,  $GND = 0\text{ V}$ , unless otherwise noted)

| Parameter                                                                                                                                                                                                                                             | B Versions                                                                      |                                                | T Version                                                                       |                                                | Unit                                                                                                       | Test Conditions/Comments                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------------------------------|------------------------------------------------|---------------------------------------------------------------------------------|------------------------------------------------|------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
|                                                                                                                                                                                                                                                       | 25°C                                                                            | –40°C to +85°C                                 | 25°C                                                                            | –55°C to +125°C                                |                                                                                                            |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                |
| <b>ANALOG SWITCH</b><br>Analog Signal Range<br>$R_{ON}$                                                                                                                                                                                               | 45                                                                              | 0 V to $V_{DD}$<br>75                          | 45                                                                              | 0 V to $V_{DD}$<br>75                          | V<br>$\Omega$ typ<br>$\Omega$ max                                                                          | $V_D = 3.5\text{ V}$ , $I_S = -10\text{ mA}$ ;<br>$V_{DD} = 4.5\text{ V}$                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      |
| <b>LEAKAGE CURRENTS</b><br>Source OFF Leakage $I_S$ (OFF)<br>Drain OFF Leakage $I_D$ (OFF)<br>Channel ON Leakage $I_D$ , $I_S$ (ON)                                                                                                                   | $\pm 0.025$<br>$\pm 0.1$<br>$\pm 0.025$<br>$\pm 0.1$<br>$\pm 0.05$<br>$\pm 0.2$ | $\pm 2.5$<br>$\pm 2.5$<br>$\pm 2.5$<br>$\pm 5$ | $\pm 0.025$<br>$\pm 0.1$<br>$\pm 0.025$<br>$\pm 0.1$<br>$\pm 0.05$<br>$\pm 0.2$ | $\pm 2.5$<br>$\pm 2.5$<br>$\pm 2.5$<br>$\pm 5$ | nA typ<br>nA max<br>nA typ<br>nA max<br>nA typ<br>nA max                                                   | $V_{DD} = 5.5\text{ V}$<br>$V_D = 4.5/1\text{ V}$ , $V_S = 1/4.5\text{ V}$ ;<br>Test Circuit 2<br>$V_D = 4.5/1\text{ V}$ , $V_S = 1/4.5\text{ V}$ ;<br>Test Circuit 2<br>$V_D = V_S = 4.5\text{ V}/1\text{ V}$ ;<br>Test Circuit 3                                                                                                                                                                                                                                                                                                                                                                             |
| <b>DIGITAL INPUTS</b><br>Input High Voltage, $V_{INH}$<br>Input Low Voltage, $V_{INL}$<br>Input Current<br>$I_{INL}$ or $I_{INH}$                                                                                                                     |                                                                                 | 2.4<br>0.8<br><br>$\pm 0.1$                    |                                                                                 | 2.4<br>0.8<br><br>$\pm 0.1$                    | V min<br>V max<br><br>$\mu\text{A}$ typ<br>$\mu\text{A}$ max                                               | <br><br><br>$V_{IN} = V_{INL}$ or $V_{INH}$                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                    |
| <b>DYNAMIC CHARACTERISTICS</b> <sup>2</sup><br>$t_{ON}$<br>$t_{OFF}$<br>Break-Before-Make Time<br>Delay, $t_D$ (ADG513 Only)<br>Charge Injection<br>OFF Isolation<br>Channel-to-Channel Crosstalk<br>$C_S$ (OFF)<br>$C_D$ (OFF)<br>$C_D$ , $C_S$ (ON) | 250<br>50<br>200<br>16<br>68<br>85<br>9<br>9<br>35                              | 500<br>100                                     | 250<br>50<br>200<br>16<br>68<br>85<br>9<br>9<br>35                              | 500<br>100                                     | ns typ<br>ns max<br>ns typ<br>ns max<br>ns typ<br>pC typ<br>dB typ<br>dB typ<br>pF typ<br>pF typ<br>pF typ | $R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$ ;<br>$V_S = 2\text{ V}$ ; Test Circuit 4<br>$R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$ ;<br>$V_S = 2\text{ V}$ ; Test Circuit 4<br>$R_L = 300\ \Omega$ , $C_L = 35\text{ pF}$ ;<br>$V_{S1} = V_{S2} = 2\text{ V}$ ; Test Circuit 5<br>$V_S = 0\text{ V}$ , $R_S = 0\ \Omega$ , $C_L = 10\text{ nF}$ ;<br>Test Circuit 6<br>$R_L = 50\ \Omega$ , $C_L = 5\text{ pF}$ , $f = 1\text{ MHz}$ ;<br>Test Circuit 7<br>$R_L = 50\ \Omega$ , $C_L = 5\text{ pF}$ , $f = 1\text{ MHz}$ ;<br>Test Circuit 8<br>$f = 1\text{ MHz}$<br>$f = 1\text{ MHz}$<br>$f = 1\text{ MHz}$ |
| <b>POWER REQUIREMENTS</b><br>$V_{DD}$<br>$I_{DD}$                                                                                                                                                                                                     |                                                                                 | 4.5/5.5<br>1                                   |                                                                                 | 4.5/5.5<br>1                                   | V min/max<br>$\mu\text{A}$ typ<br>$\mu\text{A}$ max                                                        | $V_{DD} = 5.5\text{ V}$<br>Digital Inputs = 0 V or 5 V                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                         |

## NOTES

<sup>1</sup>Temperature ranges are as follows: B Versions –40°C to +85°C; T Version –55°C to +125°C.<sup>2</sup>Guaranteed by design, not subject to production test.

Specifications subject to change without notice.

### Single Supply ( $V_{DD} = 3.3\text{ V} \pm 10\%$ , $V_{SS} = 0\text{ V}$ , $GND = 0\text{ V}$ , unless otherwise noted)

|                                                         | <b>B Version</b> |                        |                  |                                                                          |
|---------------------------------------------------------|------------------|------------------------|------------------|--------------------------------------------------------------------------|
| <b>Parameter</b>                                        | <b>25°C</b>      | <b>0°C to<br/>70°C</b> | <b>Unit</b>      | <b>Test Conditions/Comments</b>                                          |
| <b>ANALOG SWITCH</b>                                    |                  |                        |                  |                                                                          |
| Analog Signal Range                                     |                  | 0 V to V <sub>DD</sub> | V                |                                                                          |
| R <sub>ON</sub>                                         | 200              | 500                    | Ω typ<br>Ω max   | V <sub>D</sub> = 1.5 V, I <sub>S</sub> = -1 mA;<br>V <sub>DD</sub> = 3 V |
| <b>LEAKAGE CURRENTS</b>                                 |                  |                        |                  |                                                                          |
| Source OFF Leakage I <sub>S</sub> (OFF)                 | ±0.025           |                        | nA typ           | V <sub>DD</sub> = 3.6 V                                                  |
|                                                         | ±0.1             | ±2.5                   | nA max           | V <sub>D</sub> = 2.6/1 V, V <sub>S</sub> = 1/2.6 V;                      |
| Drain OFF Leakage I <sub>D</sub> (OFF)                  | ±0.025           |                        | nA typ           | Test Circuit 2                                                           |
|                                                         | ±0.1             | ±2.5                   | nA max           | V <sub>D</sub> = 2.6/1 V, V <sub>S</sub> = 1/2.6 V;                      |
| Channel ON Leakage I <sub>D</sub> , I <sub>S</sub> (ON) | ±0.05            |                        | nA typ           | Test Circuit 2                                                           |
|                                                         | ±0.2             | ±5                     | nA max           | V <sub>D</sub> = V <sub>S</sub> = 2.6 V/1 V;                             |
|                                                         |                  |                        |                  | Test Circuit 3                                                           |
| <b>DIGITAL INPUTS</b>                                   |                  |                        |                  |                                                                          |
| Input High Voltage, V <sub>INH</sub>                    |                  | 2.4                    | V min            |                                                                          |
| Input Low Voltage, V <sub>INL</sub>                     |                  | 0.8                    | V max            |                                                                          |
| Input Current                                           |                  |                        |                  |                                                                          |
| I <sub>INL</sub> or I <sub>INH</sub>                    | 0.005            | ±0.1                   | μA typ<br>μA max | V <sub>IN</sub> = V <sub>INL</sub> or V <sub>INH</sub>                   |
| <b>DYNAMIC CHARACTERISTICS<sup>2</sup></b>              |                  |                        |                  |                                                                          |
| t <sub>ON</sub>                                         | 600              | 1200                   | ns typ<br>ns max | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF;                          |
| t <sub>OFF</sub>                                        | 100              | 160                    | ns typ<br>ns max | V <sub>S</sub> = 1 V; Test Circuit 4                                     |
| Break-Before-Make Time                                  |                  |                        |                  | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF;                          |
| Delay, t <sub>D</sub> (ADG513 Only)                     | 500              |                        | ns typ           | V <sub>S</sub> = 1 V; Test Circuit 4                                     |
| Charge Injection                                        | 11               |                        | pC typ           | R <sub>L</sub> = 300 Ω, C <sub>L</sub> = 35 pF;                          |
|                                                         |                  |                        |                  | V <sub>S1</sub> = V <sub>S2</sub> = 1 V; Test Circuit 5                  |
| OFF Isolation                                           | 68               |                        | dB typ           | V <sub>S</sub> = 0 V, R <sub>S</sub> = 0 Ω, C <sub>L</sub> = 10 nF;      |
|                                                         |                  |                        |                  | Test Circuit 6                                                           |
| Channel-to-Channel Crosstalk                            | 85               |                        | dB typ           | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz;                 |
|                                                         |                  |                        |                  | Test Circuit 7                                                           |
| C <sub>S</sub> (OFF)                                    | 9                |                        | pF typ           | R <sub>L</sub> = 50 Ω, C <sub>L</sub> = 5 pF, f = 1 MHz;                 |
| C <sub>D</sub> (OFF)                                    | 9                |                        | pF typ           | Test Circuit 8                                                           |
| C <sub>D</sub> , C <sub>S</sub> (ON)                    | 35               |                        | pF typ           | f = 1 MHz                                                                |
|                                                         |                  |                        |                  | f = 1 MHz                                                                |
| <b>POWER REQUIREMENTS</b>                               |                  |                        |                  |                                                                          |
| V <sub>DD</sub>                                         |                  | 3/3.6                  | V min/max        |                                                                          |
| I <sub>DD</sub>                                         | 0.0001           | 1                      | μA typ<br>μA max | V <sub>DD</sub> = 3.6 V                                                  |
|                                                         |                  |                        |                  | Digital Inputs = 0 V or 3 V                                              |

<sup>1</sup>Temperature range is as follows: B Version -40°C to +85°C.

<sup>2</sup>Guaranteed by design, not subject to production test.

Specifications subject to change without notice.

## ABSOLUTE MAXIMUM RATINGS<sup>1</sup>

(T<sub>A</sub> = +25°C unless otherwise noted)

|                                            |                                                                                    |
|--------------------------------------------|------------------------------------------------------------------------------------|
| V <sub>DD</sub> to V <sub>SS</sub> .....   | 44 V                                                                               |
| V <sub>DD</sub> to GND .....               | –0.3 V to +25 V                                                                    |
| V <sub>SS</sub> to GND .....               | +0.3 V to –25 V                                                                    |
| Analog, Digital Inputs <sup>2</sup> .....  | V <sub>SS</sub> – 2 V to V <sub>DD</sub> + 2 V or<br>30 mA, Whichever Occurs First |
| Continuous Current, S or D .....           | 30 mA                                                                              |
| Peak Current, S or D .....                 | 100 mA<br>(Pulsed at 1 ms, 10% Duty Cycle max)                                     |
| Operating Temperature Range                |                                                                                    |
| Industrial (B Version) .....               | –40°C to +85°C                                                                     |
| Extended (T Version) .....                 | –55°C to +125°C                                                                    |
| Storage Temperature Range .....            | –65°C to +150°C                                                                    |
| Junction Temperature .....                 | 150°C                                                                              |
| Cerdip Package, Power Dissipation .....    | 900 mW                                                                             |
| θ <sub>JA</sub> Thermal Impedance .....    | 76°C/W                                                                             |
| Lead Temperature, Soldering (10 sec) ..... | 300°C                                                                              |

|                                            |         |
|--------------------------------------------|---------|
| Plastic Package, Power Dissipation .....   | 470 mW  |
| θ <sub>JA</sub> Thermal Impedance .....    | 117°C/W |
| Lead Temperature, Soldering (10 sec) ..... | 260°C   |
| SOIC Package, Power Dissipation .....      | 600 mW  |
| θ <sub>JA</sub> Thermal Impedance .....    | 77°C/W  |
| Lead Temperature, Soldering                |         |
| Vapor Phase (60 sec) .....                 | 215°C   |
| Infrared (15 sec) .....                    | 220°C   |

## NOTES

<sup>1</sup>Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those listed in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability. Only one absolute maximum rating may be applied at any one time.

<sup>2</sup>Overvoltages at IN, S or D will be clamped by internal diodes. Current should be limited to the maximum ratings given.

## CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the ADG511/ADG512/ADG513 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high-energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.



## ORDERING GUIDE

| Model <sup>1</sup>     | Temperature Range <sup>2</sup> | Package Option <sup>3</sup> |
|------------------------|--------------------------------|-----------------------------|
| ADG511BN               | –40°C to +85°C                 | N-16                        |
| ADG511BR               | –40°C to +85°C                 | R-16A                       |
| ADG511ABR <sup>4</sup> | –40°C to +85°C                 | R-16A                       |
| ADG511TQ <sup>4</sup>  | –55°C to +125°C                | Q-16                        |
| ADG512BN               | –40°C to +85°C                 | N-16                        |
| ADG512BR               | –40°C to +85°C                 | R-16A                       |
| ADG512ABR <sup>4</sup> | –40°C to +85°C                 | R-16A                       |
| ADG513BN               | –40°C to +85°C                 | N-16                        |
| ADG513BR               | –40°C to +85°C                 | R-16A                       |
| ADG513ABR <sup>4</sup> | –40°C to +85°C                 | R-16A                       |

## NOTES

<sup>1</sup>For availability of MIL-STD-883, Class B processed parts, contact factory.

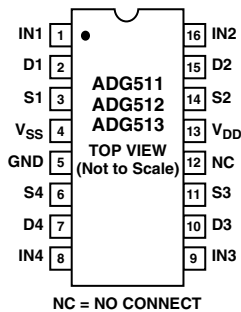
<sup>2</sup>3.3 V specifications apply over 0°C to 70°C temperature range.

<sup>3</sup>N = Plastic DIP; R = 0.15" Small Outline IC (SOIC); Q = Cerdip.

<sup>4</sup>Trench isolated latch-up proof parts. See Trench Isolation section.

# ADG511/ADG512/ADG513

## PIN CONFIGURATION (DIP/SOIC)



Truth Table (ADG511/ADG512)

| ADG511<br>In | ADG512<br>In | Switch<br>Condition |
|--------------|--------------|---------------------|
| 0            | 1            | ON                  |
| 1            | 0            | OFF                 |

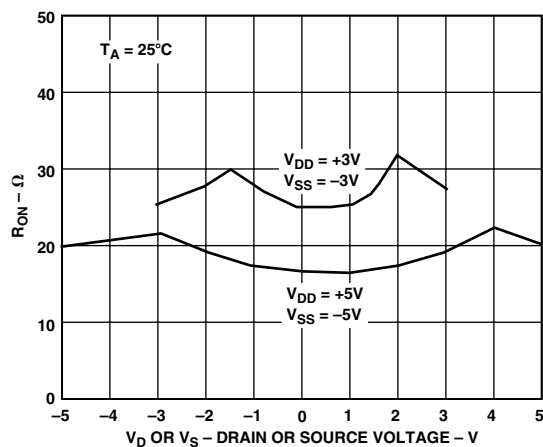
Truth Table (ADG513)

| Logic | Switch<br>1, 4 | Switch<br>2, 3 |
|-------|----------------|----------------|
| 0     | OFF            | ON             |
| 1     | ON             | OFF            |

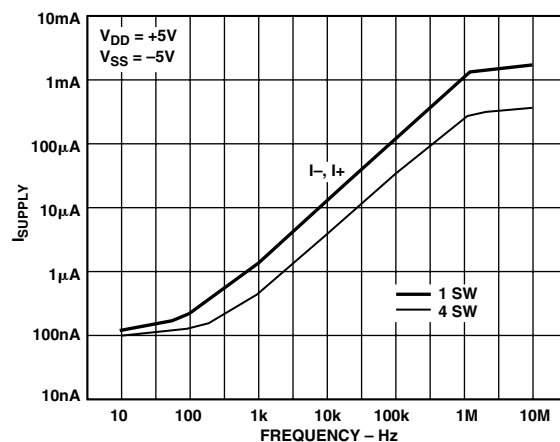
## TERMINOLOGY

|                  |                                                                                                                         |
|------------------|-------------------------------------------------------------------------------------------------------------------------|
| $V_{DD}$         | Most Positive Power Supply Potential.                                                                                   |
| $V_{SS}$         | Most Negative Power Supply Potential in dual supplies. In single supply applications, it may be connected to GND.       |
| GND              | Ground (0 V) Reference.                                                                                                 |
| S                | Source Terminal. May be an input or output.                                                                             |
| D                | Drain Terminal. May be an input or output.                                                                              |
| IN               | Logic Control Input.                                                                                                    |
| $R_{ON}$         | Ohmic Resistance between D and S.                                                                                       |
| $I_S$ (OFF)      | Source Leakage Current with the switch "OFF."                                                                           |
| $I_D$ (OFF)      | Drain Leakage Current with the switch "OFF."                                                                            |
| $I_D, I_S$ (ON)  | Channel Leakage Current with the switch "ON."                                                                           |
| $V_D$ ( $V_S$ )  | Analog Voltage on terminals D, S.                                                                                       |
| $C_S$ (OFF)      | "OFF" Switch Source Capacitance.                                                                                        |
| $C_D$ (OFF)      | "OFF" Switch Drain Capacitance.                                                                                         |
| $C_D, C_S$ (ON)  | "ON" Switch Capacitance.                                                                                                |
| $t_{ON}$         | Delay between applying the digital control input and the output switching on.                                           |
| $t_{OFF}$        | Delay between applying the digital control input and the output switching off.                                          |
| $t_D$            | "OFF" or "ON" time measured between the 90% points of both switches when switching from one address state to another.   |
| Crosstalk        | A measure of unwanted signal which is coupled through from one channel to another as a result of parasitic capacitance. |
| Off Isolation    | A measure of unwanted signal coupling through an "OFF" switch.                                                          |
| Charge Injection | A measure of the glitch impulse transferred from the digital input to the analog output during switching.               |

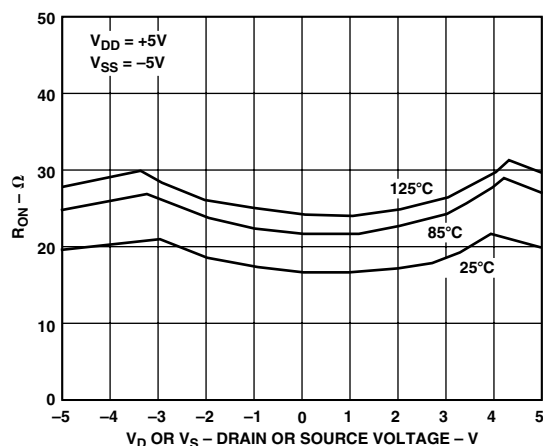
# Typical Performance Characteristics—ADG511/ADG512/ADG513



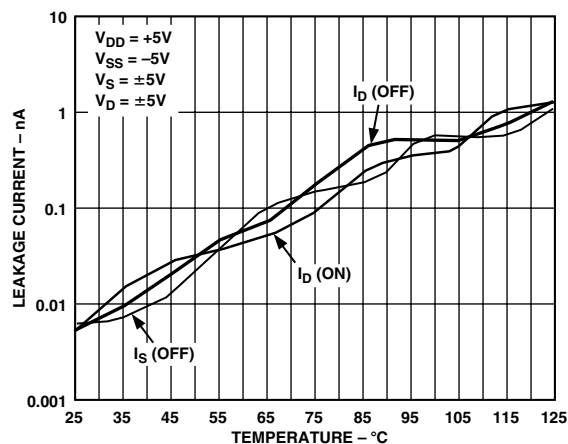
TPC 1. On Resistance as a Function of  $V_D$  ( $V_S$ ) Dual Supplies



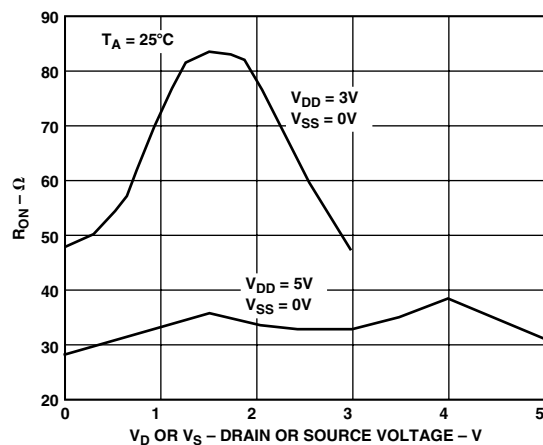
TPC 4. Supply Current vs. Input Switching Frequency



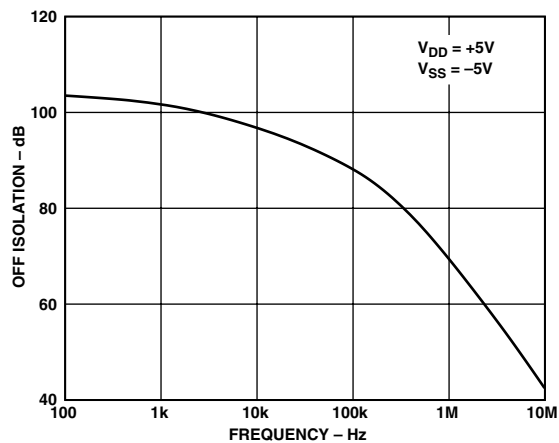
TPC 2. On Resistance as a Function of  $V_D$  ( $V_S$ ) for Different Temperatures



TPC 5. Leakage Currents as a Function of Temperature

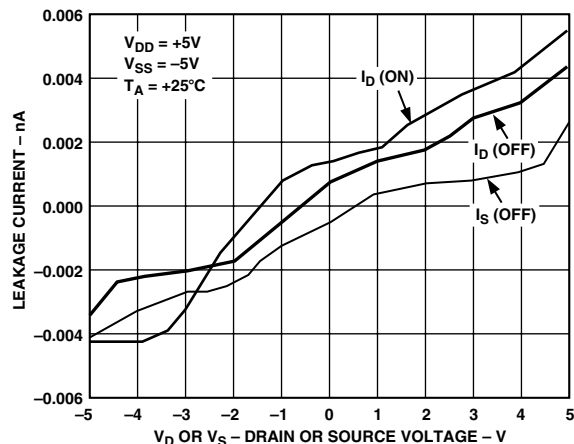


TPC 3. On Resistance as a Function of  $V_D$  ( $V_S$ ) Single Supply

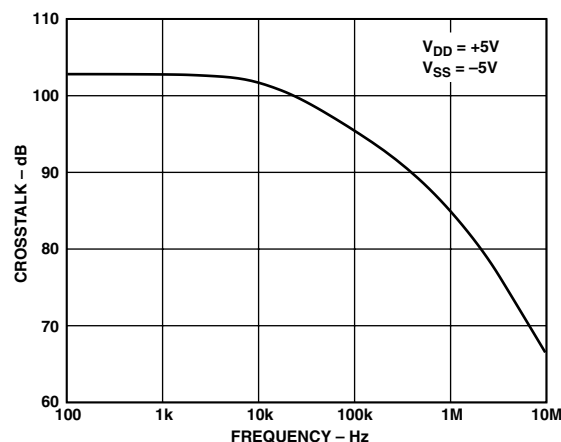


TPC 6. Off Isolation vs. Frequency

# ADG511/ADG512/ADG513



TPC 7. Leakage Currents as a Function of  $V_D$  ( $V_S$ )



TPC 8. Crosstalk vs. Frequency

## APPLICATION

Figure 1 illustrates a precise sample-and-hold circuit. An AD845 is used as the input buffer while the output operational amplifier is an OP07. During the track mode, SW1 is closed and the output  $V_{OUT}$  follows the input signal  $V_{IN}$ . In the hold mode, SW1 is opened and the signal is held by the hold capacitor  $C_H$ .

Due to switch and capacitor leakage, the voltage on the hold capacitor will decrease with time. The ADG511/ADG512/ADG513 minimizes this droop due to its low leakage specifications. The droop rate is further minimized by the use of a poly-styrene hold capacitor. The droop rate for the circuit shown is typically  $15 \mu\text{V}/\mu\text{s}$ .

A second switch, SW2, which operates in parallel with SW1, is included in this circuit to reduce pedestal error. Since both switches will be at the same potential, they will have a differential effect on the op amp OP07, which will minimize charge injection effects. Pedestal error is also reduced by the compensation

network  $R_C$  and  $C_C$ . This compensation network also reduces the hold time glitch while optimizing the acquisition time. Using the illustrated op amps and component values, the pedestal error has a maximum value of 5 mV over the  $\pm 3 \text{ V}$  input range. The acquisition time is  $2.5 \mu\text{s}$  while the settling time is  $1.85 \mu\text{s}$ .

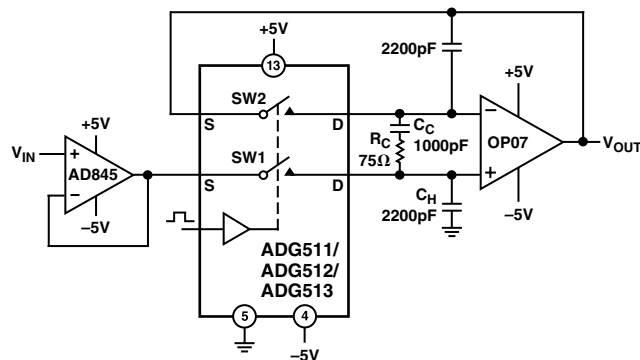


Figure 1. Accurate Sample-and-Hold

## TRENCH ISOLATION

The MOS devices that make up the ADG511A/ADG512A/ADG513A are isolated from each other by an oxide layer (trench) (see Figure 2). When the NMOS and PMOS devices are not electrically isolated from each other, there exists the possibility of "latch-up" caused by parasitic junctions between CMOS transistors. Latch-up is caused when P-N junctions that are normally reverse biased, become forward biased, causing large currents to flow. This can be destructive.

CMOS devices are normally isolated from each other by *Junction Isolation*. In Junction Isolation the N and P wells of the CMOS transistors form a diode that is reverse biased under normal operation. However, during overvoltage conditions, this diode becomes forward biased. A Silicon-Controlled Rectifier (SCR)-type circuit is formed by the two transistors, causing a significant amplification of the current that, in turn, leads to latch-up. With Trench Isolation, this diode is removed; the result is a latch-up-proof circuit.

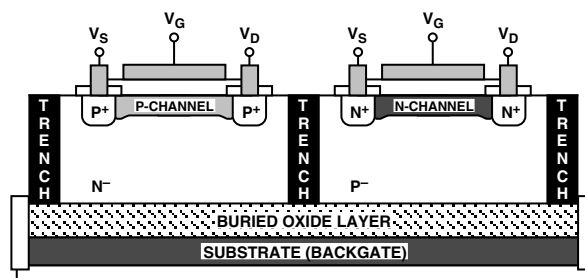
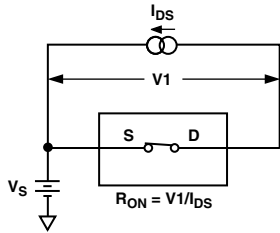
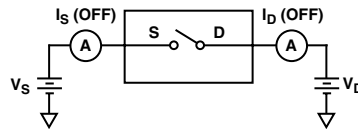


Figure 2. Trench Isolation

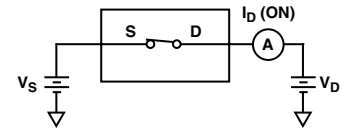
## Test Circuits



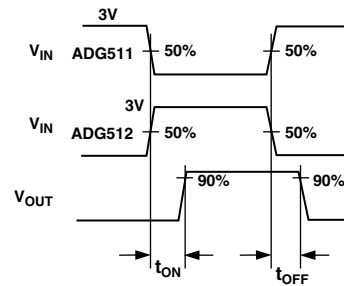
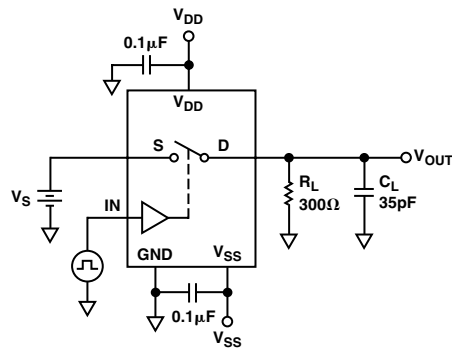
Test Circuit 1. On Resistance



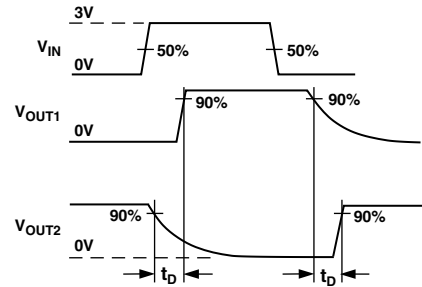
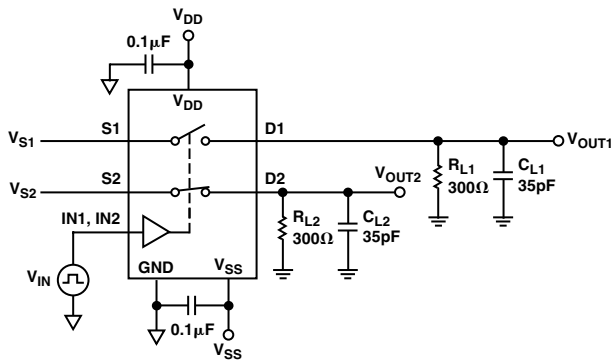
Test Circuit 2. Off Leakage



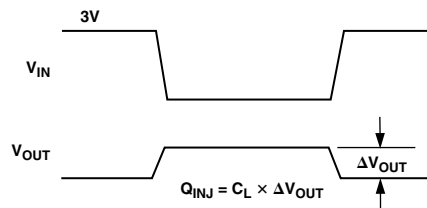
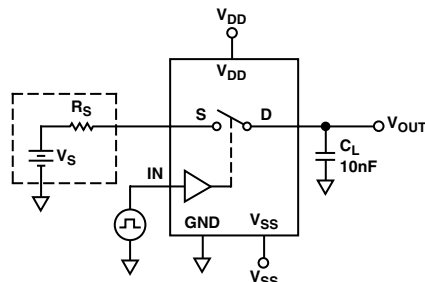
Test Circuit 3. On Leakage



Test Circuit 4. Switching Times

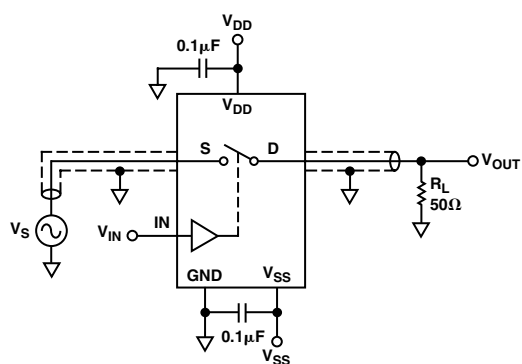


Test Circuit 5. Break-Before-Make Time Delay

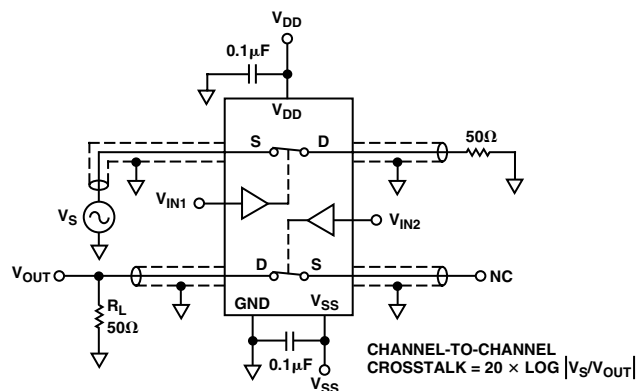


Test Circuit 6. Charge Injection

# ADG511/ADG512/ADG513



Test Circuit 7. Off Isolation

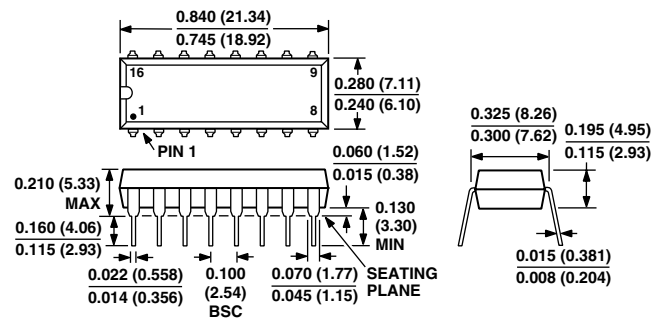


Test Circuit 8. Channel-to-Channel Crosstalk

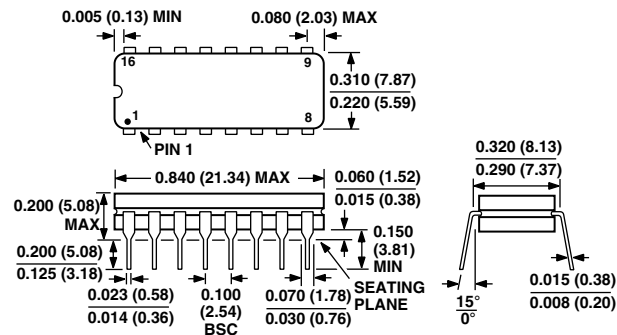
## OUTLINE DIMENSIONS

Dimensions shown in inches and (mm).

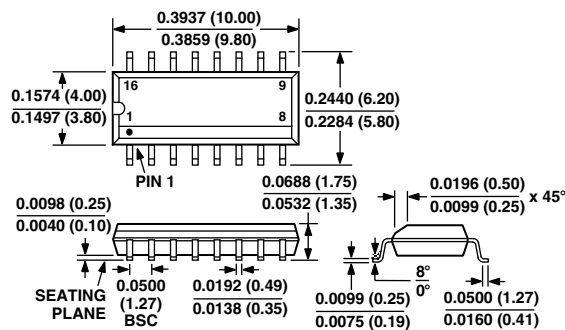
**16-Lead Plastic DIP**  
**(N-16)**



### 16-Lead Cerdip (Q-16)



**16-Lead SOIC  
(R-16A)**



# ADG511/ADG512/ADG513—Revision History

| Location                                                                                        | Page |
|-------------------------------------------------------------------------------------------------|------|
| Data Sheet changed from REV. B to REV. C.                                                       |      |
| Changes to Specifications table, Dual Supply, and Notes: “T Versions” made singular . . . . .   | 2    |
| Changes to Specifications table, Single Supply, and Notes: “T Versions” made singular . . . . . | 3    |
| Change to Ordering Guide: Removed one line . . . . .                                            | 5    |

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