Chopper-Stabilized Precision Hall-Effect Switches

SELECTION GUIDE

Part Number	Packing ^[1]	Mounting	Ambient, T _A	Switchpoints (Typ.) (G)		Output In South (Positive) Magnetic Field	
			(°C)	B _{OP}	B _{RP}	Magnetic Fleid	
A1120ELHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount					
A1120ELHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 85				
A1120EUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole		35	25		
A1120LLHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount		35	25		
A1120LLHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 150				
A1120LUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole					
A1121ELHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount					
A1121ELHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 85				
A1121EUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole		95	70		
A1121LLHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount		95			
A1121LLHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 150			On (logic low)	
A1121LUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole					
A1122ELHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount		- 150	125		
A1122ELHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 85				
A1122EUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole					
A1122LLHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount					
A1122LLHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 150				
A1122LUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole					
A1123LLHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount					
A1123LLHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 150	280	225		
A1123LUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole					
A1125ELHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount					
A1125ELHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 85				
A1125EUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole		35	25	Off (logic high)	
A1125LLHLX-T	13-in. reel, 10000 pieces/reel	3-pin SOT23W surface mount		- 35	20		
A1125LLHLT-T ^[2]	7-in. reel, 3000 pieces/reel	3-pin SOT23W surface mount	-40 to 150				
A1125LUA-T ^[3]	Bulk, 500 pieces/bag	3-pin SIP through hole					

^[1] Contact Allegro for additional packing options.

^[2] Available through authorized Allegro distributors only.

^[3] The chopper-style UA package is not for new design; the matrix HD style UA package is recommended for new designs.

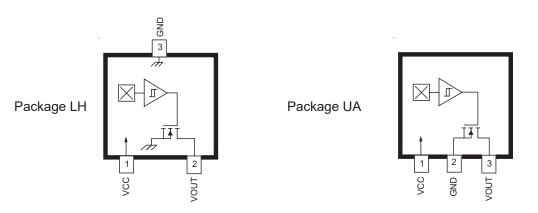




ABSOLUTE MAXIMUM RATINGS

Characteristic Symbol		Notes	Rating	Units
Forward Supply Voltage	V _{CC}		26.5	V
Reverse Supply Voltage	V _{RCC}		-30	V
Output Off Voltage	V _{OUT}		26	V
Continuous Output Current	I _{OUT}		25	mA
Reverse Output Current	I _{ROUT}		-50	mA
Operating Ambient Temperature	т	Range E	-40 to 85	°C
Operating Ambient Temperature	T _A	Range L	-40 to 150	°C
Maximum Junction Temperature	T _J (max)		165	°C
Storage Temperature	T _{stg}		-65 to 170	°C

PINOUT DIAGRAMS AND TERMINAL LIST TABLE



Terminal List

Name	Description	Number			
Name	Description	Package LH	Package UA		
VCC	Connects power supply to chip	1	1		
VOUT	Output from circuit	2	3		
GND	Ground	3	2		



Chopper-Stabilized Precision Hall-Effect Switches

ELECTRICAL CHARACTERISTICS: Valid over full operating voltage and ambient temperature ranges, unless otherwise noted

Characteristics	Symbol		Test Conditions	Min.	Typ. [1]	Max.	Unit ^[2]
ELECTRICAL CHARACTERISTICS							
Forward Supply Voltage	V _{CC}	Operating	J, T _J < 165°C	3	_	24	V
Output Leakage Current	IOUTOFF	A1120 A1121 A1122 A1123	V _{OUT} = 24 V, B < B _{RP}	_	_	10	μA
		A1125	V _{OUT} = 24 V, B > B _{OP}	_	-	10	μA
Output Saturation Voltage	V _{OUT(SAT)}	A1120 A1121 A1122 A1123	I _{OUT} = 20 mA, B > B _{OP}	_	185	500	mV
		A1125	I _{OUT} = 20 mA, B < B _{RP}	_	185	500	mV
Output Current Limit	I _{OM}	A1120 A1121 A1122 A1123	B > B _{OP}	30	_	60	mA
		A1125	B < B _{RP}	30	-	60	mA
Power-On Time ^[3]	t _{PO}	V _{CC} > 3.0 V, B < B _{RP} (min) – 10 G, B > B _{OP} (max) + 10 G		-	-	25	μs
Chopping Frequency	f _C			-	800	_	kHz
Output Rise Time [3][4]	t _r	R _L = 820 Ω, C _S = 20 pF		_	0.2	2	μs
Output Fall Time ^{[3][4]}	t _f	R _L = 820 Ω, C _S = 20 pF		_	0.1	2	μs
	I _{CC(ON)}	A1120 A1121 A1122 A1123	V _{CC} = 12 V, B > B _{OP}	_	_	4	mA
Summer Current		A1125	V _{CC} = 12 V, B < B _{RP}	_	-	4	mA
Supply Current	I _{CC(OFF)}	A1120 A1121 A1122 A1123	V _{CC} = 12 V, B < B _{RP}	_	_	4	mA
		A1125	V _{CC} = 12 V, B > B _{OP}	_	-	4	mA
Reverse Supply Current	I _{RCC}	$V_{RCC} = -30 V$		_	_	-5	mA
Supply Zener Clamp Voltage	Vz	I _{CC} = 5 m	A; T _A = 25°C	28	-	_	V
Zener Impedance	Ι _Z	I _{CC} = 5 m	A; T _A = 25°C	-	50	_	Ω

Continued on the next page ...



Chopper-Stabilized Precision Hall-Effect Switches

ELECTRICAL CHARACTERISTICS (continued): Valid over full operating voltage and ambient temperature ranges, unless otherwise noted

Characteristics	Symbol	Test Conditions		Min.	Typ. [1]	Max.	Unit ^[2]	
MAGNETIC CHARACTERISTICS								
		A1120		_	35	50	G	
		A1121		50	95	135	G	
Operate Point	B _{OP}	A1122		120	150	200	G	
		A1123		205	280	355	G	
		A1125		-	35	50	G	
	B _{RP}	A1120		5	25	_	G	
		A1121		40	70	110	G	
Release Point		A1122		110	125	190	G	
		A1123		150	225	300	G	
		A1125		5	25	_	G	
		A1120		-	10	_	G	
	B _{HYS}	A1121		10	25	42	G	
Hysteresis		A1122	(B _{OP} – B _{RP})	10	25	42	G	
		A1123		30	55	80	G	
		A1125		_	10	_	G	

^[1] Typical data are are at T_A = 25°C and V_{CC} = 12 V, and are for initial design estimations only.

 $^{[2]}$ 1 G (gauss) = 0.1 mT (millitesla).

^[3] Guaranteed by device design and characterization.

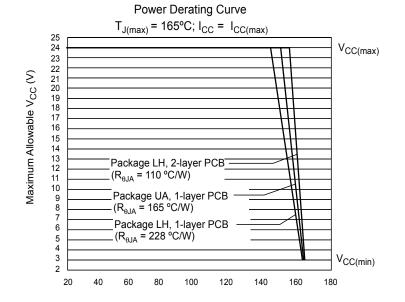
 $^{[4]}C_S$ = oscilloscope probe capacitance.



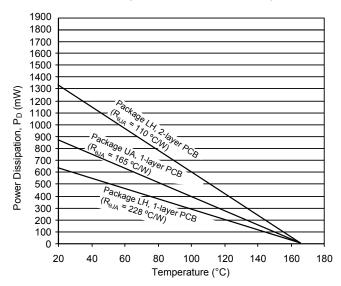
Chopper-Stabilized Precision Hall-Effect Switches

THERMAL CHARACTERISTICS: May require derating at maximum conditions; see application information

Characteristic Symbol Test Conditions		Value	Units	
Package Thermal Resistance		Package LH, 1-layer PCB with copper limited to solder pads		°C/W
	$R_{\theta JA}$	Package LH, 2-layer PCB with 0.463 in ² of copper area each side connected by thermal vias	110	°C/W
		Package UA, 1-layer PCB with copper limited to solder pads	165	°C/W

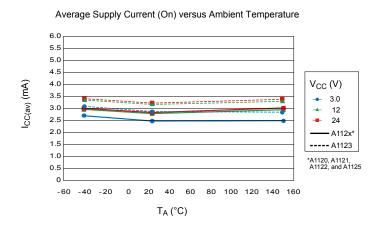


Power Dissipation versus Ambient Temperature

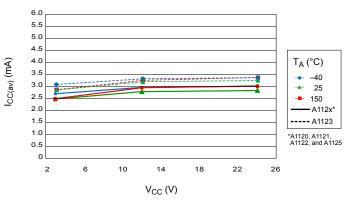




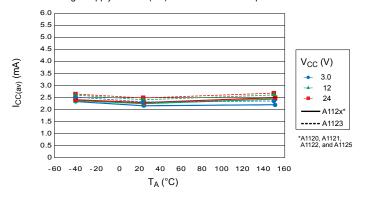
CHARACTERISTIC PERFORMANCE A1120, A1121, A1122, A1123, and A1125 Electrical Characteristics



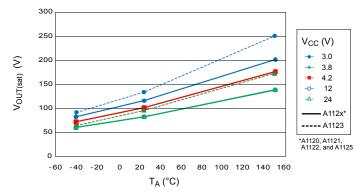
Average Supply Current (On) versus Average Supply Voltage



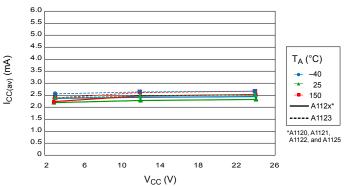
Average Supply Current (Off) versus Ambient Temperature



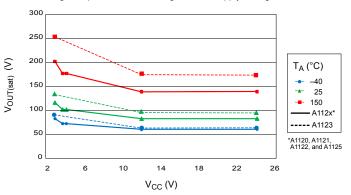
Average Output Saturation Voltage versus Ambient Temperature



Average Supply Current (Off) versus Average Supply Voltage



Average Output Saturation Voltage versus Supply Voltage

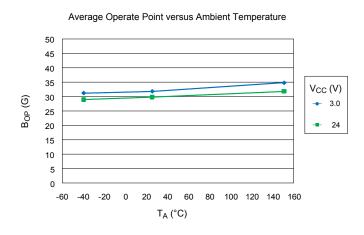




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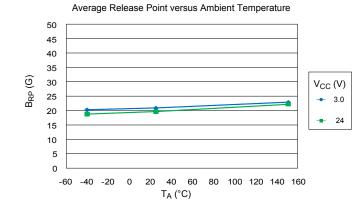
Chopper-Stabilized Precision Hall-Effect Switches

A1120 and A1125 Magnetic Characteristics

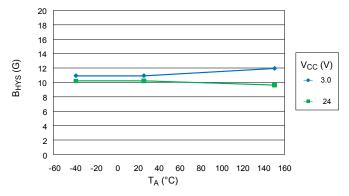


50 45 40 35 T_A (°C) B_{OP} (G) 30 **→** -40 25 + 25 **-** 150 20 15 10 5 0 2 6 10 22 26 14 18 $V_{CC}(V)$

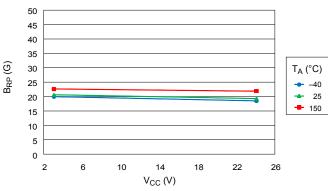
Average Operate Point versus Average Supply Voltage



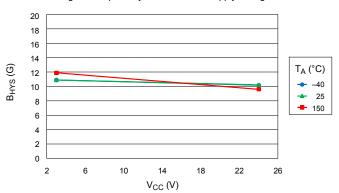
Average Switchpoint Hysteresis versus Ambient Temperature



Average Release Point versus Average Supply Voltage



Average Switchpoint Hysteresis versus Supply Voltage

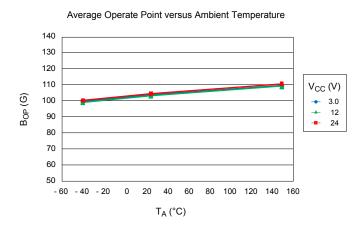


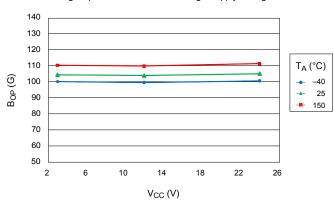


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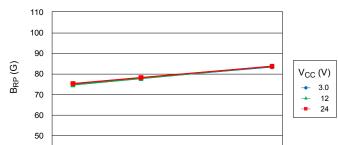
Chopper-Stabilized Precision Hall-Effect Switches

A1121 Magnetic Characteristics



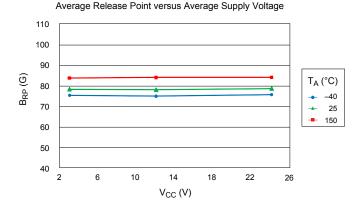


Average Operate Point versus Average Supply Voltage

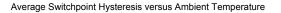


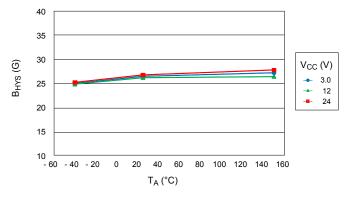
Average Release Point versus Ambient Temperature

100 120 140 160

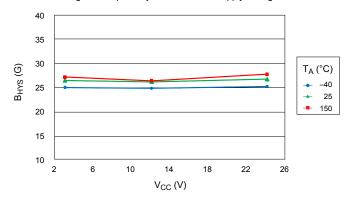


 $T_A\,(^\circ C)$





Average Switchpoint Hysteresis versus Supply Voltage





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40

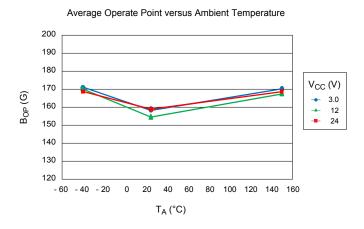
- 60 - 40 - 20

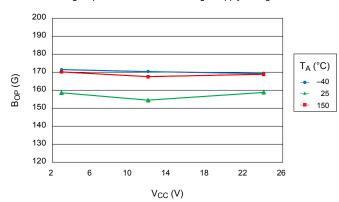
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20 40 60 80

Chopper-Stabilized Precision Hall-Effect Switches

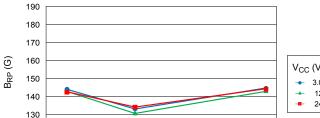
A1122 Magnetic Characteristics



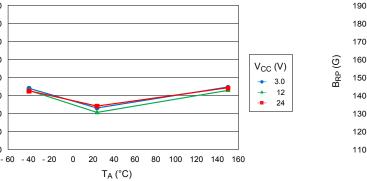


Average Release Point versus Average Supply Voltage

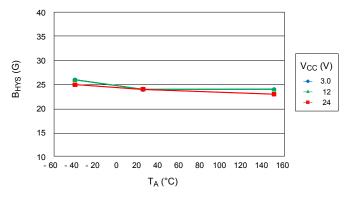
Average Operate Point versus Average Supply Voltage



Average Release Point versus Ambient Temperature



Average Switchpoint Hysteresis versus Ambient Temperature



14

V_{CC} (V)

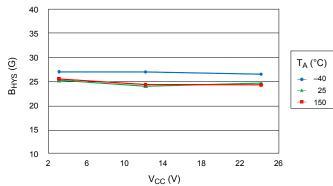
18

22

2

6

10



Average Switchpoint Hysteresis versus Supply Voltage



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120

110

T_A (°C)

25 .

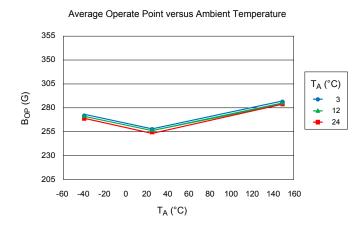
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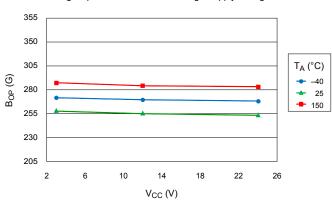
• 150

26

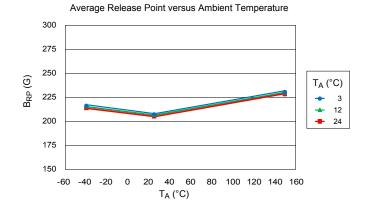
Chopper-Stabilized Precision Hall-Effect Switches



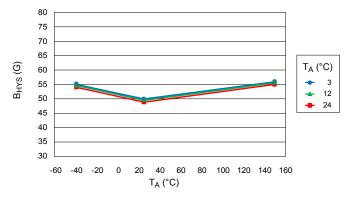




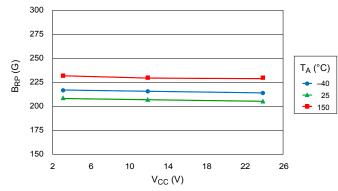
Average Operate Point versus Average Supply Voltage



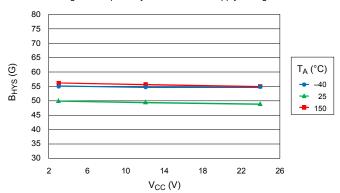
Average Switchpoint Hysteresis versus Ambient Temperature



Average Release Point versus Average Supply Voltage



Average Switchpoint Hysteresis versus Supply Voltage





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Chopper-Stabilized Precision Hall-Effect Switches

FUNCTIONAL DESCRIPTION

Operation

The output of the A1120, A1121, A1122, and A1123 devices switches low (turns on) when a magnetic field perpendicular to the Hall element exceeds the operate point threshold, B_{OP} (see panel A of figure 1). When the magnetic field is reduced below the release point, B_{RP} , the device output goes high (turns off). The output of the A1125 devices switches high (turns off) when a magnetic field perpendicular to the Hall element exceeds the operate point threshold, B_{OP} (see panel B of figure 1). When the magnetic field is reduced below the release point threshold, B_{OP} (see panel B of figure 1). When the magnetic field is reduced below the release point, B_{RP} , the device output goes low (turns on).

After turn-on, the output voltage is $V_{OUT(SAT)}$. The output transistor is capable of sinking current up to the short circuit current limit, I_{OM} , which is a minimum of 30 mA.

The difference in the magnetic operate and release points is the hysteresis, B_{HYS} , of the device. This built-in hysteresis allows clean switching of the output even in the presence of external mechanical vibration and electrical noise. Powering-on the device in the hysteresis range (less than B_{OP} and higher than B_{RP}) will

give an indeterminate output state. The correct state is attained after the first excursion beyond B_{OP} or B_{RP} .

Applications

It is strongly recommended that an external bypass capacitor be connected (in close proximity to the Hall element) between the supply and ground of the device to reduce external noise in the application. As is shown in panel B of figure 1, a 0.1 μ F capacitor is typical.

Extensive applications information for Hall effect devices is available in:

- Hall-Effect IC Applications Guide, Application Note 27701
- *Guidelines for Designing Subassemblies Using Hall-Effect Devices*, Application Note 27703.1
- Soldering Methods for Allegro's Products SMT and Through-Hole, Application Note 26009

All are provided on the Allegro website, www.allegromicro.com.

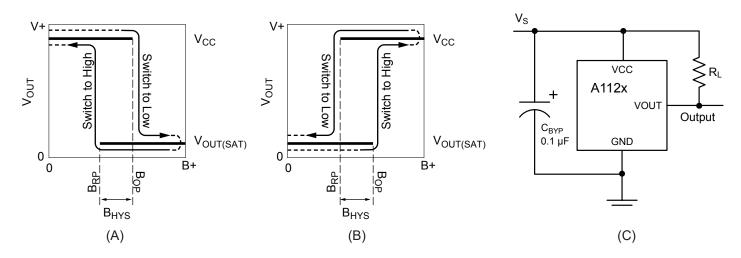


Figure 1. Device switching behavior. In panels A and B, on the horizontal axis, the B+ direction indicates increasing south polarity magnetic field strength. This behavior can be exhibited when using an electrical circuit such as that shown in panel C.



Chopper Stabilization Technique

When using Hall effect technology, a limiting factor for switchpoint accuracy is the small signal voltage developed across the Hall element. This voltage is disproportionally small relative to the offset that can be produced at the output of the Hall element. This makes it difficult to process the signal while maintaining an accurate, reliable output over the specified operating temperature and voltage ranges.

Chopper stabilization is a unique approach used to minimize Hall offset on the chip. The Allegro technique, namely Dynamic Quadrature Offset Cancellation, removes key sources of the output drift induced by thermal and mechanical stresses. This offset reduction technique is based on a signal modulation-demodulation process. The undesired offset signal is separated from the magnetic field-induced signal in the frequency domain, through modulation. The subsequent demodulation acts as a modulation process for the offset, causing the magnetic field induced signal to recover its original spectrum at baseband, while the dc offset becomes a high-frequency signal. The magnetic sourced signal then can pass through a low-pass filter, while the modulated DC offset is suppressed. This configuration is illustrated in figure 2. The chopper stabilization technique uses a 400 kHz high frequency clock. For demodulation process, a sample and hold technique is used, where the sampling is performed at twice the chopper frequency (800 kHz). This high-frequency operation allows a greater sampling rate, which results in higher accuracy and faster signal-processing capability. This approach desensitizes the chip to the effects of thermal and mechanical stresses, and produces devices that have extremely stable quiescent Hall output voltages and precise recoverability after temperature cycling. This technique is made possible through the use of a BiCMOS process, which allows the use of low-offset, low-noise amplifiers in combination with high-density logic integration and sample-and-hold circuits.

The repeatability of magnetic field-induced switching is affected slightly by a chopper technique. However, the Allegro high frequency chopping approach minimizes the affect of jitter and makes it imperceptible in most applications. Applications that are more likely to be sensitive to such degradation are those requiring precise sensing of alternating magnetic fields; for example, speed sensing of ring-magnet targets. For such applications, Allegro recommends its digital device families with lower sensitivity to jitter. For more information on those devices, contact your Allegro sales representative.

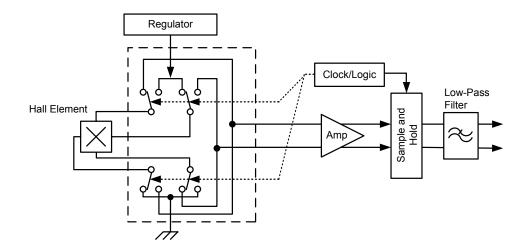


Figure 2. Model of chopper stabilization technique



Power Derating

The device must be operated below the maximum junction temperature of the device, $T_{J(max)}$. Under certain combinations of peak conditions, reliable operation may require derating supplied power or improving the heat dissipation properties of the application. This section presents a procedure for correlating factors affecting operating T_J . (Thermal data is also available on the Allegro MicroSystems website.)

The Package Thermal Resistance, $R_{\theta JA}$, is a figure of merit summarizing the ability of the application and the device to dissipate heat from the junction (die), through all paths to the ambient air. Its primary component is the Effective Thermal Conductivity, K, of the printed circuit board, including adjacent devices and traces. Radiation from the die through the device case, $R_{\theta JC}$, is relatively small component of $R_{\theta JA}$. Ambient air temperature, T_A , and air motion are significant external factors, damped by overmolding.

The effect of varying power levels (Power Dissipation, P_D), can be estimated. The following formulas represent the fundamental relationships used to estimate T_J , at P_D .

$$P_{D} = V_{IN} \times I_{IN} \qquad (1)$$

$$\Delta T = P_{D} \times R_{\theta JA} \qquad (2)$$

$$T_{J} = T_{A} + \Delta T \qquad (3)$$

For example, given common conditions such as: $T_A = 25^{\circ}C$, $V_{CC} = 12 \text{ V}$, $I_{CC} = 1.6 \text{ mA}$, and $R_{\theta JA} = 165^{\circ}C/W$, then:

 $P_D = V_{CC} \times I_{CC} = 12 \text{ V} \times 1.6 \text{ mA} = 19 \text{ mW}$

 $\Delta T = P_D \times R_{\theta JA} = 19 \text{ mW} \times 165^{\circ}\text{C/W} = 3^{\circ}\text{C}$

 $T_{J} = T_{A} + \Delta T = 25^{\circ}C + 3^{\circ}C = 28^{\circ}C$

A worst-case estimate, $P_{D(max)}$, represents the maximum allowable power level ($V_{CC(max)}$, $I_{CC(max)}$), without exceeding $T_{J(max)}$, at a selected $R_{\theta JA}$ and T_A .

Example: Reliability for V_{CC} at T_A =150°C, package LH, using a minimum-K PCB.

Observe the worst-case ratings for the device, specifically: $R_{\theta JA}=228$ °C/W, $T_J(max)=165$ °C, $V_{CC}(max)=24$ V, and $I_{CC}(max)=4$ mA.

Calculate the maximum allowable power level, $P_D(max)$. First, invert equation 3:

 $\Delta T_{max} = T_J(max) - T_A = 165 \circ C - 150 \circ C = 15 \circ C$

This provides the allowable increase to T_J resulting from internal power dissipation. Then, invert equation 2:

$$P_D(max) = \Delta T_{max} \div R_{\theta JA} = 15^{\circ}C \div 228 \text{ }^{\circ}C/W = 66 \text{ mW}$$

Finally, invert equation 1 with respect to voltage:

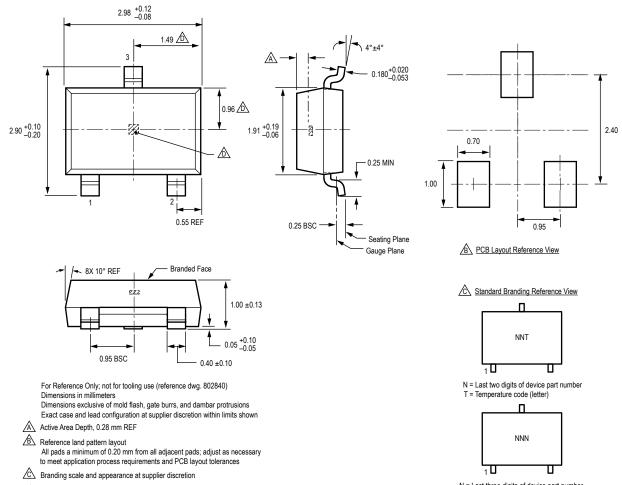
$$V_{CC(est)} = P_D(max) \div I_{CC}(max) = 66 \text{ mW} \div 4 \text{ mA} = 16.5 \text{ V}$$

The result indicates that, at T_A , the application and device can dissipate adequate amounts of heat at voltages $\leq V_{CC(est)}$.

Compare $V_{CC(est)}$ to $V_{CC}(max)$. If $V_{CC(est)} \leq V_{CC}(max)$, then reliable operation between $V_{CC(est)}$ and $V_{CC}(max)$ requires enhanced $R_{\theta JA}$. If $V_{CC(est)} \geq V_{CC}(max)$, then operation between $V_{CC(est)}$ and $V_{CC}(max)$ is reliable under these conditions.



Package LH, 3-Pin (SOT-23W)

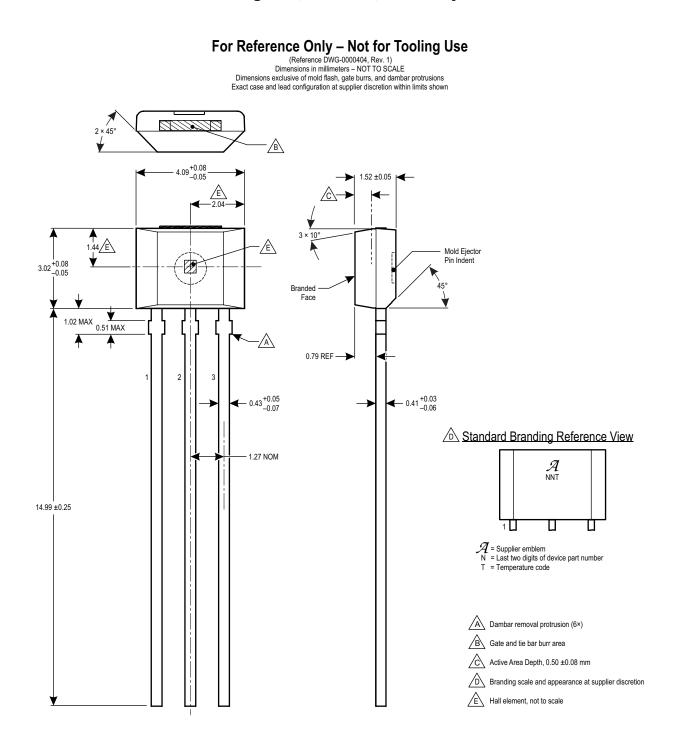




N = Last three digits of device part number

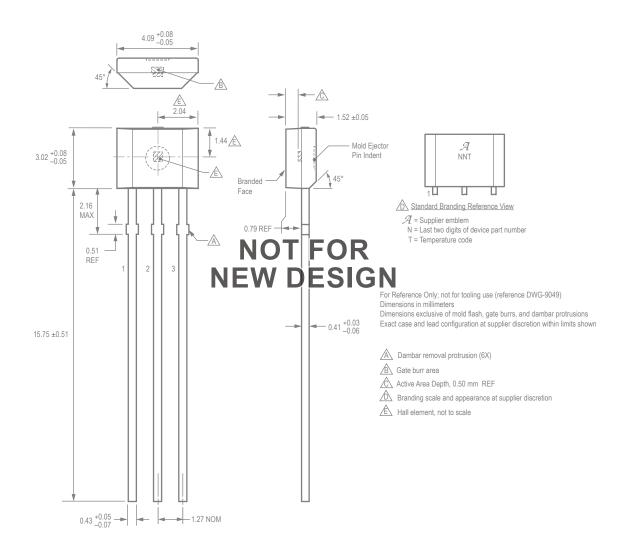


Package UA, 3-Pin SIP, Matrix Style





Package UA, 3-Pin SIP, Chopper Style





Chopper-Stabilized Precision Hall-Effect Switches

Revision History

Number	Date	Description
15	September 3, 2013	Update product offerings; Update UA package drawing
16	September 16, 2015	Added AEC-Q100 qualification under Features and Benefits
17	November 4, 2016	Chopper-style UA package designated as not for new design
18	February 15, 2019	Minor editorial updates
19	March 6, 2020	Minor editorial updates

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