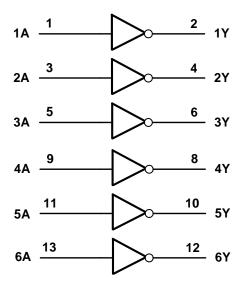


Pin Descriptions

Pin Number	Pin Name	Function
1	1A	Data Input
2	1Y	Data Output
3	2A	Data Input
4	2Y	Data Output
5	3A	Data Input
6	3Y	Data Output
7	GND	Ground
8	4Y	Data Output
9	4A	Data Input
10	5Y	Data Output
11	5A	Data Input
12	6Y	Data Output
13	6A	Data Input
14	Vcc	Supply Voltage

Logic Diagram



Function Table

Input	Output
Α	Υ
Н	L
L	Н



Absolute Maximum Ratings (Note 4) (@T_A = +25°C, unless otherwise specified.)

Symbol	Description	Rating	Unit
ESD HBM	Human Body Model ESD Protection	2	KV
ESD CDM	Charged Device Model ESD Protection	1	KV
ESD MM	Machine Model ESD Protection	200	V
Vcc	Supply Voltage Range	-0.5 to +7.0	V
VI	Input Voltage Range (Note 5)	-0.5 to +7.0	V
I _{IK}	Input Clamp Current $V_1 < -0.5V$ or $V_1 > V_{CC} +0.5V$	±20	mA
I _{OK}	Output Clamp Current $V_O < -0.5V$ or $V_O > V_{CC} +0.5V$	±20	mA
lo	Continuous Output Current -0.5V < V _O V _{CC} +0.5V	±25	mA
Icc	Continuous Current Through Vcc	50	mA
I _{GND}	Continuous Current Through GND	-50	mA
TJ	Operating Junction Temperature	-40 to +150	°C
T _{STG}	Storage Temperature	-65 to +150	°C
P _{TOT}	Total Power Dissipation	500	mW

Notes:

Recommended Operating Conditions (Note 6) (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Conditions	Min	Max	Unit
Vcc	Supply Voltage	_	2.0	6.0	V
VI	Input Voltage		0	Vcc	V
Vo	Output Voltage	_	0	V_{CC}	V
		$V_{CC} = 2.0V$	_	625	
Δt/ΔV	Input Transition Rise or Fall Rate	$V_{CC} = 4.5V$	_	140	ns/V
		$V_{CC} = 6.0V$	_	85	
T _A	Operating Free-Air Temperature	_	-40	+125	°C

Note:

6. Unused inputs should be held at $V_{\mbox{\footnotesize CC}}$ or Ground.

^{4.} Stresses beyond the absolute maximum may result in immediate failure or reduced reliability. These are stress values and device operation should be within recommend values.

^{5.} Input Voltage cannot exceed V_{CC} to the extent the Maximum clamp current is exceeded.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Test Conditions	V	T _A = -40°	C to +85°C	$T_A = -40^{\circ}C \text{ to } +125^{\circ}C$		Unit
Symbol	Farameter	rest Conditions	V _{CC}	Min	Max	Min	Max	Unit
	Link Lavallanut	_	2.0V	1.7	_	1.7	_	
V_{IH}	High-Level Input Voltage	—	4.5V	3.6	_	3.6		V
	Voltago	_	6.0V	4.8	_	4.8	_	
	Low Lovel Input	_	2.0V	_	0.3	_	0.3	
VIL	Low-Level Input Voltage	_	4.5V	_	0.9	_	0.9	V
	voltago	_	6.0V	_	1.2	_	1.2	
		$I_{OH} = -20\mu A$	2.0V	1.8	_	1.9		
		$I_{OH} = -20\mu A$	4.5V	4.0	_	4.0	_	
V _{OH}	High-Level Output Voltage	$I_{OH} = -20\mu A$	6.0V	5.5	_	5.5	_	V
	Voltage	$I_{OH} = -4.0 \text{mA}$	4.5V	3.84	_	3.7	_	
		$I_{OH} = -5.2 \text{mA}$	6.0V	5.34	_	5.2	_	
		$I_{OL} = 20\mu A$	2.0V	_	0.2	_	0.2	
		$I_{OL} = 20\mu A$	4.5V	_	0.5	_	0.5	
VoL	Low-Level Output Voltage	$I_{OL} = 20\mu A$	6.0V	_	0.5	_	0.5	V
	Voltage	$I_{OL} = 4mA$	4.5V	_	0.33	_	0.40	
		$I_{OL} = 5.2 \text{mA}$	6.0V	_	0.33	_	0.40	
II	Input Current	$V_I = GND$ to 5.5V	6.0V	_	±1	_	±1	μA
Icc	Supply Current	$V_I = GND \text{ or } V_{CC}, I_O = 0$	6.0V	_	20	_	40	μA

Switching Characteristics

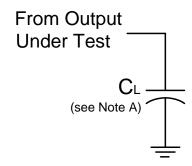
Symbol Barameter		Test	V	-	Γ _A = +25°C	;	-40°C to +85°C	-40°C to +125°C	Unit
Syllibol	Symbol Parameter Condition		V _{CC}	Min	Тур	Max	Max	Max	Unit
	Decrease tier Delevi	Ciauro 1	2.0V	_	19	70	90	105	
t _{PD}	Propagation Delay An to Yn	Figure 1 $C_L = 50pF$	4.5V	_	7	14	18	21	ns
	ANIOTN		6.0V	_	5	12	15	18	
		Ciauro 1	2.0V	_	19	75	95	110	
t _t		Figure 1 $C_L = 50pF$	4.5V	_	7	15	19	22	ns
		CL = 50pr	6.0V	_	6	13	16	19	

Operating Characteristics (@T_A = +25°C, unless otherwise specified.)

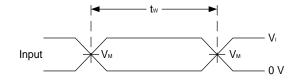
Parameter		Test Conditions	V _{CC} = 6V Typ	Unit
C _{pd}	Power Dissipation Capacitance per Gate	f = 1MHz	10	pF
Cı	Input Capacitance	$V_I = V_{CC} - \text{ or GND}$	4	pF



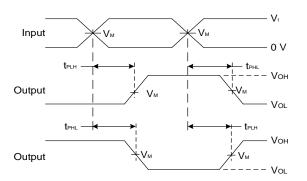
Parameter Measurement Information



V	, Inputs		V	C.	
VCC	VI	t _r /t _f	V _M	OL.	
2.0V to 6.0V	Vcc	6ns	V _{CC} /2	15pF, 50pF	



Voltage Waveform Pulse Duration



Voltage Waveform Propagation Delay Times
Inverting and Non Inverting Outputs

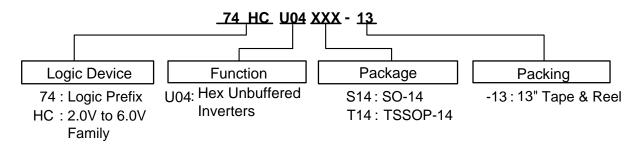
A. Includes test lead and test apparatus capacitance. Notes:

- B. All pulses are supplied at pulse repetition rate ≤ 1 MHz.
 C. Inputs are measured separately one transition per measurement.
- D. t_{PLH} and t_{PHL} are the same as t_{PD}.

Figure 1 Load Circuit and Voltage Waveforms



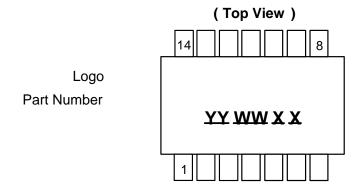
Ordering Information



Device Package Code		Packaging	7" Tape and Reel	
Device	Package Code	(Note 7)	Quantity	Part Number Suffix
74HCU04S14-13	S14	SO-14	2500/Tape & Reel	-13
74HCU04T14-13	T14	TSSOP-14	2500/Tape & Reel	-13

Marking Information

(1) SO-14, TSSOP-14



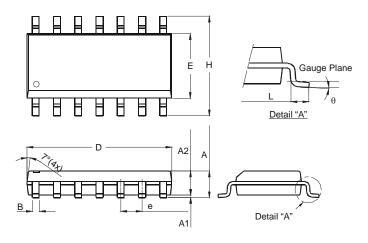
Part Number	Package
74HCU04S14	SO-14
74HCU04T14	TSSOP-14



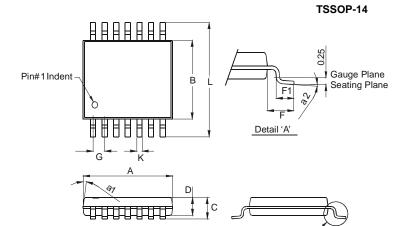
Package Outline Dimensions (All dimensions in mm.)

Please see http://www.diodes.com/package-outlines.html for the latest version.





	SO-14					
Dim	Min	Max				
Α	1.47	1.73				
A1	0.10	0.25				
A2	A2 1.45 Typ					
В	0.33	0.51				
D	8.53	8.74				
Е	3.80	3.99				
е	1.27	Тур				
Н	5.80	6.20				
L	0.38	1.27				
θ	0°	8°				
All Di	All Dimensions in mm					

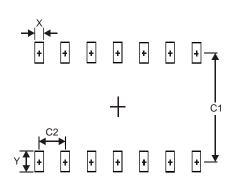


TSSOP-14						
Dim	Min	Max				
a1	7° (4X)				
a2	0°	8°				
Α	4.9	5.10				
В	4.30	4.50				
С	-	1.2				
D	0.8	1.05				
F	1.00	Тур				
F1	0.45	0.75				
G	0.65 Typ					
K	0.19	0.30				
L 6.40 Typ						
All Dir	nensions	s in mm				



Suggested Pad Layout

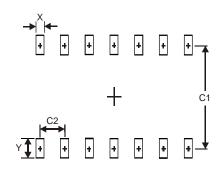
Please see http://www.diodes.com/package-outlines.html for the latest version.



SO-14

Dimensions	Value (in mm)
Х	0.60
Y	1.50
C1	5.4
C2	1.27

TSSOP-14



Dimensions	Value (in mm)
X	0.45
Υ	1.45
C1	5.9
C2	0.65



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