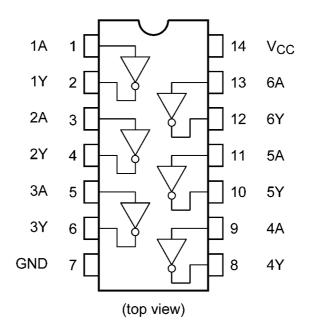
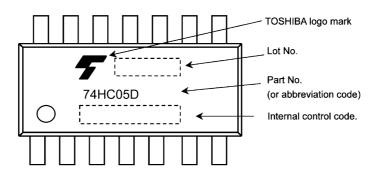
5. Pin Assignment

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6. Marking



7. IEC Logic Symbol

1A	1 \land	(<u>2)</u> 1Y
2A_(3)	· <u>×</u>	(4) 2Y
3A_(5)		(6) _{3Y}
4A_(9)		(8)4Y
5A_(11)		(10) 5Y
6A (13)		(12) ₆ Y

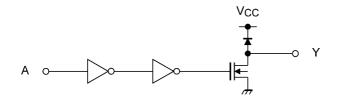
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8. Truth Table

	А	Y
	L	Z
	Н	L
7.	High impe	dance

Z: High impedance

9. System Diagram (per gate)



10. Absolute Maximum Ratings (Note)

Characteristics	Symbol	Note	Rating	Unit
Supply voltage	V _{CC}		-0.5 to 7.0	V
Input voltage	V _{IN}		-0.5 to V _{CC} + 0.5	V
Output voltage	V _{OUT}		-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}		±20	mA
Output diode current	I _{ОК}		±20	mA
Output current	I _{OUT}		±25	mA
V _{CC} /ground current	I _{CC}		±50	mA
Power dissipation	PD	(Note 1)	500	mW
Storage temperature	T _{stg}		-65 to 150	C°

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: P_D derates linearly with -8 mW/°C above 85 °C

11. Operating Ranges (Note)

Characteristics	Characteristics Symbol		Rating	Unit
Supply voltage	V _{CC}	—	2.0 to 6.0	V
Input voltage	V _{IN}	—	0 to V _{CC}	V
Output voltage	V _{OUT}	—	0 to V _{CC}	V
Operating temperature	T _{opr}	_	-40 to 125	°C
Input rise and fall times	t _r ,t _f	—	0 to 50	μS

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs and bus inputs must be tied to either V_{CC} or GND.

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12. Electrical Characteristics

12.1. DC Characteristics (Unless otherwise specified, $T_a = 25 \text{ °C}$)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
High-level input voltage	V _{IH}	—		2.0	1.50	_	_	V
				4.5	3.15	—	_	
				6.0	4.20	_		
Low-level input voltage	VIL	—		2.0	_	_	0.50	~
				4.5	_	_	1.35	
				6.0	_	_	1.80	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 20 μA	2.0	_	0.0	0.1	~
				4.5	—	0.0	0.1	
				6.0	—	0.0	0.1	
			I_{OL} = 4 mA	4.5	—	0.17	0.26	
			I _{OL} = 5.2 mA	6.0	—	0.18	0.26	
Output OFF-state leakage current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC}		6.0	—	—	±0.5	μA
Input leakage current	I _{IN}	$V_{IN} = V_{CC}$ or GND		6.0		_	±0.1	μA
Quiescent supply current	I _{CC}	$V_{IN} = V_{CC}$ or GND		6.0		_	1.0	μA

12.2. DC Characteristics (Unless otherwise specified, T_a = -40 to 85 °C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	—		2.0	1.50	—	V
				4.5	3.15	—	
				6.0	4.20	_	
Low-level input voltage	VIL	—		2.0		0.50	
				4.5		1.35	
				6.0		1.80	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 20 μA	2.0	_	0.1	V
				4.5	_	0.1	
				6.0	_	0.1	
			I _{OL} = 4 mA	4.5	_	0.33	
			I _{OL} = 5.2 mA	6.0	_	0.33	
Output OFF-state leakage current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC}		6.0	_	±5.0	μΑ
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_	±1.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	_	10.0	μA

12.3. DC Characteristics (Unless otherwise specified, $T_a = -40$ to 125 °C)

Characteristics	Symbol	Test Condit	ion	V _{CC} (V)	Min	Max	Unit
High-level input voltage	V _{IH}	—		2.0	1.50	—	V
				4.5	3.15		
				6.0	4.20		
Low-level input voltage	VIL	—		2.0	_	0.50	V
				4.5	_	1.35	
				6.0	_	1.80	
Low-level output voltage	V _{OL}	V _{IN} = V _{IH}	I _{OL} = 20 μA	2.0	_	0.1	V
				4.5	—	0.1	
				6.0	_	0.1	
			I _{OL} = 4 mA	4.5	_	0.4	
			I _{OL} = 5.2 mA	6.0	_	0.4	
Output OFF-state leakage current	I _{OZ}	V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC}		6.0	—	±10.0	μA
Input leakage current	I _{IN}	V _{IN} = V _{CC} or GND		6.0	_	±1.0	μA
Quiescent supply current	I _{CC}	V _{IN} = V _{CC} or GND		6.0	_	40.0	μA

12.4. AC Characteristics (Unless otherwise specified, C_L =15 pF, V_{CC} = 5 V, T_a = 25 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	—	_	4	8	ns
Propagation delay time	t _{PLZ}	R _L = 1 kΩ	_	8	15	ns
	t _{PZL}		_	6	15	

12.5. AC Characteristics (Unless otherwise specified, C_L =50 pF, T_a = 25 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	Note	Test Condition	V _{CC} (V)	Min	Тур.	Max	Unit
Output transition time	t_{TLH}, t_{THL}		—	2.0	_	30	75	ns
				4.5	—	8	15	
				6.0	—	7	13	
Propagation delay time	t _{PLZ}		$R_L = 1k\Omega$	2.0	—	20	90	ns
				4.5	—	11	18	
				6.0	_	10	15	
Propagation delay time	t _{PZL}		$R_L = 1k\Omega$	2.0	—	33	90	ns
				4.5	_	9	18	
				6.0	_	7	15	
Input capacitance	C _{IN}		—		—	5	10	pF
Output capacitance	C _{OUT}		_		_	3	_	pF
Power dissipation capacitance	C _{PD}	(Note 1)	_		_	7	_	рF

Note 1: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load. Average operating current can be obtained by the equation. $I_{CC(opr)} = C_{PD} \times V_{CC} \times f_{IN} + I_{CC}/6$ (per gate)

12.6. AC Characteristics

(Unless otherwise specified, C_L =50 pF, T_a = -40 to 85 °C, Input: t_r = t_f = 6 ns)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	—	2.0	—	95	ns
			4.5	_	19	
			6.0	_	16	
Propagation delay time	t _{PLZ}	$R_L = 1k\Omega$	2.0	_	115	ns
			4.5	—	23	
			6.0	_	20	
Propagation delay time	t _{PZL}	$R_L = 1k\Omega$	2.0	_	115	ns
			4.5	_	23]
			6.0	_	20	

12.7. AC Characteristics

(Unless otherwise specified, C_L =50 pF, T_a = -40 to 125 °C, Input: t_r = t_f = 6 ns)

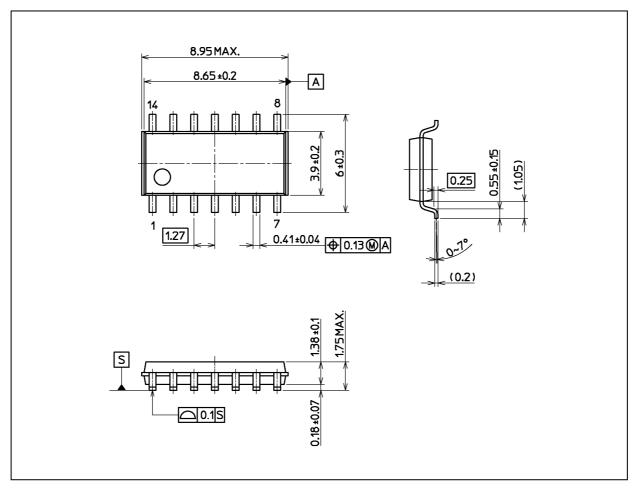
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Output transition time	t _{TLH} ,t _{THL}	_	2.0	_	110	ns
			4.5	_	22	
			6.0	_	19	
Propagation delay time	t _{PLZ}	R _L = 1 kΩ	2.0	_	135	ns
			4.5	_	27	
			6.0	_	23	
Propagation delay time	t _{PZL}	R _L = 1 kΩ	2.0	_	135	ns
			4.5	_	27	
			6.0	_	23	



Package Dimensions

74HC05D

Unit: mm



Weight: 0.13 g (typ.)

Package Name(s) Nickname: SOIC14

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