

The TISP70xxL1 is guaranteed to withstand the listed international ESD (ElectroStatic Discharge), and lightning impulses in both polarities. Terminals marked NC do not have any internal connections and may be left floating or tied to some circuit point. The TISP7038L1 is a functional replacement for the TPN3021.

#### Absolute Maximum Ratings, T<sub>J</sub> = 25 °C (Unless Otherwise Noted)

Rating	Symbol	Value	Unit
Repetitive peak off-state voltage TISP7015L1 TISP7038L1	V <sub>DRM</sub>	± 8 ± 28	V
Non-repetitive peak on-state pulse current (see Notes 1 and 2)			
2/10 (Telcordia GR-1089-CORE, 2/10 voltage wave shape)		200	
1/20 (ITU-T K.22, 1.2/50 voltage wave shape, also VDE0878)		100	
8/20 (IEC 61000-4-5, Figure 12 generator, 1.2/50 voltage wave shape)	lana	100	Α
10/160 (TIA/EIA-IS-968 (formally FCC Part 68), 10/160 voltage wave shape)	I <sub>PPSM</sub>	75	
5/310 (ITU-T k.20/21, 10/700 voltage wave shape, also IEC 61000-4-5 and VDE0433)		50	
10/560 (TIA/EIA-IS-968 (formally FCC Part 68), 10/560 voltage wave shape)		40	
10/1000 (Telcordia GR-1089-CORE, 10/1000 voltage wave shape)		30	
Non-repetitive peak on-state current (see Note 1)			
16.7 ms (60 Hz) full sine wave		9	
20 ms (50 Hz) full sine wave	I <sub>TSM</sub>	8	Α
0.2 s 50 Hz/60 Hz a.c.	TOW	3	
2.0 s 50 Hz/60 Hz a.c.		1.5	
Junction temperature		-40 to +150	°C
Storage temperature range		-65 to +150	°C

NOTES: 1. Initially the TISP70xxL1 must be in thermal equilibrium at the specified TA. The surge may be repeated after the TISP70xxL1 returns to its initial conditions.

2. These non-repetitive rated currents are peak values of either polarity.

#### EMC Immunity Test Ratings, TA = 25 °C (Unless Otherwise Noted)

Rating		Value	Unit
Level 3 open-circuit voltage, IEC 61000-4-2, 2001-4, ESD generator, also ITU-T K.20			
contact discharge air discharge	V <sub>O/C</sub>	6 8	kV

### Electrical Characteristics, T<sub>J</sub> = 25 °C (Unless Otherwise Noted)

	Parameter Test Conditions		Min	Тур	Max	Unit
I <sub>DRM</sub>	Repetitive peak off- state current	$V_D = \pm V_{DRM}$			±4	μΑ
V <sub>(BO)</sub>	Breakover voltage	$dv/dt = \pm 250 \text{ V/ms}, \text{ R}_{\mbox{SOURCE}} = 300 \ \Omega \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \$			±15 ±38	V
I <sub>(BO)</sub>	Breakover current	dv/dt = $\pm 250$ V/ms, R <sub>SOURCE</sub> = 300 $\Omega$			±300	mA
I <sub>H</sub>	Holding current	$IT = \pm 5 \text{ A}$ , $di/dt = \pm 30 \text{ mA/ms}$	±30			mA

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#### Electrical Characteristics, T<sub>J</sub> = 25 °C (Unless Otherwise Noted)

	Parameter	Test Conditions		Min	Тур	Max	Unit
C <sub>KA</sub>	Off-state capacitance	f = 1 MHz, V <sub>d</sub> = 1 V rms, V <sub>D</sub> = 0 (see Note 3)	TISP7015L1 TISP7038L1		24 17		pF

NOTE 3: Value for any terminal pair, three-terminal guarded measurement with zero voltage bias on the unmeasured terminal.

#### **Thermal Characteristics**

Paramete	er	Test Conditions	Min	Тур	Max	Unit
$R_{\Theta JA}$ Junction to	free air thermal resistance	P <sub>tot</sub> = 0.8 W, T <sub>A</sub> = 25 °C, 5 cm <sup>2</sup> , FR4 PCB			170	°C/W

#### **Parameter Measurement Information**

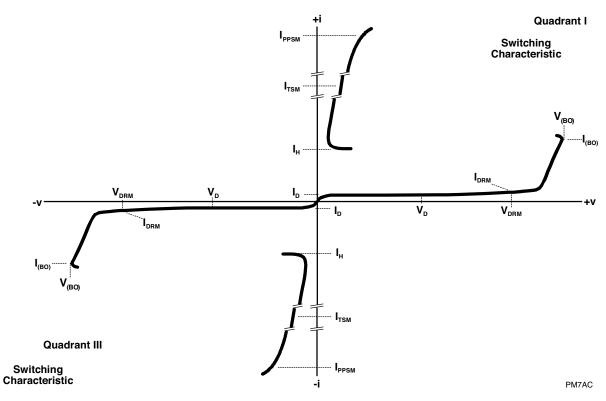
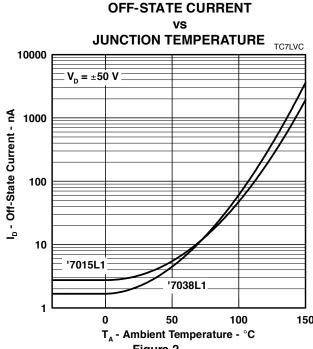


Figure 1. Voltage-Current Characteristic for any Terminal Pair

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#### **Typical Characteristics**



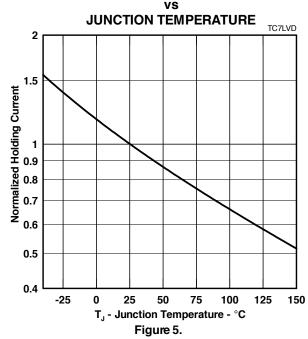
# 150 Figure 2.

## **ON-STATE CURRENT** vs **ON-STATE VOLTAGE** TC7LVB 100 $t_W = 100 \mu s$ T<sub>A</sub> = 25 °C I- On-State Current - A 0.1 2 5 6 7 8 910 4 20 V<sub>τ</sub> - On-State Voltage - V Figure 4.

## NORMALIZED BREAKOVER VOLTAGE **JUNCTION TEMPERATURE** TC7LVE 1.10 Normalized Breakover Voltage '7038L1 1.05 1.00 '7015L1 0.95 -25 50 75 100 125 150 T<sub>.1</sub> - Junction Temperature - °C

## NORMALIZED HOLDING CURRENT

Figure 3.



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#### **Typical Characteristics**

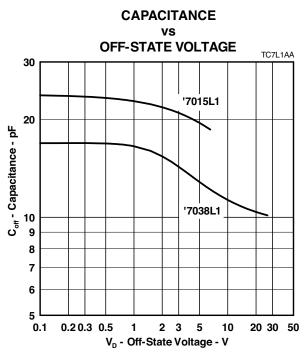


Figure 6.

#### **Rating and Thermal Information**

## NON-REPETITIVE PEAK ON-STATE CURRENT

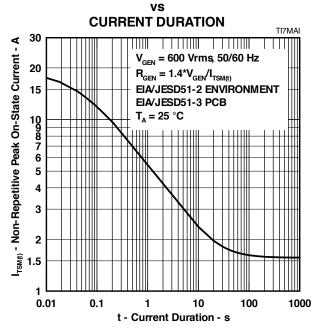


Figure 7.

## **V**<sub>DRM</sub> **DERATING FACTOR**

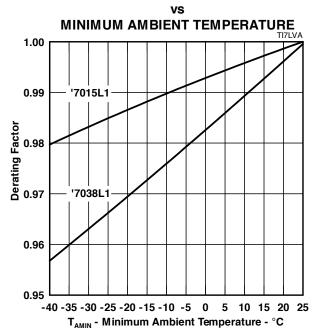


Figure 8.

#### **MECHANICAL DATA**

#### **Device Symbolization Code**

Devices will be coded as below.

Device	Symbolization Code
TISP7015L1DR-S	7015L1
TISP7038L1DR-S	7038L1

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