Ms	vim	uım	Rati	nae
IVIC		IUIII	Nau	Hys

Τ.	=	25	°C	

A			
Parameter	Symbol		Values
Operating temperature	T <sub>op</sub>	min.	-40 °C
	ор	max.	100 °C
Storage temperature	T <sub>stg</sub>	min.	-40 °C
	olg	max.	100 °C
Forward current	I <sub>F</sub>	max.	100 mA
Surge current	I <sub>FSM</sub>	max.	1 A
$t_p \le 300  \mu\text{s};  D = 0$	1 0111		
Reverse voltage 3)	$V_R$	max.	5 V
Power consumption	P <sub>tot</sub>	max.	180 mW
ESD withstand voltage	V <sub>ESD</sub>	max.	2 kV
acc. to ANSI/ESDA/JEDEC JS-001 (HBM, Class 2)			



## **Characteristics**

 $I_{_{\rm F}}$  = 100 mA;  $t_{_{
m p}}$  = 20 ms;  $T_{_{
m A}}$  = 25 °C

Parameter	Symbol	Values	
Peak wavelength	$\lambda_{peak}$	typ.	860 nm
Centroid wavelength	$\lambda_{ ext{centroid}}$	typ.	850 nm
Spectral bandwidth at 50% I <sub>rel,max</sub> (FWHM)	Δλ	typ.	30 nm
Half angle	φ	typ.	12 °
Dimensions of active chip area	LxW	typ.	0.3 x 0.3 mm x mm
Rise time (10% / 90%) $I_F = 100 \text{ mA}; R_L = 50 \Omega$	t,	typ.	12 ns
Fall time (10% / 90%) $I_F = 100 \text{ mA}; R_L = 50 \Omega$	t <sub>f</sub>	typ.	12 ns
Forward voltage 4)	$V_{F}$	typ. max.	1.5 V 1.7 V
Forward voltage $^{4)}$ I <sub>F</sub> = 1 A; t <sub>p</sub> = 100 µs	$V_{F}$	typ. max.	2.4 V 2.9 V
Reverse current <sup>3)</sup> V <sub>R</sub> = 5 V	I <sub>R</sub>	typ. max.	0.01 μA 10 μA
Radiant intensity <sup>1)</sup> $I_F = 1 \text{ A}; t_p = 25 \mu \text{s}$	l <sub>e</sub>	typ.	720 mW/sr
Total radiant flux 5)	Фе	typ.	60 mW
Temperature coefficient of voltage	$TC_{V}$	typ.	-0.7 mV / K
Temperature coefficient of brightness	TC,	typ.	-0.5 % / K
Temperature coefficient of wavelength	$TC_{\lambda}$	typ.	0.3 nm / K
Thermal resistance junction solder point real 6)	$R_{ ext{thJS real}}$	max.	180 K / W
Thermal resistance junction ambient real 7)	$R_{thJA}$	max.	340 K / W



# **Brightness Groups**

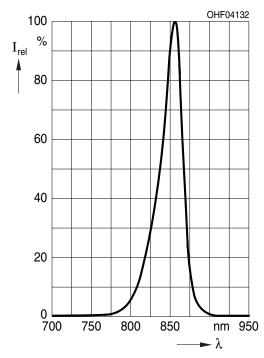
T<sub>A</sub> = 25 °C

Group	Radiant intensity $^{1)2)}$ $I_F = 100 \text{ mA}$ ; $t_p = 20 \text{ ms}$ min. $I_e$	Radiant intensity $^{1)2)}$ $I_F = 100 \text{ mA}; t_p = 20 \text{ ms}$ max. $I_e$
U	45 mW/sr	71 mW/sr
V	71 mW/sr	112 mW/sr
AW	112 mW/sr	180 mW/sr

Only one group in one packing unit (variation lower 2:1).

# Relative Spectral Emission 8), 9)

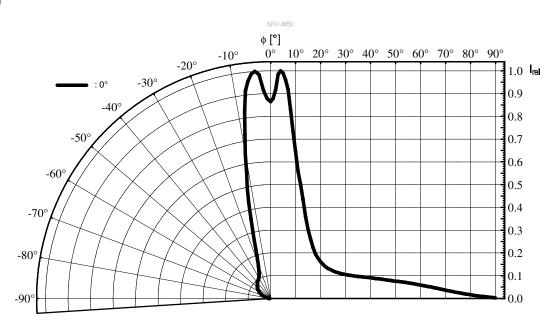
 $I_{e,rel} = f(\lambda); I_F = 100 \text{ mA}; t_p = 20 \text{ ms}$ 





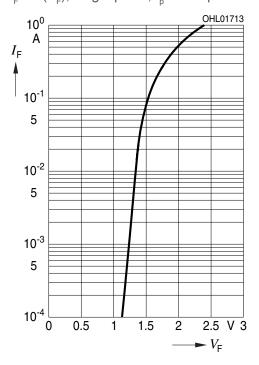
### Radiation Characteristics 8), 9)

$$I_{e,rel} = f(\phi)$$



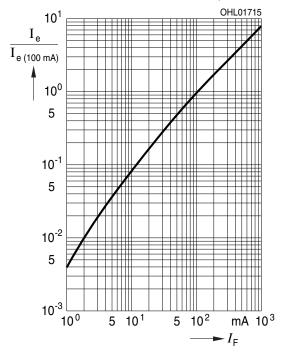
#### Forward current 8), 9)

 $I_F = f(V_F)$ ; single pulse;  $t_p = 100 \mu s$ 



# Relative Radiant Intensity 8), 9)

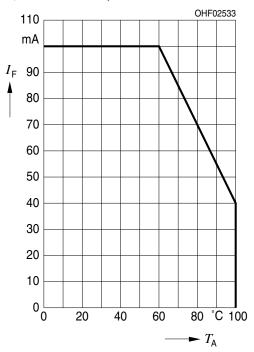
 $I_e/I_e(100 \text{mA}) = f(I_F)$ ; single pulse;  $t_p = 25 \mu \text{s}$ 





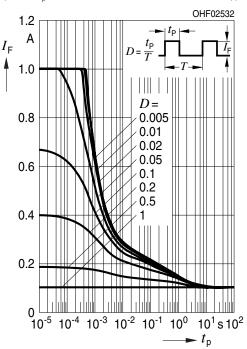
# Max. Permissible Forward Current

$$I_{F,max} = f(T_A)$$
; Rth<sub>ja</sub> = 340K / W; single pulse

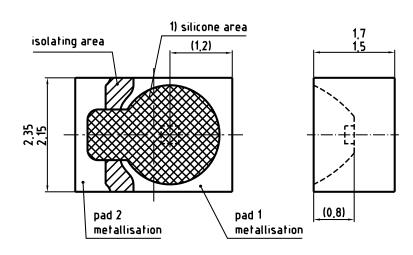


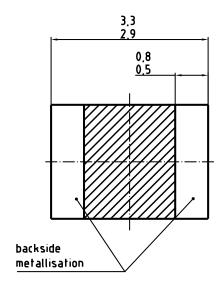
## **Permissible Pulse Handling Capability**

 $I_F = f(t_p)$ ; duty cycle D = parameter;  $T_A = 25$ °C



# **Dimensional Drawing** 10)





1) Device casted with silicone.

Ayoid mechanical stress on silicone surface.

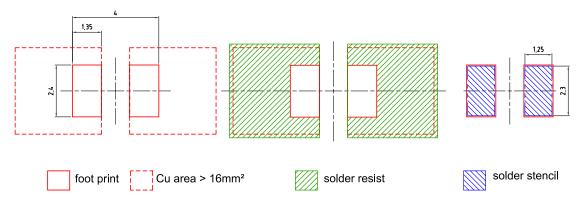
C63062-A3811-A1...-15

#### **Further Information:**

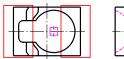
**Approximate Weight:** 23.0 mg

Pin	Description	
1	Anode	
2	Cathode	

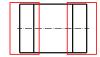
# Recommended Solder Pad 10)



Component Location on Pad





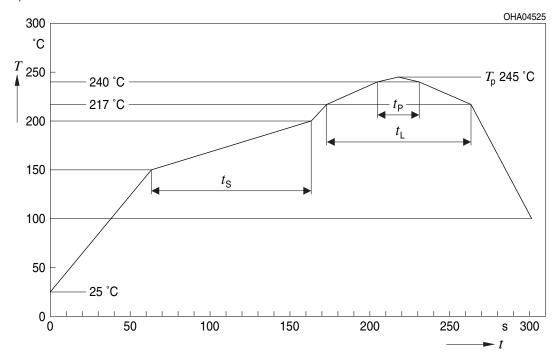


E062.3010.42-03

The package is casted with silicone. Mechanical stress at the surface of the unit should be avoided.

## **Reflow Soldering Profile**

Product complies to MSL Level 2 acc. to JEDEC J-STD-020E



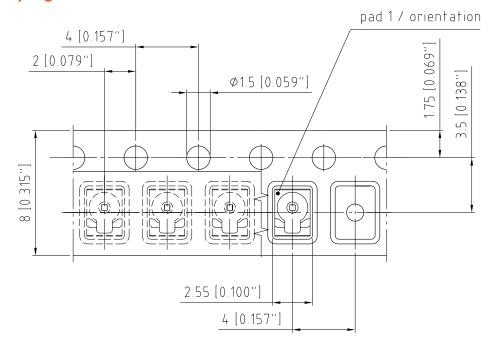
Profile Feature	Symbol	Pb	Unit		
		Minimum	Recommendation	Maximum	
Ramp-up rate to preheat*)	'		2	3	K/s
25 °C to 150 °C					
Time t <sub>s</sub>	t <sub>s</sub>	60	100	120	S
$T_{Smin}$ to $T_{Smax}$	-				
Ramp-up rate to peak*)			2	3	K/s
$T_{Smax}$ to $T_{P}$					
Liquidus temperature	$T_L$		217		°C
Time above liquidus temperature	$t_{\scriptscriptstyle L}$		80	100	S
Peak temperature	$T_{P}$		245	260	°C
Time within 5 °C of the specified peak temperature T <sub>P</sub> - 5 K	t <sub>P</sub>	10	20	30	S
			2		I//a
Ramp-down rate* $T_P$ to 100 °C			3	6	K/s
Time				480	S
25 °C to T <sub>P</sub>					

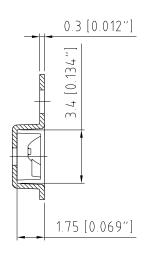
All temperatures refer to the center of the package, measured on the top of the component



<sup>\*</sup> slope calculation DT/Dt: Dt max. 5 s; fulfillment for the whole T-range

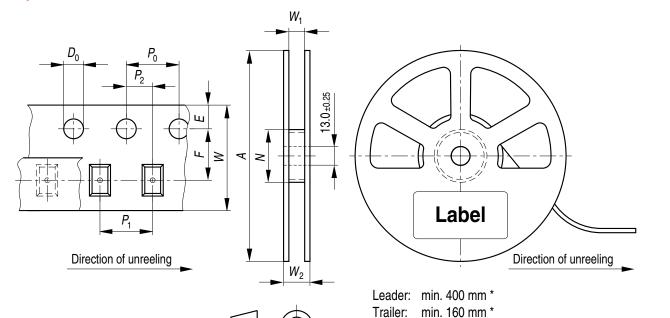
# Taping 10)





C63062-A3811-B7-03

# Tape and Reel 11)



## **Reel Dimensions**

Α	W		$N_{\min}$	$W_1$		$W_{2\text{max}}$	Pieces per PU
180 mm		8 + 0.3 / - 0.1 mm	60 mm		8.4 + 2 mm	14.4 mm	2000
330 mm		8 + 0.3 / - 0.1 mm	60 mm		8.4 + 2 mm	14.4 mm	9000

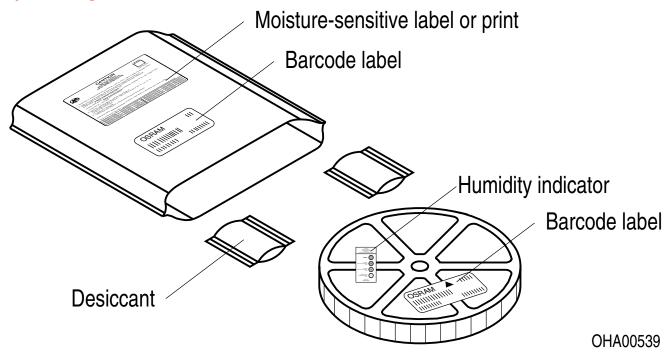
\*) Dimensions acc. to IEC 60286-3; EIA 481-D

OHAY0324

### **Barcode-Product-Label (BPL)**



## Dry Packing Process and Materials 10)



Moisture-sensitive product is packed in a dry bag containing desiccant and a humidity card according JEDEC-STD-033.



#### **Notes**

The evaluation of eye safety occurs according to the standard IEC 62471:2006 (photo biological safety of lamps and lamp systems). Within the risk grouping system of this IEC standard, the device specified in this data sheet falls into the class **exempt group (exposure time 10000 s)**. Under real circumstances (for exposure time, conditions of the eye pupils, observation distance), it is assumed that no endangerment to the eye exists from these devices. As a matter of principle, however, it should be mentioned that intense light sources have a high secondary exposure potential due to their blinding effect. When looking at bright light sources (e.g. headlights), temporary reduction in visual acuity and afterimages can occur, leading to irritation, annoyance, visual impairment, and even accidents, depending on the situation.

Subcomponents of this device contain, in addition to other substances, metal filled materials including silver. Metal filled materials can be affected by environments that contain traces of aggressive substances. Therefore, we recommend that customers minimize device exposure to aggressive substances during storage, production, and use. Devices that showed visible discoloration when tested using the described tests above did show no performance deviations within failure limits during the stated test duration. Respective failure limits are described in the IEC60810.

For further application related information please visit www.osram-os.com/appnotes



#### **Disclaimer**

#### Attention please!

The information describes the type of component and shall not be considered as assured characteristics. Terms of delivery and rights to change design reserved. Due to technical requirements components may contain dangerous substances.

For information on the types in question please contact our Sales Organization.

If printed or downloaded, please find the latest version on the OSRAM OS website.

#### **Packing**

Please use the recycling operators known to you. We can also help you – get in touch with your nearest sales office. By agreement we will take packing material back, if it is sorted. You must bear the costs of transport. For packing material that is returned to us unsorted or which we are not obliged to accept, we shall have to invoice you for any costs incurred.

#### Product and functional safety devices/applications or medical devices/applications

OSRAM OS components are not developed, constructed or tested for the application as safety relevant component or for the application in medical devices.

OSRAM OS products are not qualified at module and system level for such application.

In case buyer – or customer supplied by buyer – considers using OSRAM OS components in product safety devices/applications or medical devices/applications, buyer and/or customer has to inform the local sales partner of OSRAM OS immediately and OSRAM OS and buyer and /or customer will analyze and coordinate the customer-specific request between OSRAM OS and buyer and/or customer.



### Glossary

- Radiant intensity: Measured at a solid angle of  $\Omega$  = 0.01 sr
- 2) **Brightness:** The brightness values are measured with a tolerance of ±11%.
- Reverse Operation: Reverse Operation of 10 hours is permissible in total. Continuous reverse operation is not allowed.
- Forward Voltage: The forward voltages are measured with a tolerance of ±0.1 V.
- Total radiant flux: Measured with integrating sphere.
- Thermal resistance: junction soldering point, of the device only, mounted on an ideal heatsink (e.g. metal block)
- Thermal resistance: junction ambient, mounted on PC-board (FR4), padsize 16 mm² each
- Typical Values: Due to the special conditions of the manufacturing processes of semiconductor devices, the typical data or calculated correlations of technical parameters can only reflect statistical figures. These do not necessarily correspond to the actual parameters of each single product, which could differ from the typical data and calculated correlations or the typical characteristic line. If requested, e.g. because of technical improvements, these typ. data will be changed without any further notice.
- <sup>9)</sup> **Testing temperature:** TA = 25°C (unless otherwise specified)
- Tolerance of Measure: Unless otherwise noted in drawing, tolerances are specified with ±0.1 and dimensions are specified in mm.
- 11) **Tape and Reel:** All dimensions and tolerances are specified acc. IEC 60286-3 and specified in mm.



#### SFH 4650

Revision History					
Version	Date	Change			
1.8	2020-03-25	Characteristics Electro - Optical Characteristics (Diagrams)			



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