

PSMF05 thru PSMF24

DEVICE CHARACTERISTICS

MAXIMUM RATINGS @ 25°C Unless Otherwise Specified

PARAMETER	SYMBOL	VALUE	UNITS
Peak Pulse Power ($t_p = 8/20\mu s$) - See Figure 1	P_{PP}	100	Watts
Operating Temperature	T_L	-55 to 150	°C
Storage Temperature	T_{STG}	-55 to 150	°C
Forward Voltage @ 1A, 8/20 μs	V_{FP}	1.5	Volts

ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified

PART NUMBER	DEVICE MARKING CODE	RATED STAND-OFF VOLTAGE V_{WM} VOLTS	MINIMUM BREAKDOWN VOLTAGE (See Note 1) @ 1mA $V_{(BR)}$ VOLTS	MAXIMUM CLAMPING VOLTAGE (See Fig. 2) @ $I_p = 1A$ V_C VOLTS	MAXIMUM CLAMPING VOLTAGE (See Fig. 2) @ 8/20 μs V_C @ I_{PP}	MAXIMUM LEAKAGE CURRENT @ V_{WM} I_D μA	MAXIMUM CAPACITANCE @ 0V, 1 MHz C_J pF
PSMF05	05	5.0	6.0	9.5	12.0V @ 9.0A	10	60
PSMF12	12	12.0	13.3	15.0	22.0V @ 5.0A	1	30
PSMF15	15	15.0	16.7	-	33.0V @ 3.0A	1	25
PSMF24	24	24.0	26.7	-	55.5V @ 1.8A	1	20

Note 1: Test between pins 1 to 2, 3 to 2, 4 to 2 and 5 to 2.

FIGURE 1
PEAK PULSE POWER VS PULSE TIME

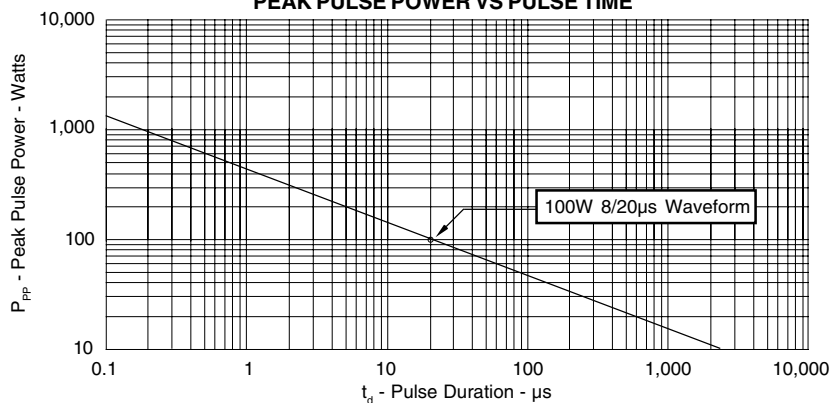
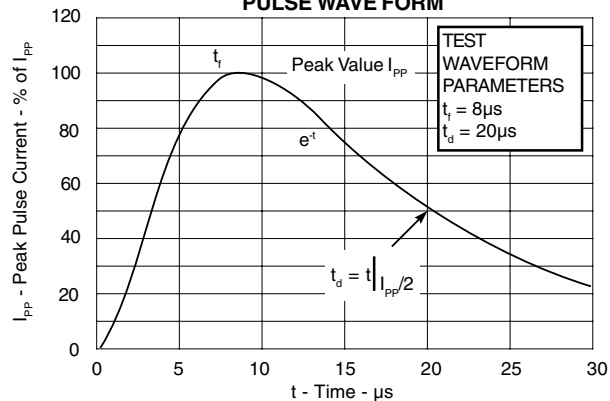
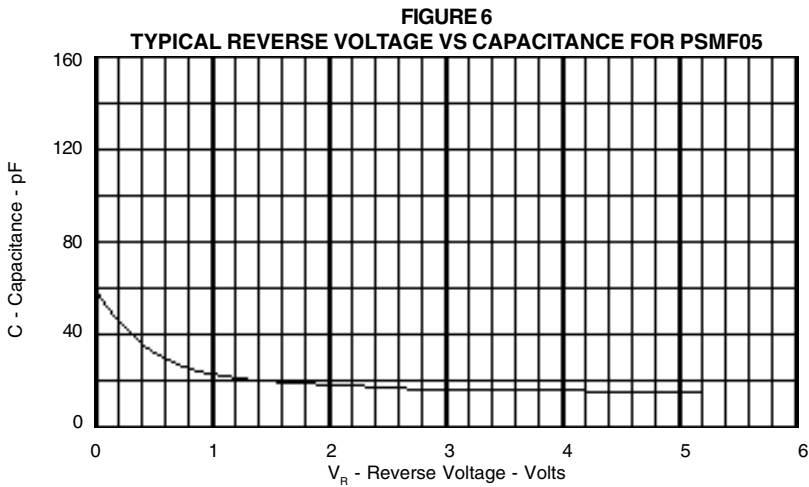
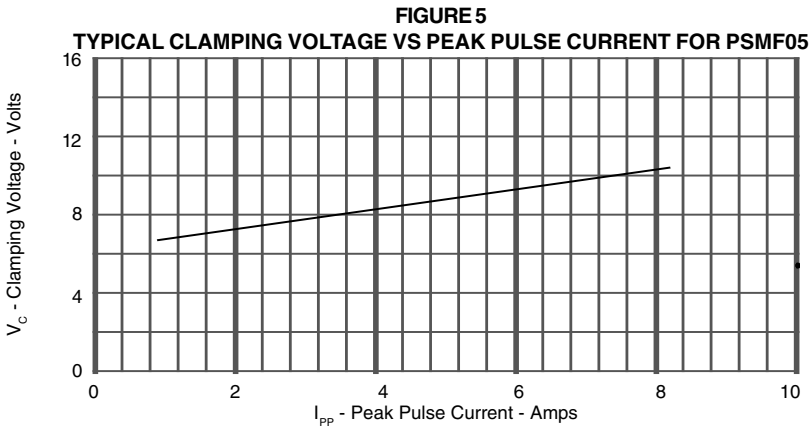
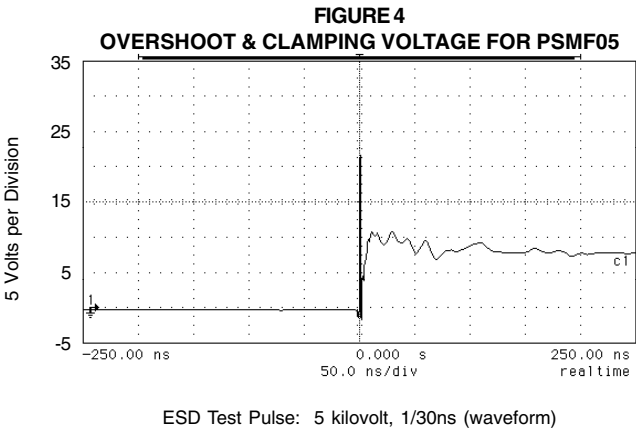
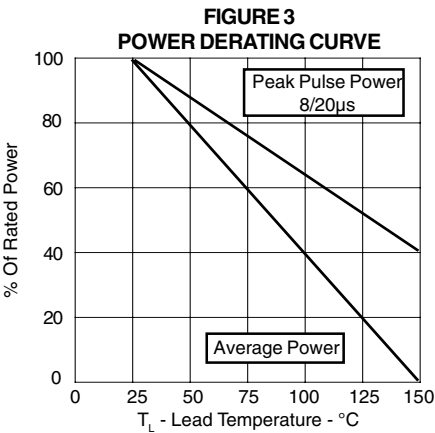


FIGURE 2
PULSE WAVE FORM



GRAPHS



APPLICATION NOTE

The PSMF Series are TVS arrays designed to protect I/O or data lines from the damaging effects of ESD or EFT. This product provides unidirectional protection, with a surge capability of 200 Watts P_{PP} per line for an 8/20 μ s waveform and ESD protection > 25 kilovolts.

UNIDIRECTIONAL COMMON-MODE CONFIGURATION (Figure 1)

The PSMF Series provides up to four (4) lines of protection in a common-mode configuration as depicted in Figure 1.

Circuit connectivity is as follows:

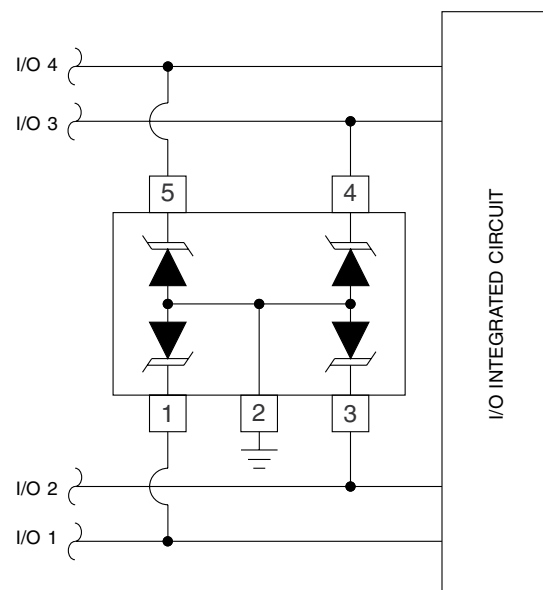
- ✓ I/O 1 is connected to Pin 1.
- ✓ I/O 2 is connected to Pin 3.
- ✓ I/O 3 is connected to Pin 4.
- ✓ I/O 4 is connected to Pin 5.
- ✓ Pin 2 is connected to ground.

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

Circuit board layout is critical for Electromagnetic Compatibility (EMC) protection. The following guidelines are recommended:

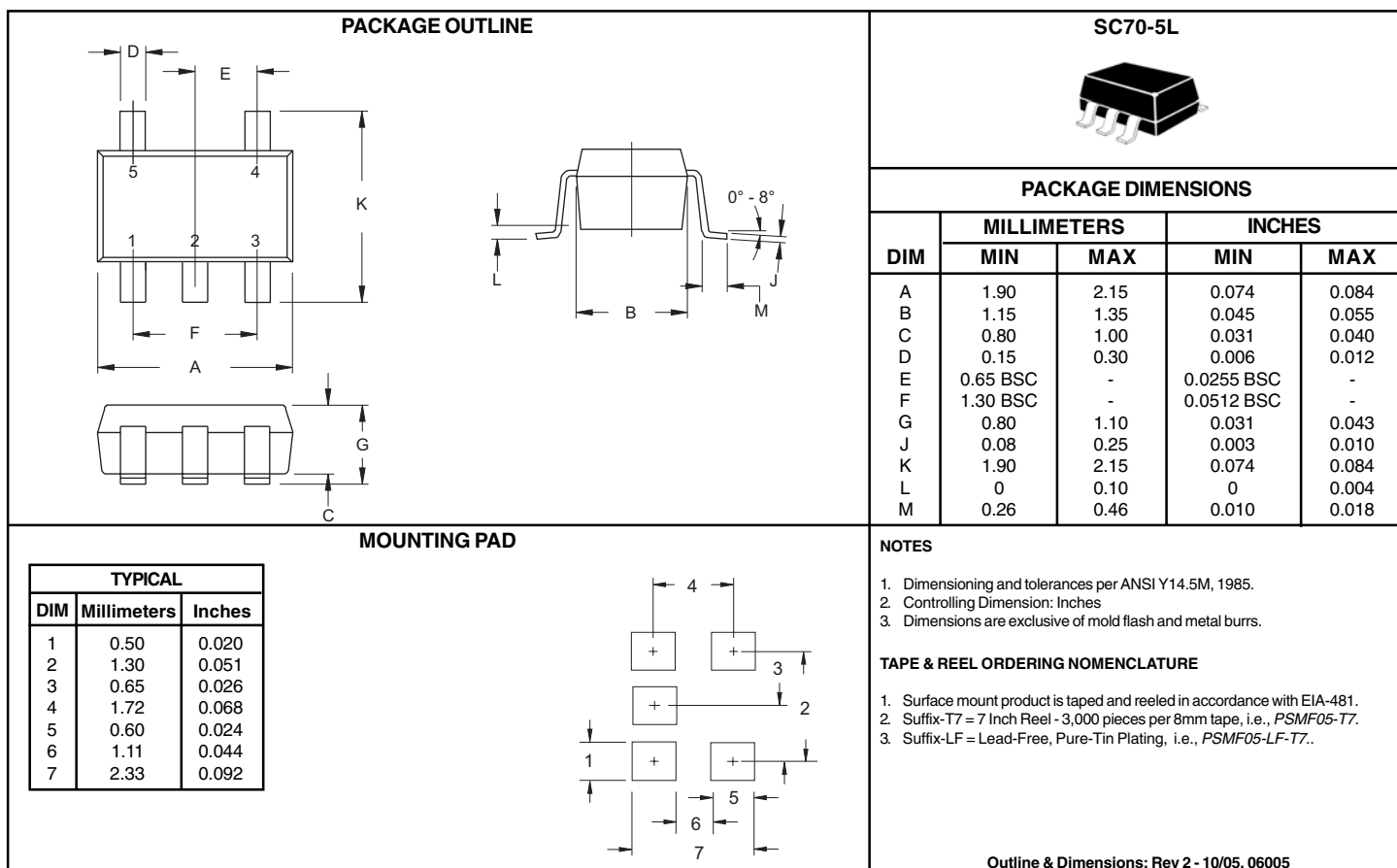
- ✓ The protection device should be placed near the input terminals or connectors, the device will divert the transient current immediately before it can be coupled into the nearby traces.
- ✓ The path length between the TVS device and the protected line should be minimized.
- ✓ All conductive loops including power and ground loops should be minimized.
- ✓ The transient current return path to ground should be kept as short as possible to reduce parasitic inductance.
- ✓ Ground planes should be used whenever possible. For multilayer PCBs, use ground vias.

Figure 1 - Unidirectional Configuration
Common-Mode I/O Port Protection



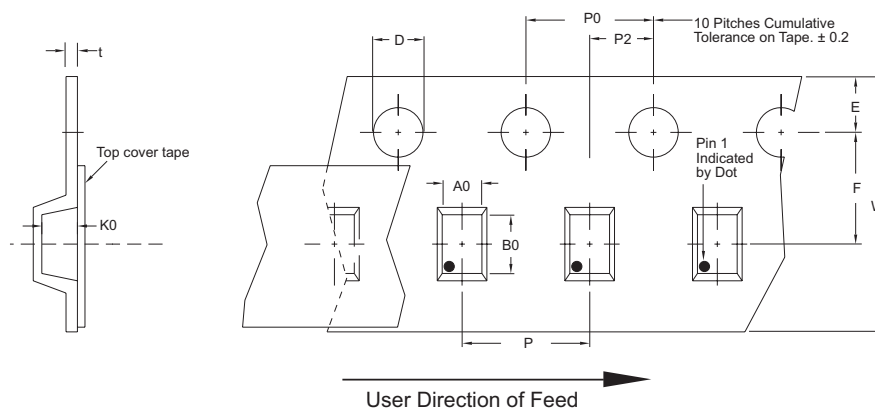
PSMF05 thru PSMF24

SC-70-5L PACKAGE OUTLINE & DIMENSIONS



Tape & Reel Specifications (Dimensions in millimeters)

Reel Dia.	Tape Width	A0	B0	K0	D	E	F	W	P0	P2	P	tmax
178mm (7")	8mm	2.25 ± 0.10	2.34 ± 0.10	1.22 ± 0.10	1.50 ± 0.10	1.75 ± 0.10	3.50 ± 0.05	8.00 ± 0.30	4.00 ± 0.10	2.00 ± 0.05	4.00 ± 0.10	0.25



COPYRIGHT © ProTek Devices 2007

SPECIFICATIONS: ProTek reserves the right to change the electrical and or mechanical characteristics described herein without notice (except JEDEC).

DESIGN CHANGES: ProTek reserves the right to discontinue product lines without notice, and that the final judgement concerning selection and specifications is the buyer's and that in furnishing engineering and technical assistance, ProTek assumes no responsibility with respect to the selection or specifications of such products.

ProTek Devices

2929 South Fair Lane, Tempe, AZ 85282

Tel: 602-431-8101 Fax: 602-431-2288

E-Mail: sales@protekdevices.com

Web Site: www.protekdevices.com