# 40EPS16PbF High Voltage Series

# Vishay High Power Products Input Rectifier Diode, 40 A



ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum forward voltage drop	$V_{FM}$	40 A, T <sub>J</sub> = 25 °C		1.14	V	
Forward slope resistance	r <sub>t</sub>	T <sub>.1</sub> = 150 °C		7.6	mΩ	
Threshold voltage	V <sub>F(TO)</sub>	1 J = 150 C		0.72	V	
Maximum reverse leakage current	1	T <sub>J</sub> = 25 °C	V <sub>B</sub> = Rated V <sub>BBM</sub>	0.1	mA	
	IRM	T <sub>J</sub> = 150 °C	VR = nated VRRM	1.0		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Maximum junction and storage temperature range		$T_J$ , $T_{Stg}$		- 40 to 150	°C	
Maximum thermal resistance, junction to case		R <sub>thJC</sub>	DC operation	0.6		
Maximum thermal resistance, junction to ambient		$R_{thJA}$		40	°C/W	
Typical thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, flat, smooth and greased	0.2		
Annuarin eta maint				6	g	
Approximate weight				0.21	OZ.	
Mounting torque -	minimum			6 (5)	kgf · cm	
	maximum			12 (10)	(lbf · in)	
Marking device			Case style TO-247AC modified (JEDEC)	40EF	PS16	

### Input Rectifier Diode, 40 A Vishay High Power Products

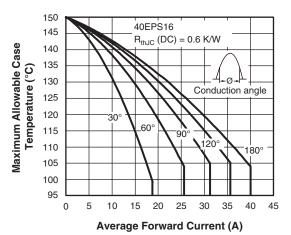
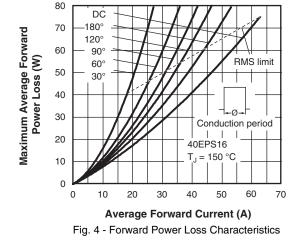


Fig. 1 - Current Rating Characteristics



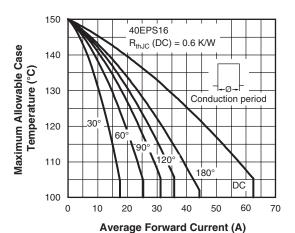


Fig. 2 - Current Rating Characteristics

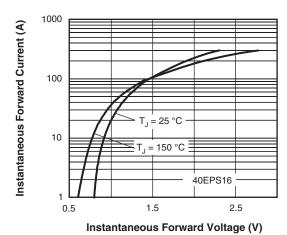


Fig. 5 - Forward Voltage Drop Chacteristics

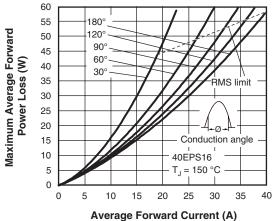


Fig. 3 - Forward Power Loss Characteristics

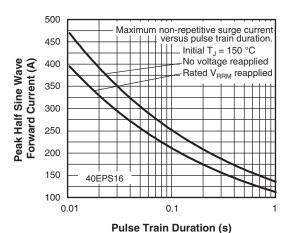


Fig. 6 - Maximum Non-Repetitive Surge Current

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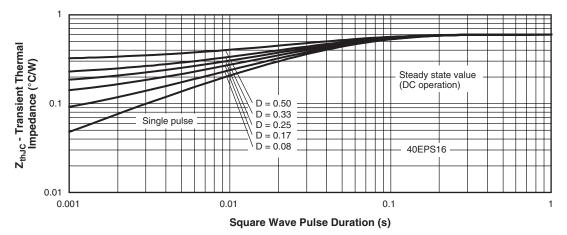
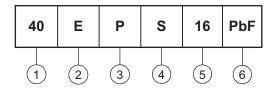


Fig. 7 - Thermal Impedance  $Z_{thJC}$  Characteristics

#### **ORDERING INFORMATION TABLE**

**Device code** 



- 1 Current rating (40 = 40 A)
- **2** Circuit configuration:

E = Single diode

- 3 Package:
  - P = TO-247AC modified
- 4 Type of silicon:

S = Standard recovery rectifier

- 5 Voltage rating (16 = 1600 V)
- 6 None = Standard production
  - PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95253			
Part marking information	http://www.vishay.com/doc?95255			

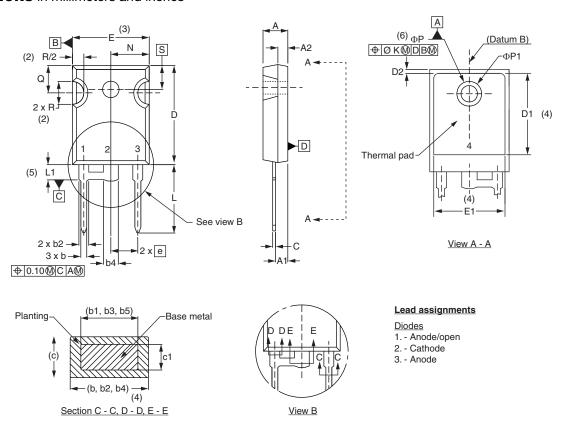
www.vishay.com

For technical questions, contact: diodes-tech@vishay.com Document



### Vishay Semiconductors

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIN	IETERS	INC	NOTES	
	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIMETERS		INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
Е	15.29	15.87	0.602	0.625	3
E1	13.72	ı	0.540	1	
е	5.46 BSC		0.215 BSC		
ΦК	2.54		0.010		
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62 BSC		0.3		
ΦР	3.56	3.66	0.14	0.144	
ФР1	-	6.98	1	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51 BSC		0.217 BSC		

#### Notes

- (1) Dimensioning and tolerance per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6)  $\Phi P$  to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c

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Vishay

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