

Vishay Semiconductors

ELECTRICAL SPE	CIFICATIONS
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PARAMETER	SYMBOL	TEST CO	VALUES	UNITS	
		5 A	T _{.1} = 25 °C	0.93	
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	10 A	1j=25 C	1.10	V
	VFM (''	5 A	T _ 125 °C	0.73	
		10 A	− T _J = 125 °C	0.86	
Maximum reverse leakage current per leg	I _{RM} ⁽¹⁾	$T_J = 25 \ ^\circ C$	V Detect V	0.05	mA
See fig. 2		T _J = 125 °C	V _R = Rated V _R	7	
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J}$ maximum		0.468	V
Forward slope resistance	r _t			28	mΩ
Maximum junction capacitance per leg	CT	V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz) 25 °C		200	pF
Typical series inductance per leg	L _S	Measured lead to lead 5 r	8.0	nH	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

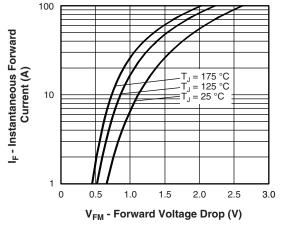
PARAMETER		SYMBOL	SYMBOL TEST CONDITIONS		UNITS	
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C	
Maximum thermal resistance, junction to case per leg		D		3.50		
Maximum thermal resistance, junction to case per package		R _{thJC}	DC operation	1.75	°C/W	
Typical thermal resistance, case to heatsink (only for TO-220)		R _{thCS}	Mounting surface, smooth and greased	0.50		
Approximate weight				2	g	
				0.07	oz.	
Mounting torque	minimum			6 (5)	kgf · cm	
Mounting torque	maximum			12 (10)	(lbf · in)	
Marking device			Case style TO-220AB	10CT	Q150	

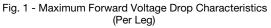


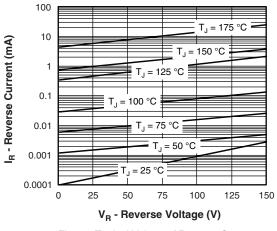
VS-10CTQ150PbF, VS-10CTQ150-N3

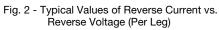
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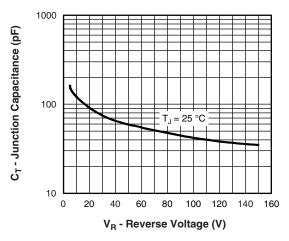
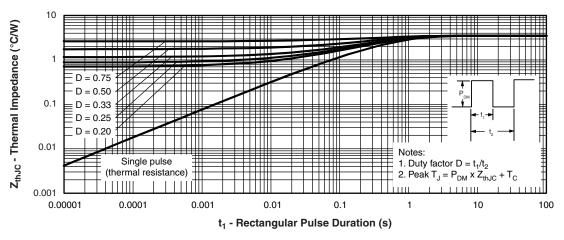


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)





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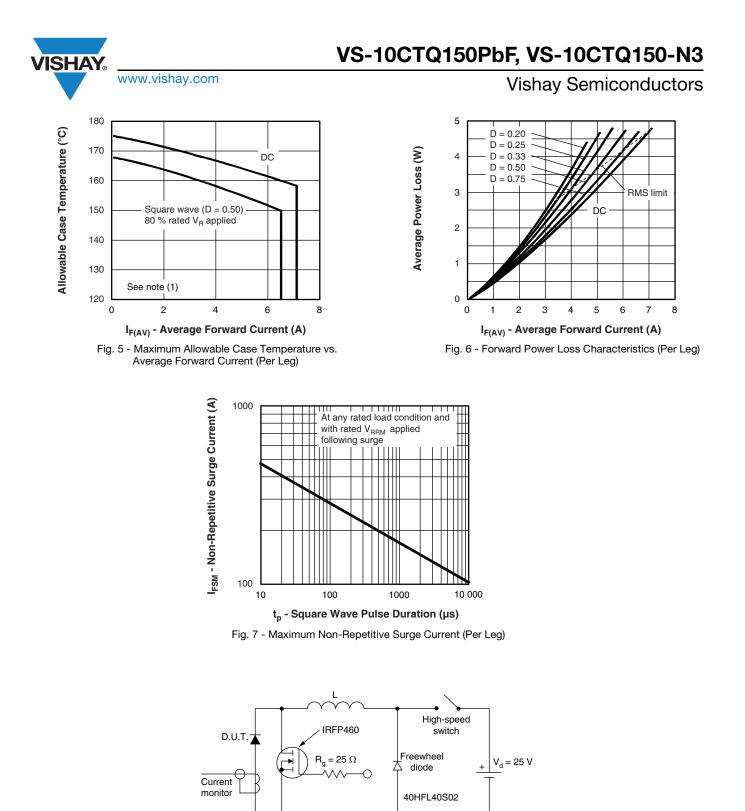




Fig. 8 - Unclamped Inductive Test Circuit

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} Pd = Forward power loss = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ Pd_{REV} = Inverse power loss = V_{R1} \times I_{R} \mbox{ (1 - D); } I_{R} \mbox{ at } V_{R1} = 10 \ V \end{array}$

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VS-10CTQ150PbF, VS-10CTQ150-N3



VS-10CTQ150-N3

50

Vishay Semiconductors

Antistatic plastic tube

ORDERING INFORMATION TABLE

Device code	VS-	10	С	т	Q	150	PbF
		2	3	4	5	6	7
	1 2 3	- Cur - Circ	rent rati cuit conf	niconduo ing (10 = iguration on catho	= 10 A) n	oduct	-
	4	- Pao	kage TO-220				
	5 6 7	- Vol	tage rati	Q" series ing (150 ntal digit	= 150 \	/)	
				ead (Pb) alogen-fi			

ORDERING INFO	RMATION (Example)		
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION
VS-10CTQ150PbF	50	1000	Antistatic plastic tube

1000

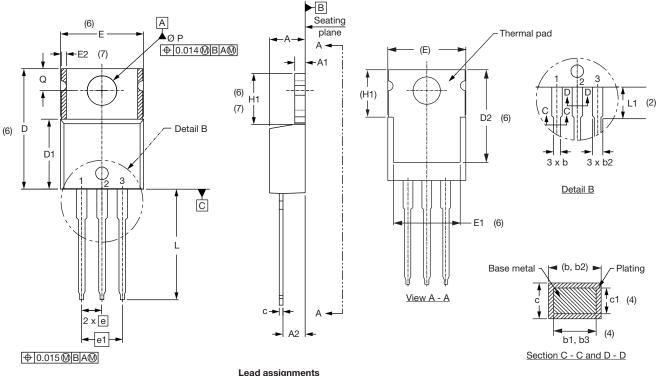
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?95222					
	TO-220AB PbF	www.vishay.com/doc?95225			
Part marking information	TO-220AB -N3	www.vishay.com/doc?95028			

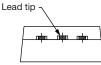


Vishay Semiconductors

TO-220AB

DIMENSIONS in millimeters and inches





ead.	assignments

Diodes

3. - Anode

1. - Anode/open 2. - Cathode

SYMBOL MILLIMETE		IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.56	2.92	0.101	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.25	0.585	0.600	3
D1	8.38	9.02	0.330	0.355	
D2	11.68	12.88	0.460	0.507	6

Notes

- ⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994
- ⁽²⁾ Lead dimension and finish uncontrolled in L1
- ⁽³⁾ Dimension D, D1 and E do not include mold flash. Mold flash shall not exceed $0.127 \text{ mm} (0.005^{\circ})$ per side. These dimensions are measured at the outermost extremes of the plastic body
- $^{\left(4\right) }$ Dimension b1, b3 and c1 apply to base metal only
- (5) Controlling dimensions: inches
- (6) Thermal pad contour optional within dimensions E, H1, D2 and E1

SYMBOL		MILLIN	IETERS	INC	HES	NOTES
	OL	MIN.	MAX.	MIN.	MAX.	NOTES
Е		10.11	10.51	0.398	0.414	3, 6
E1		6.86	8.89	0.270	0.350	6
E2		-	0.76	-	0.030	7
е		2.41	2.67	0.095	0.105	
e1		4.88	5.28	0.192	0.208	
H1		6.09	6.48	0.240	0.255	6, 7
L		13.52	14.02	0.532	0.552	
L1		3.32	3.82	0.131	0.150	2
ØΡ)	3.54	3.73	0.139	0.147	
Q		2.60	3.00	0.102	0.118	
θ		90° to 93°		90° t	o 93°	
θ		90° to 93°		90° t	90° to 93°	

Conforms to JEDEC outline TO-220AB

- $^{(7)}$ Dimensions E2 x H1 define a zone where stamping and singulation irregularities are allowed
- Outline conforms to JEDEC TO-220, except A2 (maximum) and (8) D2 (minimum) where dimensions are derived from the actual package outline

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