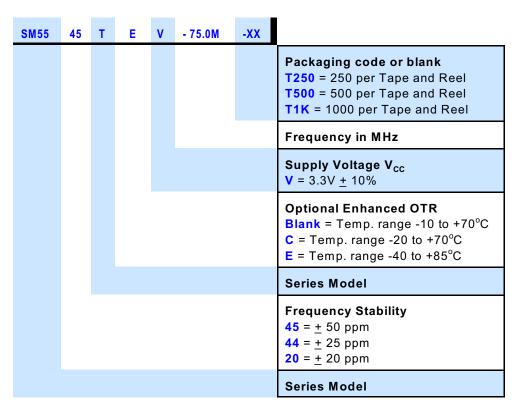
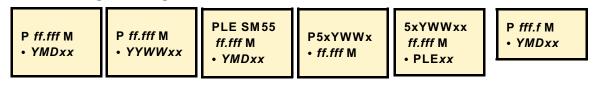


November 2018

Part Number:



Part Marking and Legend:



P or PLE = Pletronics ff.fff M or ff.ff M = Frequency in MHz

YYWW or YWW or YMD = Date of Manufacture (year and week, or year-month-day)

All other marking is internal factory codes

Specifications such as frequency stability, supply voltage and operating temperature range, etc. are not identified from the marking. External packaging labels and packing list will correctly identify the ordered Pletronics part number.

Codes for Date Code YMD

Code	6	7	8	9	0	Code	A	В	С	D	Е	F	G	Н	J	K	L	M
Year	2016	2017	2018	2019	2020	Mont	h JAN	FEB	MAR	APR	MAY	JUN	JUL	AUG	SEP	OCT	NOV	DEC
	Code		1	2	3	4	5	6	7	8	9	Α	В	С	D	Е	F	G
	Day		1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
(Code		Н	J	K	L	M	N	Р	R	Т	U	٧	W	Х	Υ	Z	
	Day		17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	

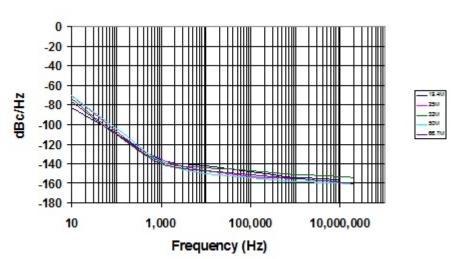


November 2018

Electrical Specification for 3.30V ±10% over the specified temperature range

Item	Min	Max	Unit	Condition			
Frequency Range	0.8	160	MHz				
Frequency Accuracy "45"	-50	+50	ppm	For all supply voltages, load changes, aging for 1			
"44"	-25	+25		year, shock, vibration and temperatures			
" <mark>20</mark> "	-20	+20					
Output Waveform		CMOS	3				
Output High Level	90	-	%	of V _{cc} (See load circuit)			
Output Low Level	-	10	%				
Output Symmetry	45	55	%	at 50% point of V _{CC} (See load circuit)			
Jitter	-	0.6	pS RMS	12 KHz to 20 MHz from the output frequency			
	-	2.5	pS RMS	10 Hz to 1 MHz from the output frequency			
Enable/Disable Internal Pull-up	50	-	Kohm	to V _{cc}			
V disable	-	30	%	of V _{cc} applied to pad 1			
V enable	70	-	%				
Output leakage V _{out} = V _{cc}	-10	+10	uA	Pad 1 low, device disabled			
$V_{OUT} = 0V$	-10	+10	uA				
Standby Current I _{cc}	-	3	uA				
Enable time	-	100	nS	Time for output to reach a logic state			
Disable time	-	100	nS	Time for output to reach a high Z state			
Start up time	-	3	mS	Time for output to reach specified frequency			
Operating Temperature Range	-10	+70	°C	Standard Temperature Range			
	-20	+70	°C	Extended Temperature Range "C" Option			
	-40	+85	°C	Extended Temperature Range "E" Option			
Storage Temperature Range	-55	+125	°C				

Typical phase noise plot for 5 oscillators at different output frequencies.





November 2018

Electrical Specification for 3.30V ±10% over the specified temperature range

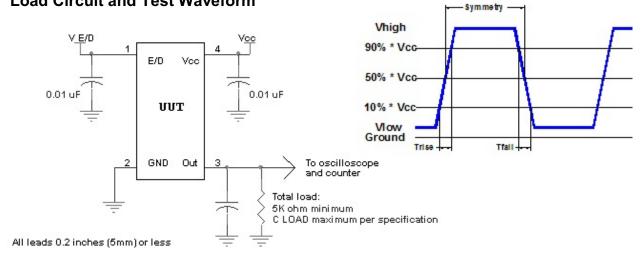
Item	Тур	Max	Unit	Condition			
Output T _{RISE} and T _{FALL}	1	5	nS	< 35 MHz	$C_{LOAD} = 15 \text{ pF}$		
	-	3	nS	≥ 35 MHz and < 70 MHz	10% to 90% of V _{cc} See Load Circuit		
	-	2.5	nS	≥ 70 MHz and < 110 MHz			
	1	2	nS	≥ 110 MHz			
	- 8 nS < 35 MHz		< 35 MHz	C _{LOAD} =30 pF			
	1	5	nS	≥ 35 MHz and < 70 MHz	10% to 90% of V _{cc} See Load Circuit		
	1	3	nS	≥ 70 MHz and < 110 MHz			
	-	2	nS	≥ 110 MHz			
V _{cc} Supply Current (I _{cc})	-	9	mA	< 8 MHz	C _{LOAD} = 15 pF		
	-	11	mA	≥ 8 MHz and < 16 MHz			
	-	17	mA	≥ 16 MHz and < 35 MHz			
	1	26	mA	≥ 35 MHz and <70 MHz			
	1	50	mA	≥ 70 MHz and < 110 MHz			
	1	70	mA	≥ 110 MHz			
	1	12	mA	< 8 MHz	C _{LOAD} = 30 pF		
	1	16	mA	≥ 8 MHz and < 16 MHz			
	-	22	mA	≥ 16 MHz and < 35 MHz			
	-	35	mA	≥ 35 MHz and <70 MHz			
	-	57	mA	≥ 70 MHz and < 110 MHz			
	-	90	mA	≥ 110 MHz			

Specifications with Pad 1 E/D open circuit



November 2018

Load Circuit and Test Waveform



Reliability: Environmental Compliance

Parameter	Condition
Mechanical Shock	MIL-STD-883 Method 2002, Condition B
Vibration	MIL-STD-883 Method 2007, Condition A
Solderability	MIL-STD-883 Method 2003
Thermal Shock	MIL-STD-883 Method 1011, Condition A

ESD Rating

Model	Minimum Voltage	Conditions
Human Body Model	1500	MIL-STD-883 Method 3115
Charged Device Model	1000	JESD 22-C101

Package Labeling

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Courier New Bar code is 39-Full ASCII

Label is 1" x 2.6" (25.4mm x 66.7mm) Font is Arial

RoHS Compliant

2nd LvL Interconnect

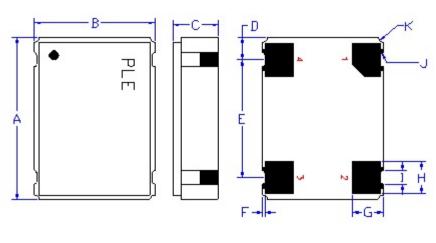
Category=e4

Max Safe Temp=260C for 10s 2X Max



November 2018

Mechanical:



	Inches	mm
Α	0.197 <u>+</u> 0.006	5.00 <u>+</u> 0.15
В	0.126 <u>+</u> 0.006	3.20 <u>+</u> 0.15
С	0.052 max	1.30 max
D ¹	0.048	1.23
E¹	0.100	2.54
F¹	0.004	0.10
G¹	0.050	1.27
H¹	0.055	1.40
l ¹	0.024	0.60
J ¹	0.004	0.10R
K¹	0.008	0.020R

Not to Scale

¹ Typical dimensions

Contacts:

Gold 11.8 to 32.7 μ inches (0.3 to 0.83 μ m) over Nickel 50 to 350 μ inches (1.27 to 8.89 μ m)

Pad	Function	Note
1	Output Enable/Disable	When this pad is not connected the oscillator shall operate. When this pad is logic low the output will be inhibited (high impedance state.) Recommend connecting this pad to $V_{\rm cc}$ if the oscillator is to be always on.
2	Ground (GND)	
3	Output	
4	Supply Voltage (V _{cc})	Recommend connecting appropriate power supply bypass capacitors as close as possible.



Layout and application information

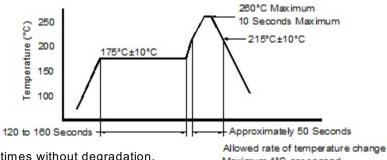
For Optimum Jitter Performance, Pletronics recommends:

- a ground plane under the device
- no large transient signals (both current and voltage) should be routed under the device
- do not layout near a large magnetic field such as a high frequency switching power supply
- do not place near piezoelectric buzzers or mechanical fans.



November 2018

Reflow Cycle (typical for lead free processing)



The part may be reflowed 3 times without degradation.

Maximum 4°C per second

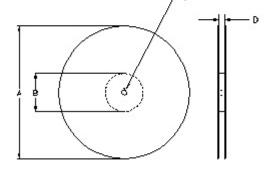
Tape and Reel: available for quantities of 250 to 1000 per reel, cut tape for < 250

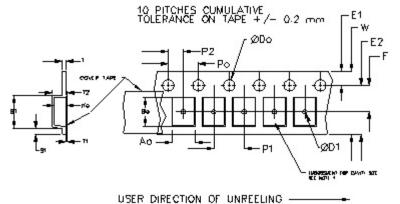
	Constant Dimensions Table 1											
Tape Size	D0	D1 Min	E1	P0	P2	S1 Min	T Max	T1 Max				
8mm		1.0			2.0							
12mm	1.5	1.5	1.75	4.0	<u>+</u> 0.05							
16mm	+0.1 -0.0	1.5	<u>+</u> 0.1	<u>+</u> 0.1	2.0	0.6	0.6	0.1				
24mm		1.5			<u>+</u> 0.1							

Variable Dimensions Table 2										
Tape Size	B1 Max	E2 Min	F	P1	T2 Max	W Max	Ao, Bo & Ko			
16 mm	12.1	14.25	7.5 <u>+</u> 0.1	8.0 <u>+</u> 0.1	8.0	16.3	Note 1			

Note 1: Embossed cavity to conform to EIA-481-B

Dimensions in mm Not to scale





Tape Width
vviutn
16.0
٧

Reel dimensions may vary from the above

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425-776-1880



November 2018

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