

Maximum Ratings (@T_A = +25°C unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	45	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _{RM}		
RMS Reverse Voltage	V _{R(RMS)}	31	V
Average Rectified Output Current @T _C = +110°C	I _O	10	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	90	A

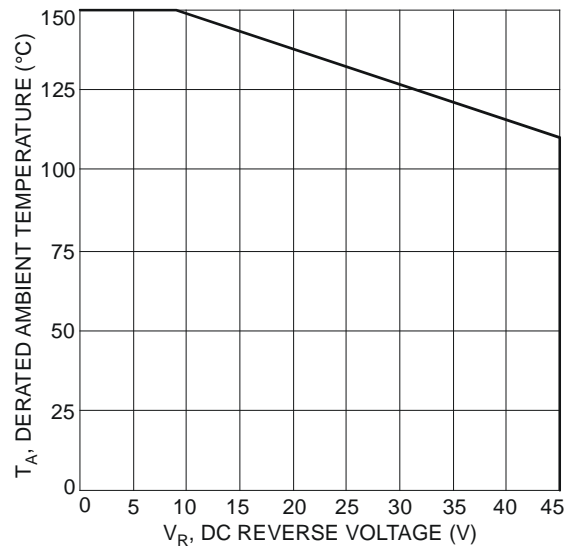
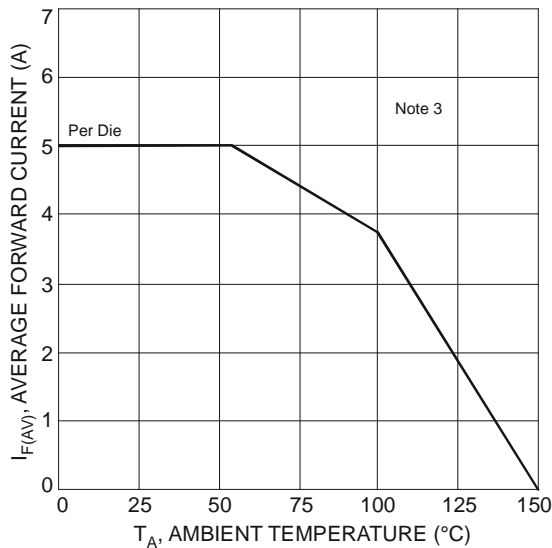
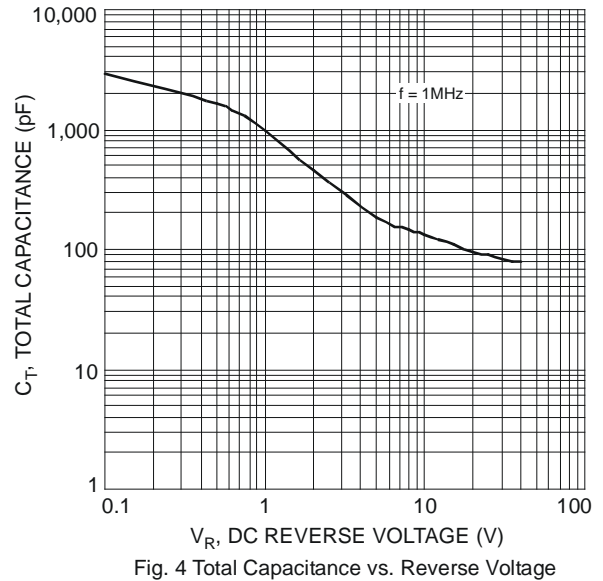
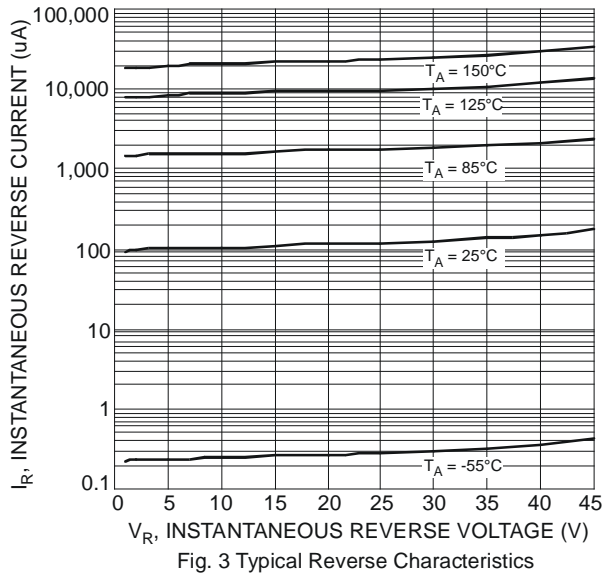
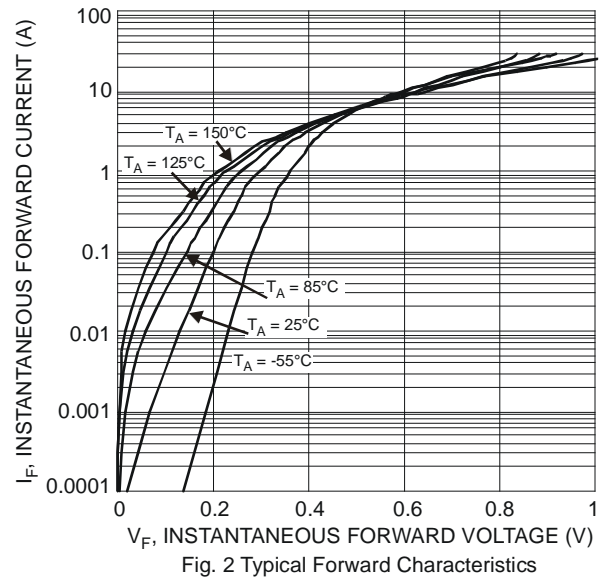
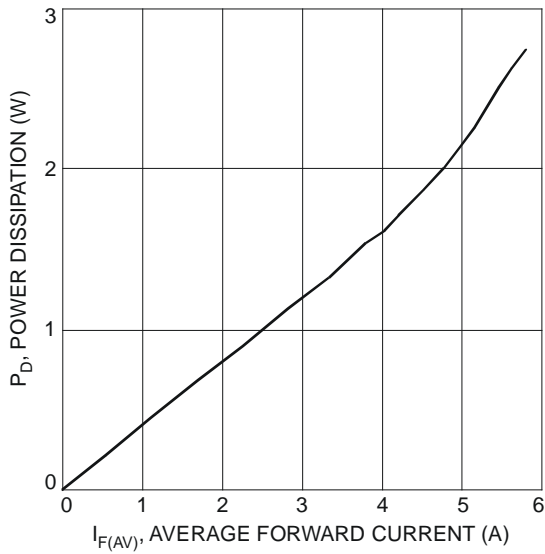
Thermal Characteristics

Characteristic	Symbol	Value	Unit
Maximum Thermal Resistance (per leg) (Note 5)	R _{θJA}	35	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics (@T_A = +25°C unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V _{(BR)R}	45	-	-	V	I _R = 0.5mA
Forward Voltage Drop (Per Leg)	V _F	-	-	0.55 0.53	V	I _F = 5A, T _J = +25°C I _F = 5A, T _J = +85°C
Leakage Current (Note 6)	I _R	-	-	0.5 100	mA	V _R = 45V, T _J = +25°C V _R = 45V, T _J = +125°C

Notes: 5. Device mounted 2inch sq. Al board. minimum recommended pad layout as shown on Diodes Inc. suggested pad layout document AP02001, which can be found on our website at <http://www.diodes.com>.
6. Short duration pulse test used to minimize self-heating effect.

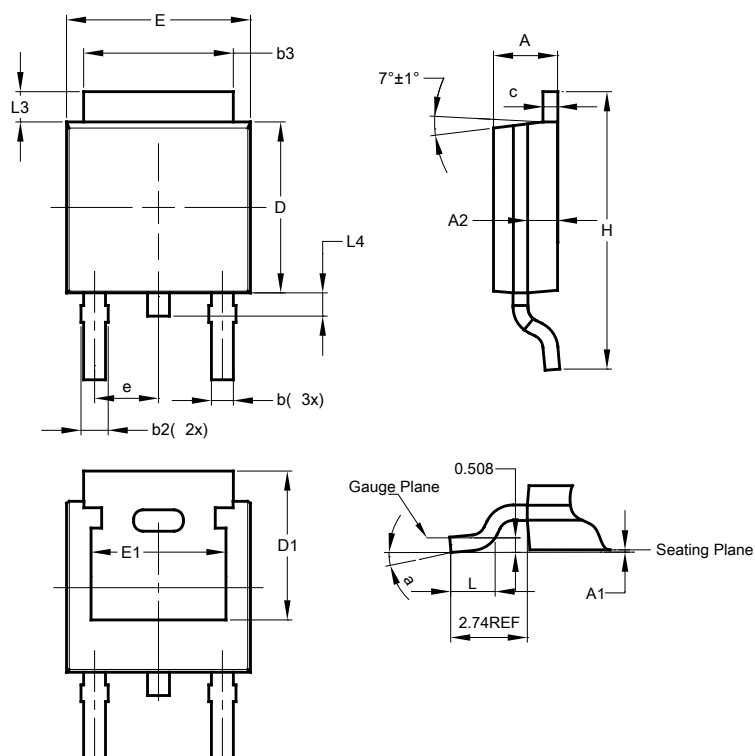


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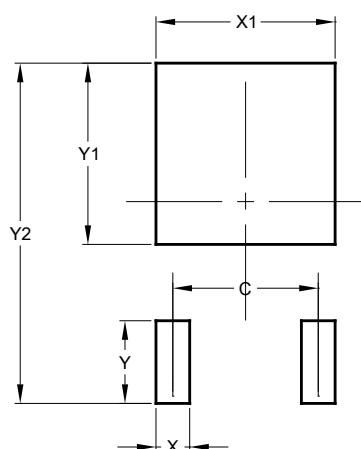
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Package Outline Dimensions



TO252 (DPAK)			
Dim	Min	Max	Typ
A	2.19	2.39	2.29
A1	0.00	0.13	0.08
A2	0.97	1.17	1.07
b	0.64	0.88	0.783
b2	0.76	1.14	0.95
b3	5.21	5.46	5.33
c	0.45	0.58	0.531
D	6.00	6.20	6.10
D1	5.21	-	-
e	-	-	2.286
E	6.45	6.70	6.58
E1	4.32	-	-
H	9.40	10.41	9.91
L	1.40	1.78	1.59
L3	0.88	1.27	1.08
L4	0.64	1.02	0.83
a	0°	10°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
C	4.572
X	1.060
X1	5.632
Y	2.600
Y1	5.700
Y2	10.700

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