Absolute Maximum Ratings

Rating	Symbol	Value	Units
Peak Pulse Current (tp = 8/20μs)	I _{PP}	4	A
ESD per IEC 61000-4-2 (Contact) ⁽¹⁾ ESD per IEC 61000-4-2 (Air) ⁽¹⁾	V _{ESD}	±20 ±25	kV
Operating Temperature	T _J	-40 to +85	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Units
Reverse Stand-Off Voltage	V _{RWM}	Any I/O to GND			5	V
Reverse Breakdown Voltage	V _{BR}	I _{BR} = 1mA, Any I/O to GND	6.5	9.7	11.5	V
Reverse Leakage Current	I _R	$V_{RWM} = 5V$, Any I/O to GND		5	100	nA
Clamping Voltage	V _c	$I_{pp} = 1A$, tp = 8/20 μ s			15	V
		$I_{pp} = 4A$, $tp = 8/20 \mu s$			25	
ESD Clamping Voltage ²	V _C	I = 4A, tp = 0.2/100ns (TLP)		14		V
ESD Clamping Voltage ²	V _C	I = 16A, tp = 0.2/100ns (TLP)		24		V
Dynamic Resistance ^{2, 3}	R _{DYN}	tp = 0.2/100ns (TLP)		1.0		Ohms
Junction Capacitance	C _J	$V_R = 0V, f = 1MHz$		0.19	0.22	pF

Notes:

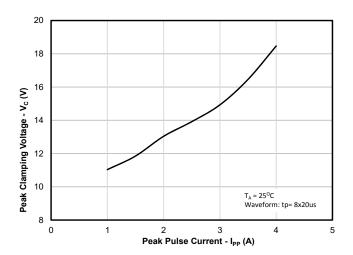
⁽¹⁾ ESD gun return path connected to Ground Reference Plane (GRP)

⁽²⁾ Transmission Line Pulse Test (TLP) Settings: tp = 100ns, tr = 0.2ns, I_{TLP} and V_{TLP} averaging window: $t_1 = 70$ ns to $t_2 = 90$ ns.

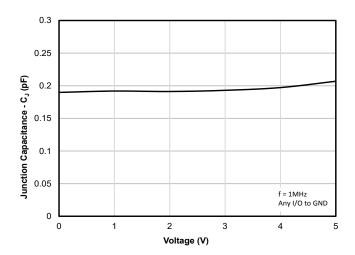
⁽³⁾ Dynamic resistance calculated from $I_{TLP} = 4A$ to $I_{TLP} = 16A$

Typical Characteristics

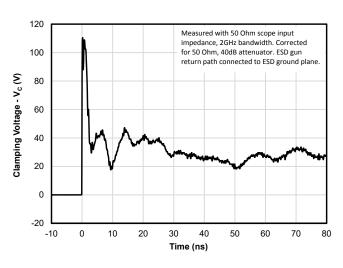
Clamping Voltage vs. Peak Pulse Current



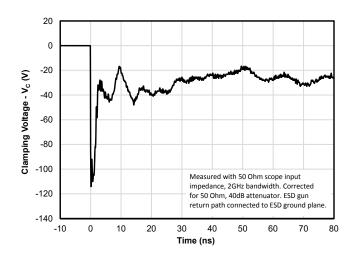
Junction Capacitance vs. Reverse Voltage



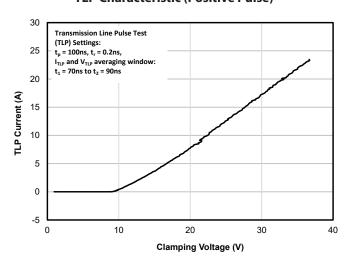
ESD Clamping Voltage (8kV Contact per IEC61000-4-2)



ESD Clamping Voltage (-8kV Contact per IEC61000-4-2)

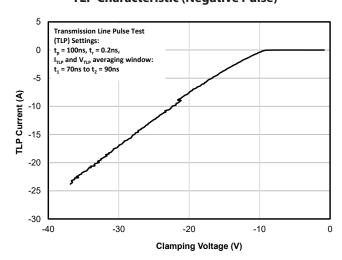


TLP Characteristic (Positive Pulse)



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TLP Characteristic (Negative Pulse)

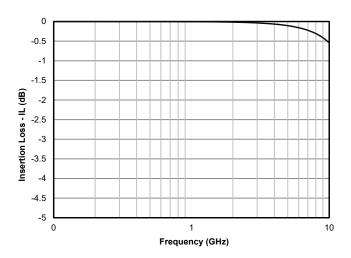


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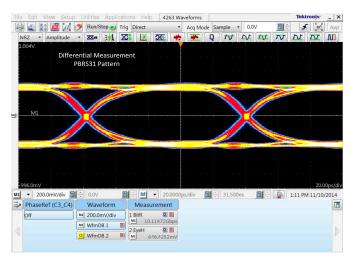
3

Typical Characteristics

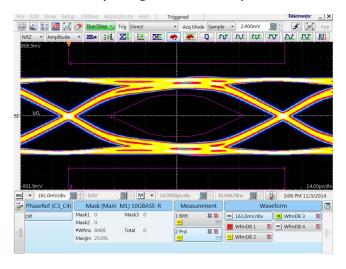
Insertion Loss (S21)



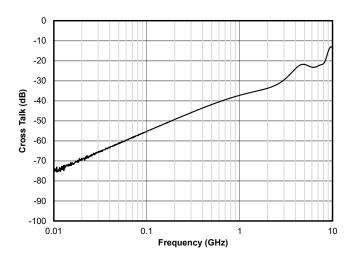
10Gb/s (USB 3.1) Eye Diagram with RClamp7534P



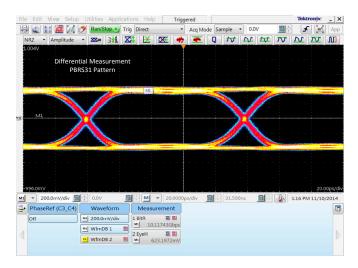
10GbE Eye Diagram with RClamp7534P



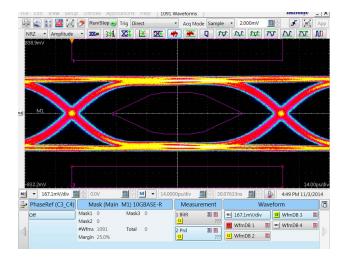
Analog Crosstalk



10Gb/s (USB 3.1) Eye Diagram without RClamp7534P



10GbE Eye Diagram without RClamp7534P



Applications Information

Assembly Guidelines

The small size of this device means that some care must be taken during the mounting process to insure reliable solder joints. The figure at the right details Semtech's recommended mounting pattern. Recommended assembly guidelines are shown in Table 1. Note that these are only recommendations and should serve only as a starting point for design since there are many factors that affect the assembly process. Exact manufacturing parameters will require some experimentation to get the desired solder application. Semtech's recommended mounting pattern is based on the following design quidelines:

Land Pattern

The recommended land pattern follows IPC standards and is designed for maximum solder coverage. Detailed dimensions are shown elsewhere in this document.

Solder Stencil

Stencil design is one of the key factors which will determine the volume of solder paste which is deposited onto the land pad. The area ratio of the stencil aperture will determine how well the stencil will print. The area ratio takes into account the aperture shape, aperture size, and stencil thickness. An area ratio of 0.70 – 0.75 is preferred for the subject package. The area ratio of a rectangular aperture is given as:

Area Ratio = (L * W) / (2 * (L + W) * T)

Where:

L = Aperture Length

W = Aperture Width

T = Stencil Thickness

Semtech recommends a stencil thickness of 0.100mm for this device. The stencil should be laser cut with electropolished finish. The stencil should have a positive taper of approximately 5 degrees. Electro polishing and tapering the walls results in reduced surface friction and better paste release. For small pitch components, Semtech recommends a square aperture with rounded corners for consistent solder release. Due to the small aperture size, a solder paste with Type 4 or smaller particles are recommended.

Recommended Mounting Pattern

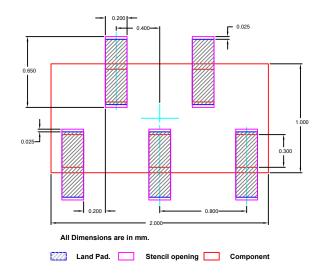
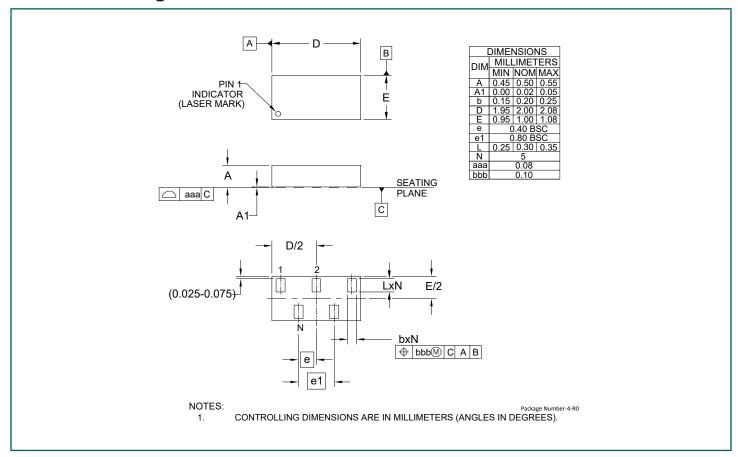
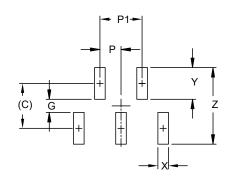


Table 1 - Recommended Assembly Guidelines			
Assembly Parameter	Recommendation		
Solder Stencil Design	Laser Cut, Electro-Polished		
Aperture Shape	Rectangular with rounded		
	corners		
Solder Stencil Thickness	0.100mm (0.004")		
Solder Paste Type	Type 4 size sphere or smaller		
Solder Reflow Profile	Per JEDEC J-STD-020		
PCB Solder pad Design	Non-Solder Mask Defined		
PCB Pad Finish	OSP or NiAu		

Outline Drawing - SGP2010N5



Land Pattern - SGP2010N5



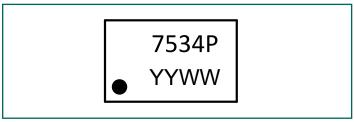
DIMENSIONS		
DIM	MILLIMETERS	
С	(0.85)	
G	0.25	
Р	0.40	
P1	0.80	
Х	0.20	
Υ	0.60	
Z	1.45	

NOTES:

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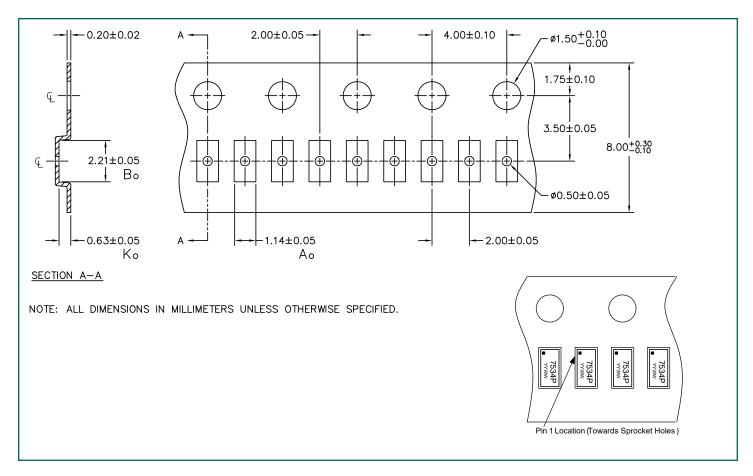
- 1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
- 2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

Marking Code



Notes: YYWW = Alphanumeric Date Code

Tape and Reel Specification



Ordering Information

Part Number	Qty per Reel	Reel Size		
RClamp7534P.TNT	10000	7 Inch		
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