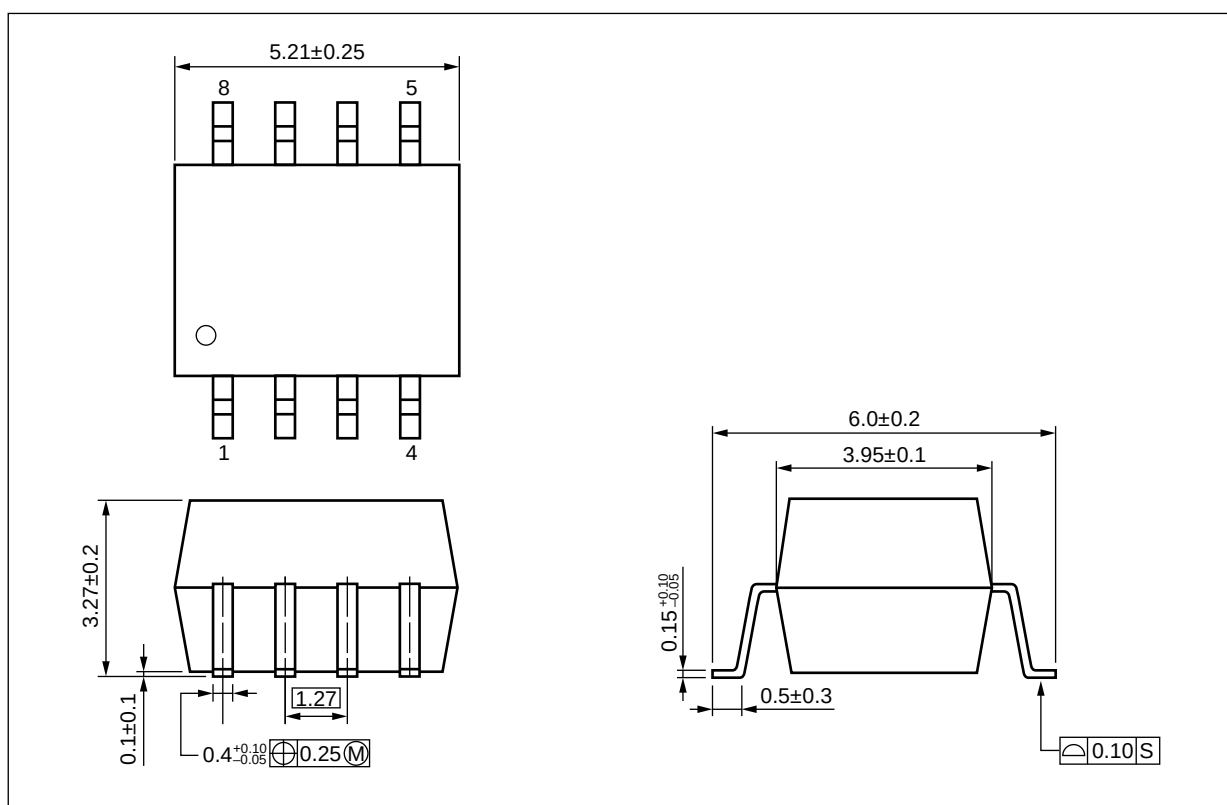
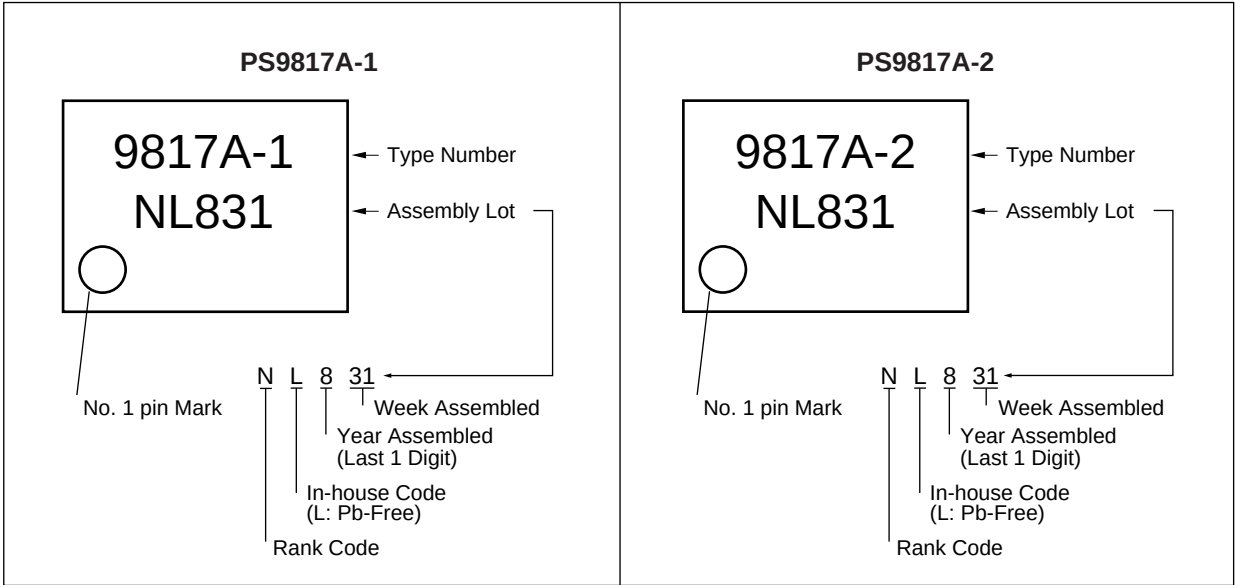


PACKAGE DIMENSIONS (UNIT: mm)

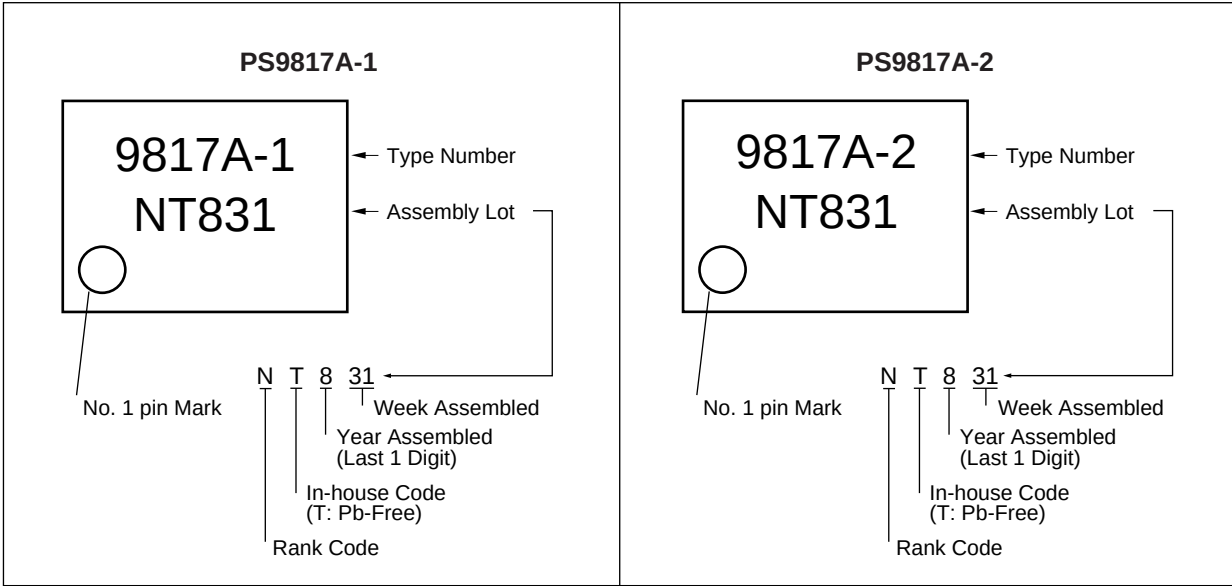


<R> MARKING EXAMPLE

SnBi PLATING



Ni/Pd/Au PLATING



<R> ORDERING INFORMATION

Part Number	Order Number	Solder plating specification	Packing Style	Safety Standards Approval	Application Part Number ^{*1}
PS9817A-1	PS9817A-1-A	Pb-Free (SnBi)	20 pcs (Tape 20 pcs cut)	Standard products (UL approved)	PS9817A-1
PS9817A-1-F3	PS9817A-1-F3-A		Embossed Tape 1 500 pcs/reel		
PS9817A-2	PS9817A-2-A		20 pcs (Tape 20 pcs cut)		PS9817A-2
PS9817A-2-F3	PS9817A-2-F3-A		Embossed Tape 1 500 pcs/reel		
PS9817A-1-V	PS9817A-1-V-A		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9817A-1
PS9817A-1-V-F3	PS9817A-1-V-F3-A		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9817A-2-V	PS9817A-2-V-A		20 pcs (Tape 20 pcs cut)	approved	PS9817A-2
PS9817A-2-V-F3	PS9817A-2-V-F3-A		Embossed Tape 1 500 pcs/reel	(Option)	
PS9817A-1	PS9817A-1-AX	Pb-Free (Ni/Pd/Au)	20 pcs (Tape 20 pcs cut)	Standard products (UL approved)	PS9817A-1
PS9817A-1-F3	PS9817A-1-F3-AX		Embossed Tape 1 500 pcs/reel		
PS9817A-2	PS9817A-2-AX		20 pcs (Tape 20 pcs cut)		PS9817A-2
PS9817A-2-F3	PS9817A-2-F3-AX		Embossed Tape 1 500 pcs/reel		
PS9817A-1-V	PS9817A-1-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9817A-1
PS9817A-1-V-F3	PS9817A-1-V-F3-AX		Embossed Tape 1 500 pcs/reel	(VDE0884 Part2)	
PS9817A-2-V	PS9817A-2-V-AX		20 pcs (Tape 20 pcs cut)	approved	PS9817A-2
PS9817A-2-V-F3	PS9817A-2-V-F3-AX		Embossed Tape 1 500 pcs/reel	(Option)	

*1 For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Ratings		Unit
			PS9817A-1	PS9817A-2	
Diode	Forward Current	I_F	20 ^{*1}	15 ^{*2}	mA/ch
	Reverse Voltage	V_R	5		V/ch
Detector	Supply Voltage	V_{CC}	7		V
	Output Voltage	V_O	7		V/ch
	Output Current	I_O	25		mA/ch
	Power Dissipation ^{*3}	P_C	40		mW/ch
Isolation Voltage ^{*4}		BV	2 500		Vr.m.s.
Operating Ambient Temperature		T_A	-40 to +85		$^\circ\text{C}$
Storage Temperature		T_{stg}	-55 to +125		$^\circ\text{C}$

***1** Reduced to 0.3 mA/ $^\circ\text{C}$ at $T_A = 60^\circ\text{C}$ or more.

***2** Reduced to 0.1 mA/ $^\circ\text{C}$ at $T_A = 60^\circ\text{C}$ or more.

***3** Applies to output pin V_O (collector pin). Reduced to 1.5 mW/ $^\circ\text{C}$ at $T_A = 65^\circ\text{C}$ or more.

***4** AC voltage for 1 minute at $T_A = 25^\circ\text{C}$, RH = 60% between input and output.

Pins 1-4 shorted together, 5-8 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	V_{FL}	0		0.8	V
High Level Input Current	I_{FH}	6.3	10	12.5	mA
Supply Voltage	V_{CC}	4.5		5.5	V
Pull-up Resistance	R_L	330		4 k	Ω
TLL ($R_L = 1.0\text{ k}\Omega$, loads)	N			5	

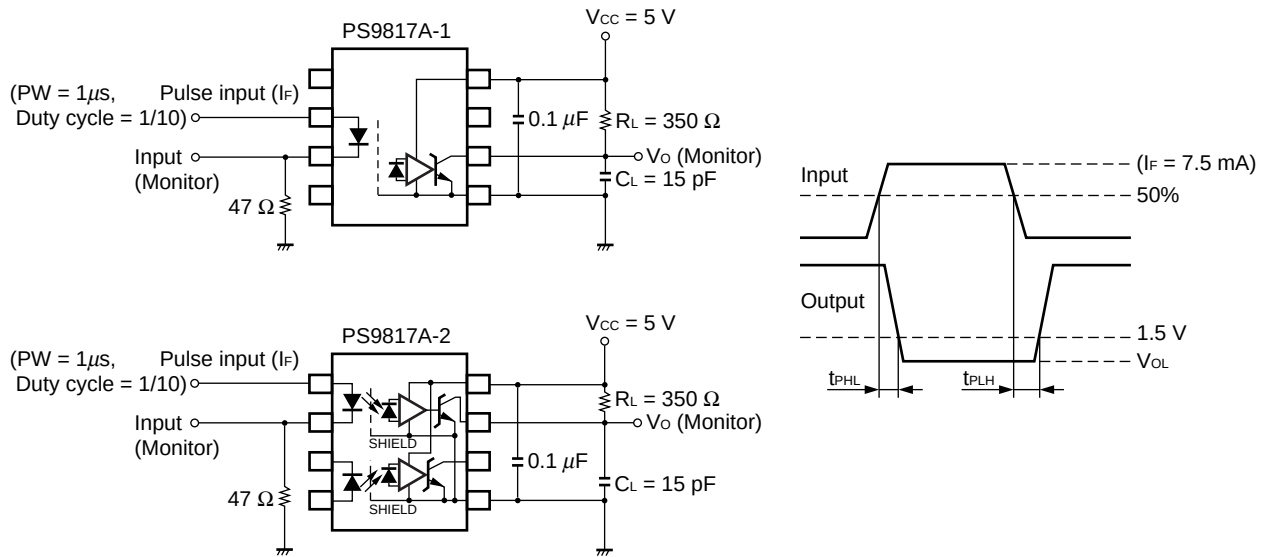
ELECTRICAL CHARACTERISTICS ($T_A = -40$ to $+85^\circ\text{C}$, unless otherwise specified)

Parameter		Symbol	Conditions	MIN.	TYP. ^{*1}	MAX.	Unit
Diode	Forward Voltage	V_F	$I_F = 10\text{ mA}$, $T_A = 25^\circ\text{C}$	1.4	1.65	1.8	V
	Reverse Current	I_R	$V_R = 3.0\text{ V}$, $T_A = 25^\circ\text{C}$			10	μA
	Terminal Capacitance	C_i	$V_F = 0\text{ V}$, $f = 1\text{ MHz}$, $T_A = 25^\circ\text{C}$		30		pF
Detector	High Level Output Current	I_{OH}	$V_{CC} = V_O = 5.5\text{ V}$, $V_F = 0.8\text{ V}$		1	100	μA
	Low Level Output Voltage ^{*2}	V_{OL}	$V_{CC} = 5.5\text{ V}$, $I_F = 5.0\text{ mA}$, $I_{OL} = 13\text{ mA}$		0.2	0.6	V
	High Level Supply Current (PS9817A-1)	I_{CCH}	$V_{CC} = 5.5\text{ V}$, $I_F = 0\text{ mA}$, $V_O = \text{open}$		4	7	mA
	High Level Supply Current (PS9817A-2)				8	14	
	Low Level Supply Current (PS9817A-1)	I_{CCL}	$V_{CC} = 5.5\text{ V}$, $I_F = 10\text{ mA}$, $V_O = \text{open}$		6	10	
	Low Level Supply Current (PS9817A-2)				12	20	
Coupled	Threshold Input Current (H $\rightarrow$ L)	I_{FHL}	$V_{CC} = 5.0\text{ V}$, $V_O = 0.8\text{ V}$, $R_L = 350\ \Omega$		2	5	mA
	Isolation Resistance	R_{I-O}	$V_{I-O} = 1\text{ kV}_{DC}$, $R_H = 40$ to 60% , $T_A = 25^\circ\text{C}$	10^{11}			Ω
	Insulation Resistance (Input-Input), (PS9817A-2)	R_{I-I}	$V_{I-I} = 1\text{ kV}_{DC}$, $R_H = 40$ to 60% , $T_A = 25^\circ\text{C}$	10^{10}			Ω
	Isolation Capacitance	C_{I-O}	$V = 0\text{ V}$, $f = 1\text{ MHz}$, $T_A = 25^\circ\text{C}$		0.6		pF
	Insulation Capacitance (Input-Input), (PS9817A-2)	C_{I-I}	$V = 0\text{ V}$, $f = 1\text{ MHz}$, $T_A = 25^\circ\text{C}$		0.3		pF
	Propagation Delay Time (H $\rightarrow$ L) ^{*3}	t_{PHL}	$V_{CC} = 5.0\text{ V}$, $R_L = 350\ \Omega$, $I_F = 7.5\text{ mA}$	$T_A = 25^\circ\text{C}$	40	75	ns
						100	
	Propagation Delay Time (L $\rightarrow$ H) ^{*3}	t_{PLH}		$T_A = 25^\circ\text{C}$	45	75	
						100	
	Rise Time	t_r			20		
	Fall Time	t_f			5		
	Pulse Width Distortion (PWD) ^{*3}	$ t_{PHL} - t_{PLH} $			5	35	
	Propagation Delay Skew	t_{PSK}				40	
	Common Mode Transient Immunity at High Level Output ^{*4}	CM_H	$V_{CC} = 5.0\text{ V}$, $R_L = 350\ \Omega$, $T_A = 25^\circ\text{C}$, $I_F = 0\text{ mA}$, $V_O > 2\text{ V}$, $V_{CM} = 1\text{ kV}$	15	20		kV/ μs
	Common Mode Transient Immunity at Low Level Output ^{*4}	CM_L	$V_{CC} = 5.0\text{ V}$, $R_L = 350\ \Omega$, $T_A = 25^\circ\text{C}$, $I_F = 7.5\text{ mA}$, $V_O < 0.8\text{ V}$, $V_{CM} = 1\text{ kV}$	15	20		

^{*1} Typical values at $T_A = 25^\circ\text{C}$

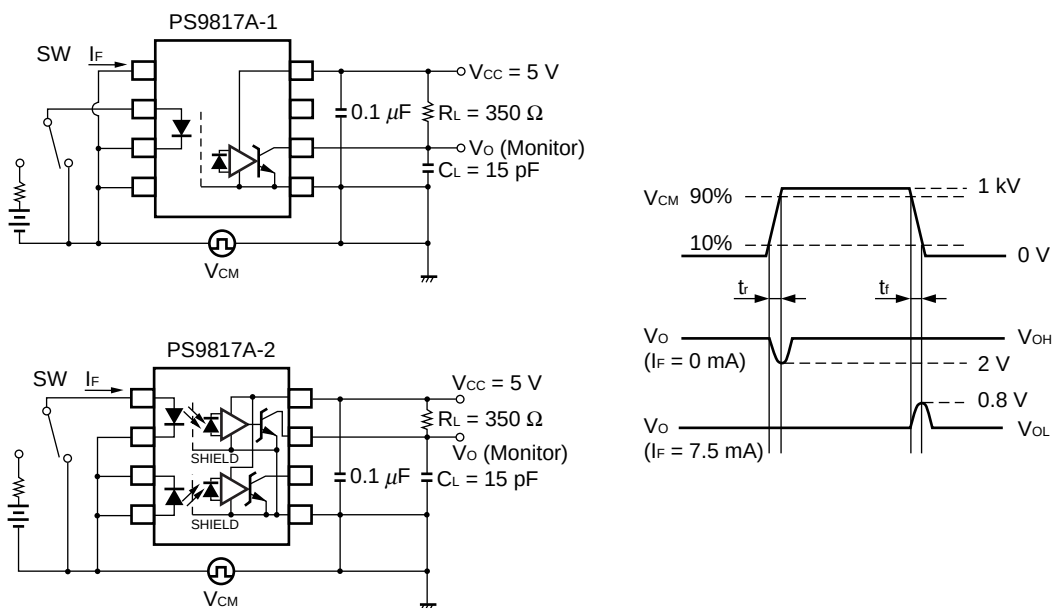
^{*2} Because V_{OL} of 2 V or more may be output when LED current input and when output supply of $V_{CC} = 2.6\text{ V}$ or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.

***3 Test circuit for propagation delay time**



Remark C_L includes probe and stray wiring capacitance.

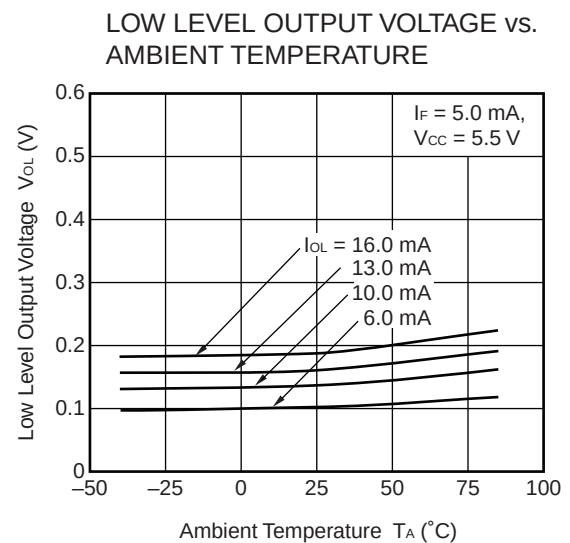
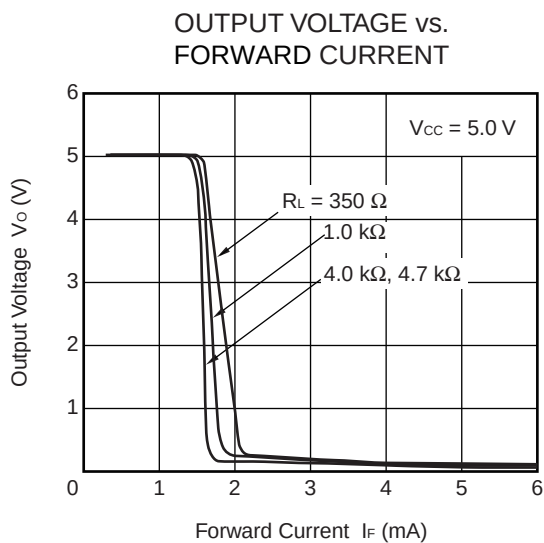
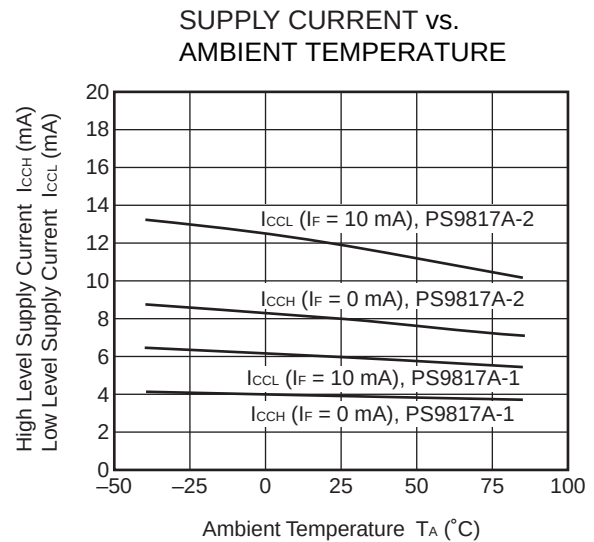
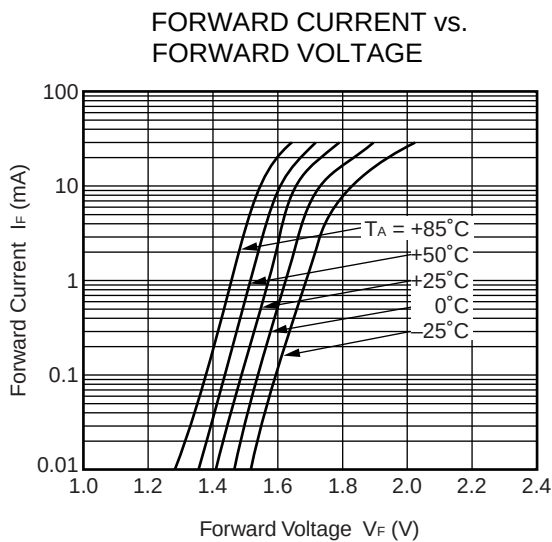
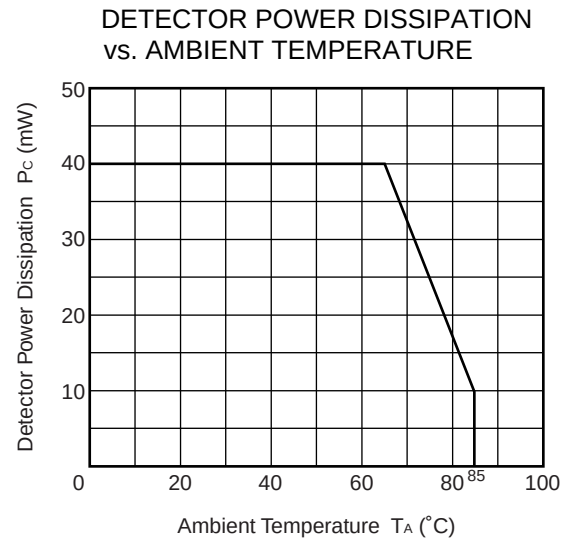
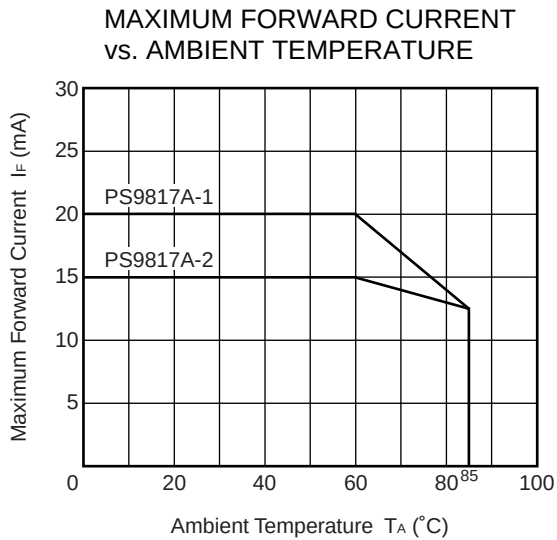
***4 Test circuit for common mode transient immunity**



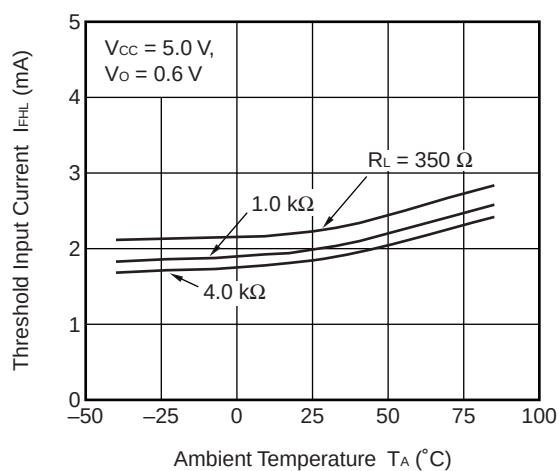
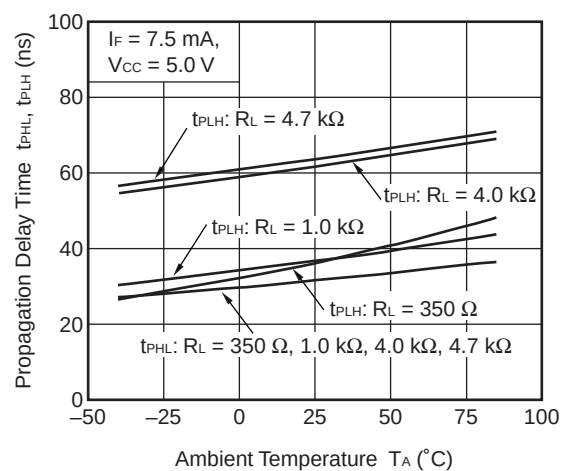
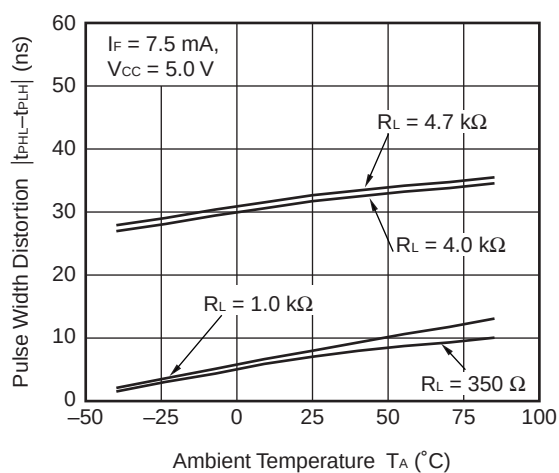
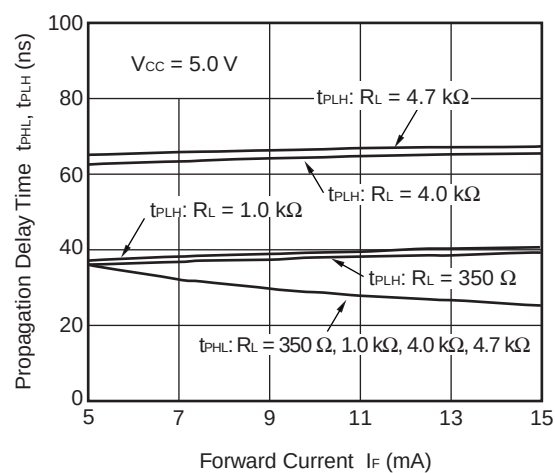
USAGE CAUTIONS

1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
2. By-pass capacitor of $0.1\mu F$ is used between V_{CC} and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm .
3. Avoid storage at a high temperature and high humidity.

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$, unless otherwise specified)



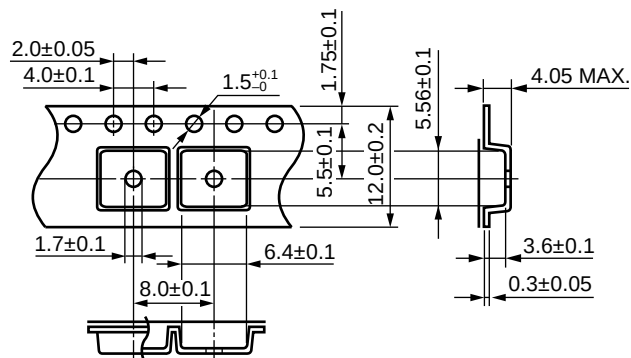
Remark The graphs indicate nominal characteristics.

THRESHOLD INPUT CURRENT vs.
AMBIENT TEMPERATUREPROPAGATION DELAY TIME vs.
AMBIENT TEMPERATUREPULSE WIDTH DISTORTION vs.
AMBIENT TEMPERATUREPROPAGATION DELAY TIME vs.
FORWARD CURRENT

Remark The graphs indicate nominal characteristics.

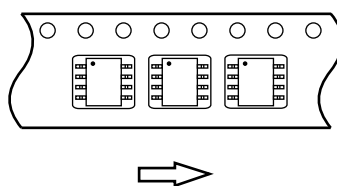
TAPING SPECIFICATIONS (UNIT: mm)

Outline and Dimensions (Tape)

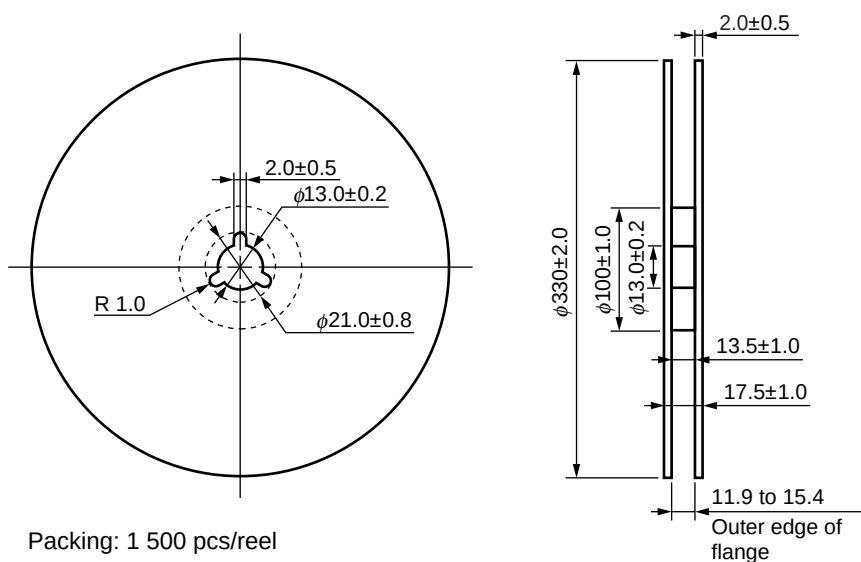


Taping Direction

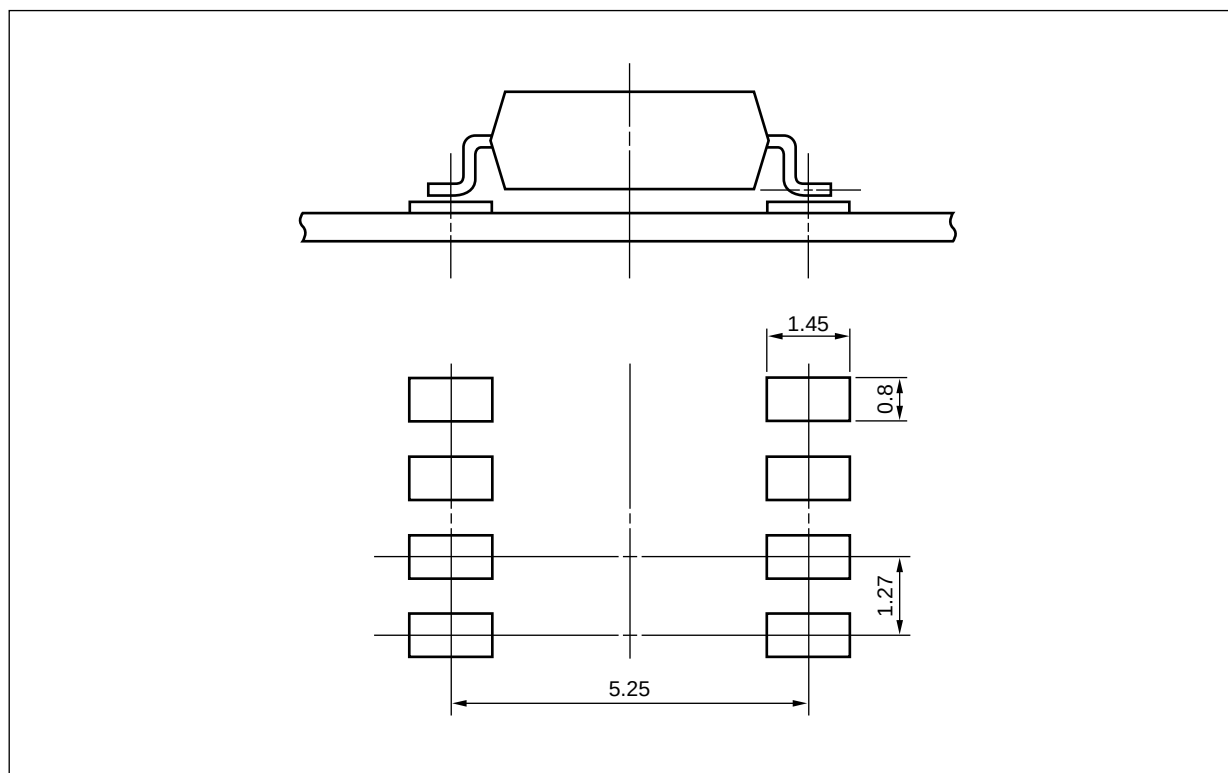
PS9817A-1-F3
PS9817A-2-F3



Outline and Dimensions (Reel)



RECOMMENDED MOUNT PAD DIMENSIONS (UNIT: mm)



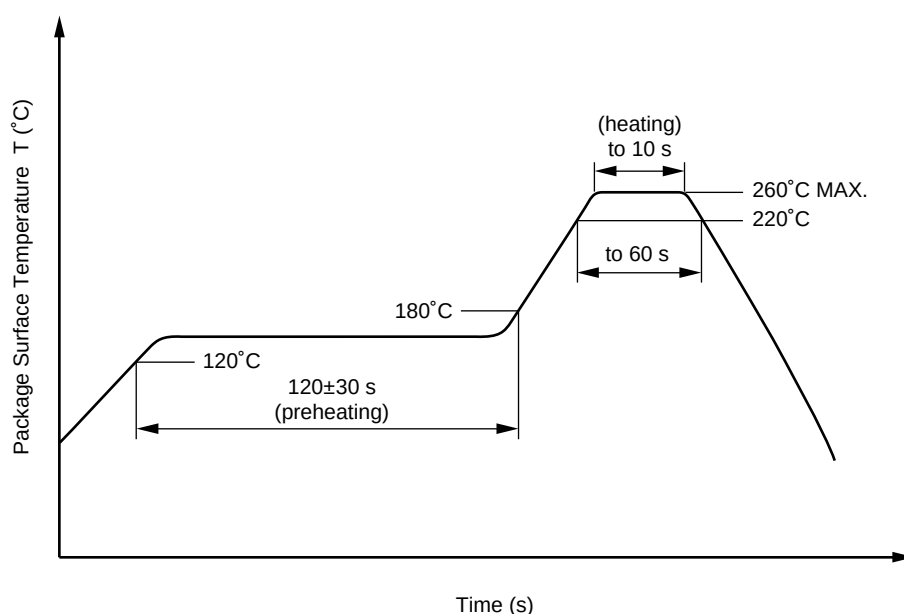
NOTES ON HANDLING

1. Recommended soldering conditions

(1) Infrared reflow soldering

- Peak reflow temperature 260°C or below (package surface temperature)
- Time of peak reflow temperature 10 seconds or less
- Time of temperature higher than 220°C 60 seconds or less
- Time to preheat temperature from 120 to 180°C 120±30 s
- Number of reflows Three
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

Recommended Temperature Profile of Infrared Reflow



(2) Wave soldering

- Temperature 260°C or below (molten solder temperature)
- Time 10 seconds or less
- Preheating conditions 120°C or below (package surface temperature)
- Number of times One (Allowed to be dipped in solder including plastic mold portion.)
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(3) Soldering by Soldering Iron

- Peak Temperature (lead part temperature) 350°C or below
- Time (each pins) 3 seconds or less
- Flux Rosin flux containing small amount of chlorine (The flux with a maximum chlorine content of 0.2 Wt% is recommended.)

(a) Soldering of leads should be made at the point 1.5 to 2.0 mm from the root of the lead

(b) Please be sure that the temperature of the package would not be heated over 100°C

(4) Cautions

- Fluxes

Avoid removing the residual flux with freon-based and chlorine-based cleaning solvent.

2. Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

USAGE CAUTIONS

1. Protect against static electricity when handling.
2. Avoid storage at a high temperature and high humidity.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter	Symbol	Speck	Unit
Application classification (DIN EN 60664-1 VDE0110 Part 1) for rated line voltages ≤ 300 Vr.m.s. for rated line voltages ≤ 600 Vr.m.s.		IV III	
Climatic test class (DIN EN 60664-1 VDE0110)		40/85/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}$, $P_d < 5$ pC	U_{IORM} U_{pr}	566 849	V_{peak} V_{peak}
Test voltage (partial discharge test, procedure b for all devices) $U_{pr} = 1.875 \times U_{IORM}$, $P_d < 5$ pC	U_{pr}	1 061	V_{peak}
Highest permissible overvoltage	U_{TR}	4 000	V_{peak}
Degree of pollution (DIN EN 60664-1 VDE0110 Part 1)		2	
Clearance distance		>4.0	mm
Creepage distance		>4.0	mm
Comparative tracking index (DIN IEC 112/VDE 0303 Part 1)	CTI	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)		III a	
Storage temperature range	T_{stg}	-55 to +125	°C
Operating temperature range	T_A	-40 to +85	°C
Isolation resistance, minimum value $V_{IO} = 500$ V dc at $T_A = 25^\circ\text{C}$ $V_{IO} = 500$ V dc at T_A MAX. at least 100°C	R_{is} MIN. R_{is} MIN.	10^{12} 10^{11}	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current I_F , $P_{si} = 0$) Power (output or total power dissipation) Isolation resistance $V_{IO} = 500$ V dc at $T_A = T_{si}$	T_{si} I_{si} P_{si} R_{is} MIN.	150 150 600 10^9	°C mA mW Ω

Caution

GaAs Products

This product uses gallium arsenide (GaAs).

GaAs vapor and powder are hazardous to human health if inhaled or ingested, so please observe the following points.

- Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
 1. Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
 2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
- Do not burn, destroy, cut, crush, or chemically dissolve the product.
- Do not lick the product or in any way allow it to enter the mouth.

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