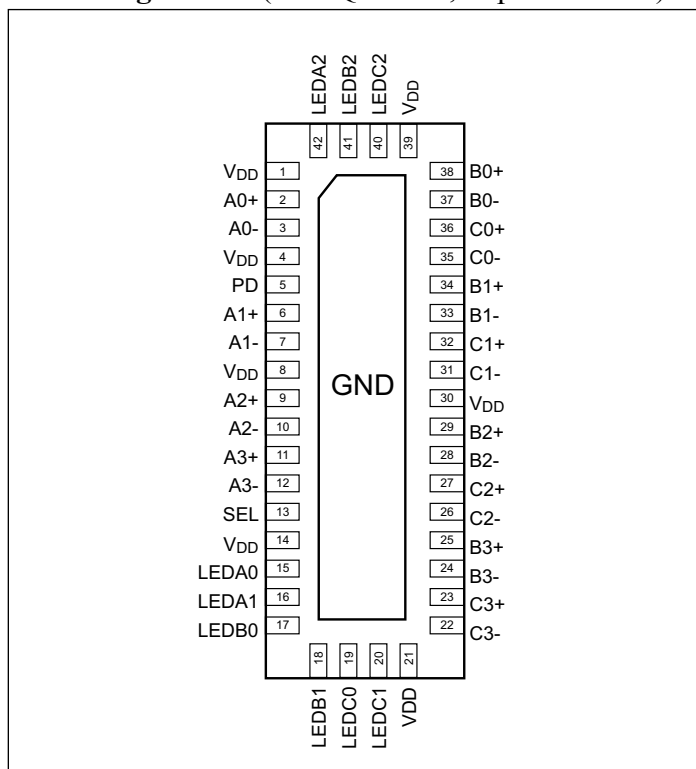


**PI3L720**

## Pin Configuration (42-TQFN ZH, Top-Side View)



## Pin Description

Pin Name	Description
A <sub>X</sub> <sup>+</sup> , A <sub>X</sub> <sup>-</sup>	Port A DeMux I/O
B <sub>X</sub> <sup>+</sup> , B <sub>X</sub> <sup>-</sup>	Port B Mux I/O
C <sub>X</sub> <sup>+</sup> , C <sub>X</sub> <sup>-</sup>	Port C LED Mux I/O
GND	Ground
LED <sub>ZX</sub>	LED I/O
PD	Power Down. Active high, with internal pull-down resistor
SEL	Select
V <sub>DD</sub>	Power

## Maximum Ratings

(Above which useful life may be impaired. For user guidelines, not tested.)

Storage Temperature.....	–65°C to +150°C
Supply Voltage to Ground Potential .....	–0.5V to +4.0V
DC Input Voltage.....	–0.5V to +5.5V
DC Output Current .....	120mA
Power Dissipation .....	0.5W

### Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

## DC Electrical Characteristics for 1000 Base-T Ethernet Switching over Operating Range

( $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{DD} = 3.3\text{V} \pm 10\%$ )

Parameter	Description	Test Conditions <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units
$V_{IH}$	Input HIGH Voltage	Guaranteed HIGH level (Control Pins)	2.0	–	–	V
$V_{IL}$	Input LOW Voltage	Guaranteed LOW level (Control Pins)	–0.5	–	0.8	
$V_{IK}$	Clamp Diode Voltage	$V_{DD} = \text{Max.}$ , $I_{IN} = -18\text{mA}$	–	–0.7	–1.2	
$I_{IH}$	Input HIGH Current	$V_{DD} = \text{Max.}$ , $V_{IN} = V_{DD}$	–	–	$\pm 2$	$\mu\text{A}$
$I_{IL}$	Input LOW Current	$V_{DD} = \text{Max.}$ , $V_{IN} = \text{GND}$	–	–	$\pm 2$	
$R_{ON}$	Switch On-Resistance <sup>(3)</sup>	$V_{DD} = \text{Min.}$ , $1.5\text{V} \leq V_{IN} \leq V_{DD}$ $I_{IN} = -40\text{mA}$	–	4.0	6.5	Ohm
$R_{FLAT(ON)}$	On-Resistance Flatness <sup>(3)</sup>	$V_{DD} = \text{Min.}$ , $V_{IN} @ 1.5\text{V}$ and $V_{DD}$ $I_{IN} = -40\text{mA}$	–	0.5	–	
$\Delta R_{ON}$	On-Resistance match from center ports to any other port <sup>(3)</sup>	$V_{DD} = \text{Min.}$ , $1.5\text{V} \leq V_{IN} \leq V_{DD}$ $I_{IN} = -40\text{mA}$	–	0.4	1.0	

## DC Electrical Characteristics for 10/100 Base-T Ethernet Switching over Operating Range

( $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{DD} = 3.3\text{V} \pm 10\%$ )

Parameter	Description	Test Conditions <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units
$V_{IH}$	Input HIGH Voltage	Guaranteed HIGH level (Control Pins)	2.0	–	–	V
$V_{IL}$	Input LOW Voltage	Guaranteed LOW level (Control Pins)	–0.5	–	0.8	
$V_{IK}$	Clamp Diode Voltage	$V_{DD} = \text{Max.}$ , $I_{IN} = -18\text{mA}$	–	–0.7	–1.2	
$I_{IH}$	Input HIGH Current	$V_{DD} = \text{Max.}$ , $V_{IN} = V_{DD}$	–	–	$\pm 2$	$\mu\text{A}$
$I_{IL}$	Input LOW Current	$V_{DD} = \text{Max.}$ , $V_{IN} = \text{GND}$	–	–	$\pm 2$	
$R_{ON}$	Switch On-Resistance <sup>(3)</sup>	$V_{DD} = \text{Min.}$ , $1.25\text{V} \leq V_{IN} \leq V_{DD}$ $I_{IN} = -10\text{mA}$ to $-30\text{mA}$	–	4.0	6.5	Ohm
$R_{FLAT(ON)}$	On-Resistance Flatness <sup>(3)</sup>	$V_{DD} = \text{Min.}$ , $V_{IN} @ 1.25\text{V}$ and $V_{DD}$ $I_{IN} = -10\text{mA}$ to $-30\text{mA}$	–	0.5	–	
$\Delta R_{ON}$	On-Resistance match from center ports to any other port <sup>(3)</sup>	$V_{DD} = \text{Min.}$ , $1.25\text{V} \leq V_{IN} \leq V_{DD}$ $I_{IN} = -10\text{mA}$ to $-30\text{mA}$	–	0.4	1.0	

**PI3L720**
**Capacitance**( $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{DD} = 3.3\text{V} \pm 10\%$ )

Parameters <sup>(4)</sup>	Description	Test Conditions <sup>(1)</sup>	Min.	Typ.	Max.	Units
$C_{IN}$	Input Capacitance	$V_{IN} = 0\text{V}$ , $f = 1\text{MHz}$		2.0	3.0	pF
$C_{OFF(B1, B2)}$	Port B Capacitance, Switch OFF			3.0	6.0	
$C_{ON(A/B)}$	A/B Capacitance, Switch ON			8.0	11.0	

**Notes:**

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at  $V_{DD} = 3.3\text{V}$ ,  $T_A = 25^{\circ}\text{C}$  ambient and maximum loading.
- Measured by the voltage drop between A and B pins at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A & B) pins.
- This parameter is determined by device characterization but is not production tested.

**Power Supply Characteristics**

Parameters	Description	Test Conditions <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units
$I_{DD\text{-Standby}}^{(3)}$	Quiescent Power Supply Current	$V_{DD} = \text{Max.}$ , $V_{IN} = \text{GND}$ or $V_{DD}$	—	0.3	0.5	mA
$I_{DD\text{-Active}}^{(3)}$	Active Power Supply Current	$V_{DD} = \text{Max.}$ , $V_{IN} = V_{DD}$ or GND		1.0	1.5	mA
$I_{DD\text{-PD}}^{(3)}$	Power Down Current	$\text{PD} = 1$ , $V_{DD} = \text{MAX}$ , $V_{IN} = V_{DD}$ or GND		0.15	0.25	mA

**Notes:**

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at  $V_{DD} = 3.3\text{V}$ ,  $T_A = 25^{\circ}\text{C}$  ambient and maximum loading..
- Active power represents normal data communication. Standby power is when the device is enabled for operation but there is no LAN traffic (cable not connected). Power down current is the minimum power state used when not connected and mobile.
- The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

**Dynamic Electrical Characteristics Over the Operating Range** ( $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{DD} = 3.3\text{V} \pm 10\%$ )

Parameter	Description	Test Conditions <sup>(1)</sup>	Min.	Typ. <sup>(2)</sup>	Max.	Units
$X_{TALK}$	Crosstalk <sup>(3)</sup>	$R_L = 100\text{-Ohm}$ , $f = 250\text{MHz}$	—	−75	—	dB
$O_{IRR}$	OFF Isolation <sup>(3)</sup>		—	−35	—	
BW	Bandwidth −3dB <sup>(3)</sup>	$R_L = 100\text{-Ohm}$	—	650	—	MHz

**Notes:**

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at  $V_{DD} = 3.3\text{V}$ ,  $T_A = 25^{\circ}\text{C}$  ambient and maximum loading..
- Guaranteed by design.
- The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

**PI3L720**
**Switching Characteristics** ( $T_A = -40^{\circ}\text{C}$  to  $+85^{\circ}\text{C}$ ,  $V_{DD} = 3.3\text{V} \pm 10\%$ )

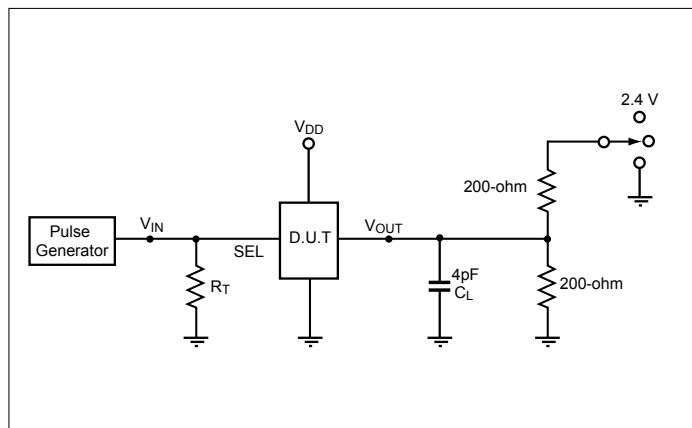
Parameter	Description	Min.	Typ. <sup>(2)</sup>	Max.	Units
$t_{PD}$	Propagation Delay <sup>(3,4)</sup>	–	0.25		ns
$t_{PZH}$ , $t_{PZL}$	Line Enable Time - SEL to $A_N$ , $B_N$	0.5	–	15.0	
$t_{PHZ}$ , $t_{PLZ}$	Line Disable Time - SEL to $A_N$ , $B_N$	0.5	–	5.0	
$t_{SK(o)}$	Output Skew between center port to any other port <sup>(3)</sup>	–	0.1	0.2	
$t_{SK(p)}$	Skew between opposite transitions of the same output ( $t_{PHL} - t_{PLH}$ ) <sup>(3)</sup>	–	0.1	0.2	
$t_{ON/OFF}$	Device enable / disable time from PD		100	200	

**Notes:**

- For max. or min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
- Typical values are at  $V_{DD} = 3.3\text{V}$ ,  $T_A = 25^{\circ}\text{C}$  ambient and maximum loading..
- Guaranteed by design.
- The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 10pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interactions with the load on the driven side.

**PI3L720**

## Test Circuit for Electrical Characteristics



### Notes:

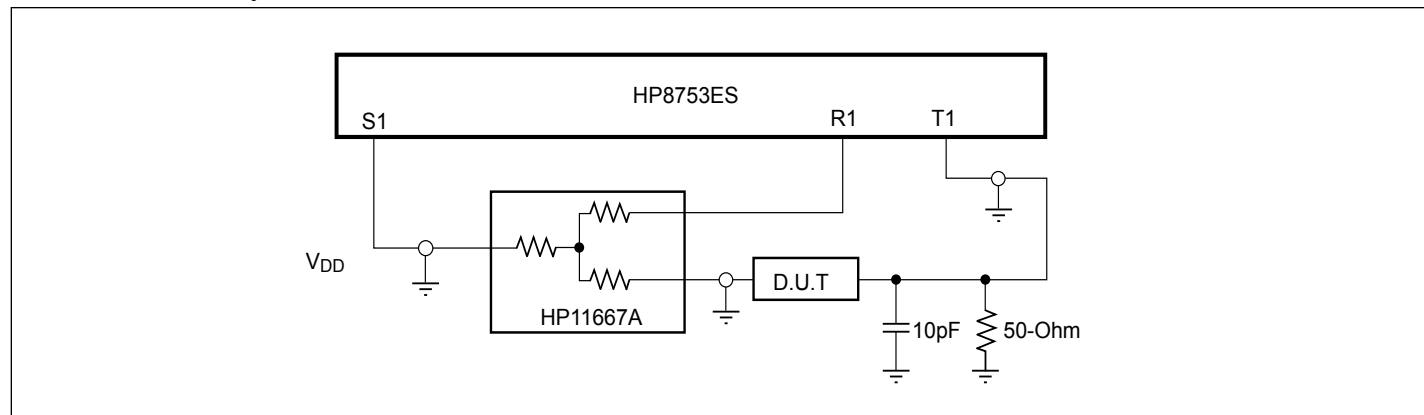
$C_L$  = Load capacitance: includes jig and probe capacitance.

$R_T$  = Termination resistance: should be equal to  $Z_{OUT}$  of the Pulse Generator

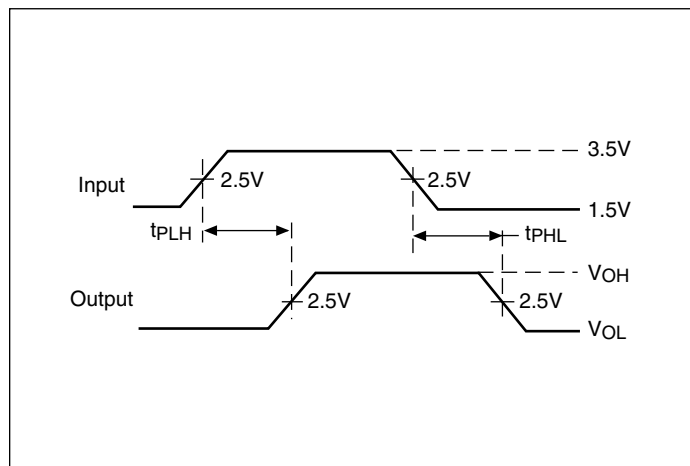
## Switch Positions

Test	Switch
$t_{PLZ}$ , $t_{PZL}$ (output on B-side)	6.0V
$t_{PHZ}$ , $t_{PZH}$ (output on B-side)	GND
Prop Delay	Open

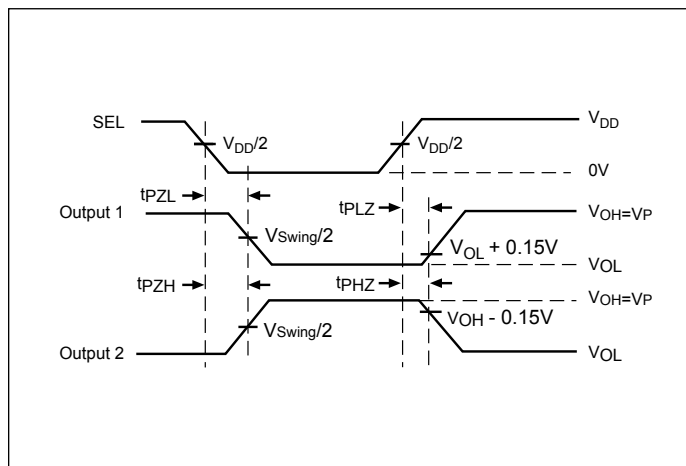
## Test Circuit for Dynamic Electrical Characteristics



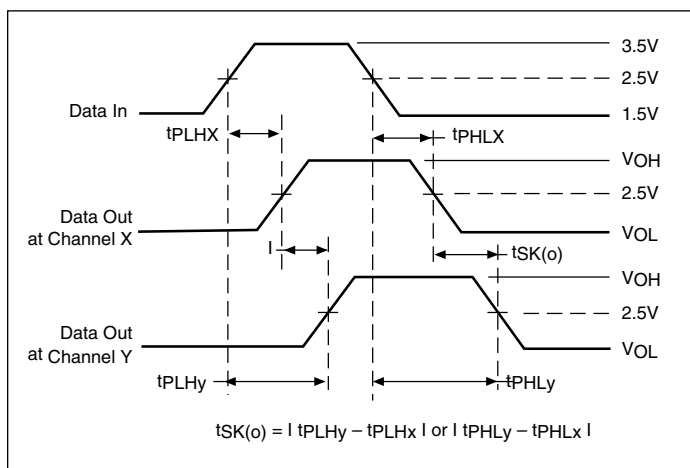
## Switching Waveforms



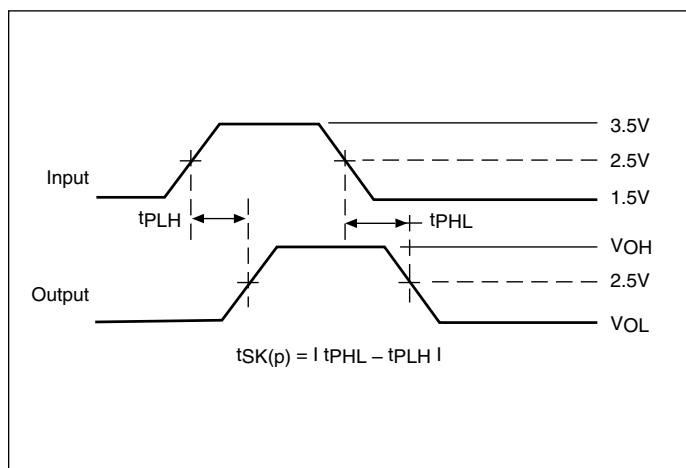
**Voltage Waveforms Propagation Delay Times**



**Voltage Waveforms Enable and Disable Times**



**Output Skew -  $t_{SK(o)}$**



**Pulse Skew -  $t_{SK(p)}$**

### Notes:

Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.

Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

All input impulses are supplied by generators having the following characteristics:  $PRR \leq \text{MHz}$ ,  $Z_O = 50\text{-}\Omega$ ,  $t_r \leq 2.5\text{ns}$ ,  $t_f \leq 2.5\text{ns}$ .

## Applications Information

### Logic Inputs

The logic control inputs can be driven up to +3.6V regardless of the supply voltage. For example, given a +3.3V supply, the output enables or select pins may be driven low to 0V and high to 3.6V. Driving IN Rail-to-Rail® minimizes power consumption.

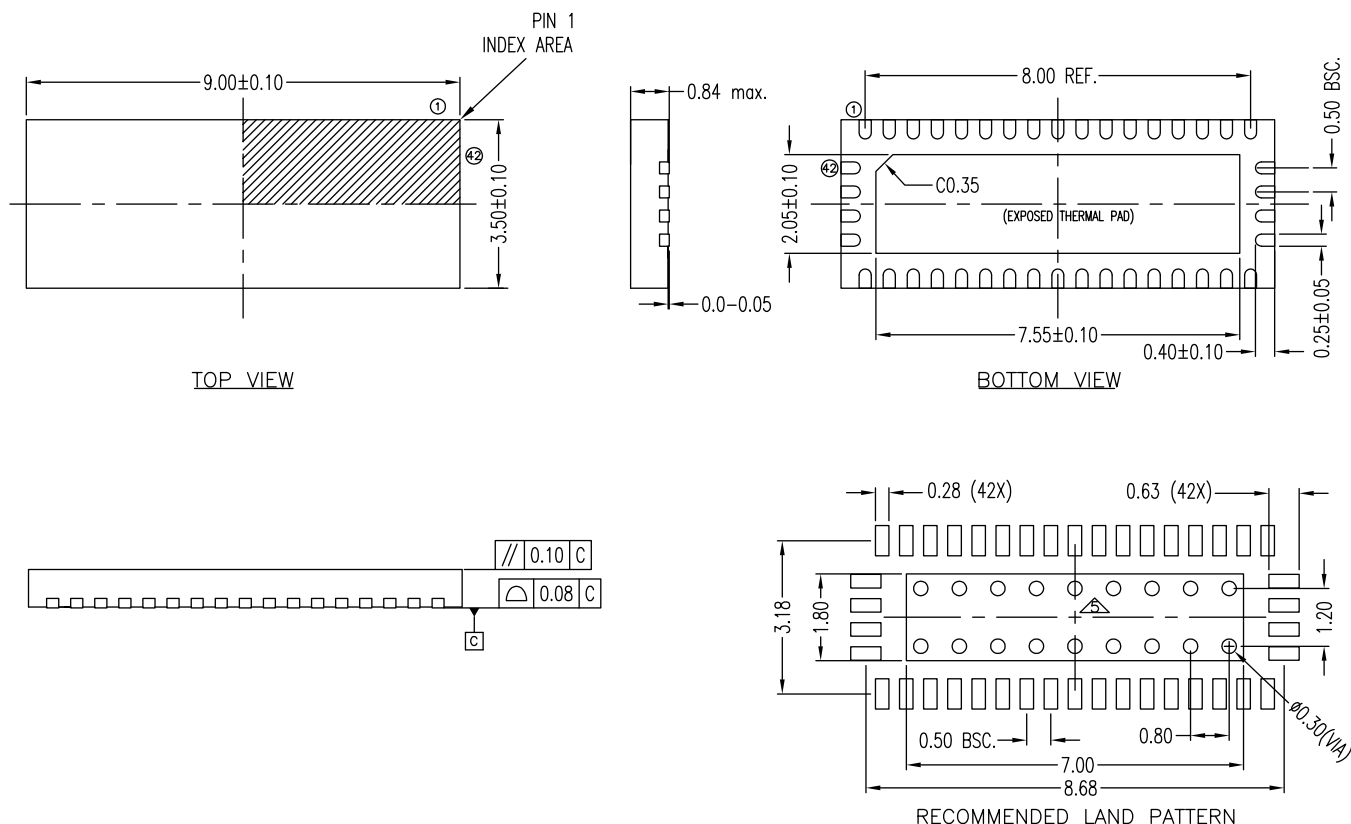
### Power-Supply Sequencing

Proper power-supply sequencing is advised for all CMOS devices. It is recommended to always apply  $V_{DD}$  before applying signals to the input/output or control pins.

Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd

**PI3L720**

# Packaging Mechanical: 42-Contact TQFN (ZH)



## NOTE :

1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
2. COPLANARITY APPLIES TO THE EXPOSED THERMAL PAD AS WELL AS THE TERMINALS.
3. REFER JEDEC MO-220
4. RECOMMENDED LAND PATTERN IS FOR REFERENCE ONLY.
5. THERMAL PAD SOLDERING AREA (MESH STENCIL IS RECOMMENDED).

16-0161

<b>DIODES</b>	<b>PERICOM</b>	DATE: 07/28/16
DESCRIPTION: 42-Contact, Very Thin Quad Flat No-Lead (TQFN)		
PACKAGE CODE: ZH (ZH42)		
DOCUMENT CONTROL #: PD-2035		
REVISION: F		

Note: For latest package info, please check: <http://www.pericom.com/support/packaging/packaging-mechanicals-and-thermal-characteristics/>

## Ordering Information

Ordering Number	Package Code	Package Description
PI3L720ZHE	ZH	42-contact, Very Thin Quad Flat No-Lead (TQFN)
PI3L720ZHEX	ZH	42-contact, Very Thin Quad Flat No-Lead (TQFN), Tape & Reel

## Notes:

- Thermal characteristics can be found on the company web site at [www.pericom.com/packaging/](http://www.pericom.com/packaging/)
- E = Pb-free & Green
- Adding an X suffix = Tape/Reel

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