

**Absolute Maximum Ratings** (Note 1)

Supply Voltage ( $V_{CC}$ )	-0.5V to +7.0V
DC Switch Voltage ( $V_S$ )	-0.5V to +7.0V
DC Input Voltage ( $V_{IN}$ ) (Note 2)	-0.5V to +7.0V
DC Input Diode Current ( $I_{IK}$ ) $V_{IN} < 0V$	-50 mA
DC Output ( $I_{OUT}$ ) Sink Current	128 mA
DC $V_{CC}/GND$ Current ( $I_{CC}/I_{GND}$ )	$\pm 100$ mA
Storage Temperature Range ( $T_{STG}$ )	-65°C to +150°C
Junction Temperature under Bias ( $T_J$ )	+150°C
Junction Lead Temperature ( $T_L$ ) (Soldering, 10 Seconds)	+260°C
Power Dissipation ( $P_D$ ) @ +85°C	
SOT23-5	200 mW
SC70-5	150 mW

**Recommended Operating Conditions** (Note 3)

Power Supply Operating ( $V_{CC}$ )	4.0V to 5.5V
Input Voltage ( $V_{IN}$ )	0V to 5.5V
Output Voltage ( $V_{OUT}$ )	0V to 5.5V
Input Rise and Fall Time ( $t_r, t_f$ )	
Switch Control Input	0 ns/V to 5 ns
Switch I/O	0 ns/V to DC
Operating Temperature ( $T_A$ )	-40°C to +85°C
Thermal Resistance ( $\theta_{JA}$ )	
SOT23-5	300°C/W
SC70-5	425°C/W

**Note 1:** The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the Electrical Characteristics tables are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

**Note 2:** The input and output negative voltage ratings may be exceeded if the input and output diode current ratings are observed.

**Note 3:** Unused inputs must be held HIGH or LOW. They may not float.

**DC Electrical Characteristics**

Symbol	Parameter	$V_{CC}$ (V)	$T_A = -40^\circ\text{C to } +85^\circ\text{C}$			Units	Conditions
			Min	Typ	Max		
$V_{IK}$	Clamp Diode Voltage	4.5			-1.2	-V	$I_{IN} = -18$ mA
$V_{IH}$	HIGH Level Input Voltage	4.5-5.5	2.0			V	
$V_{IL}$	LOW Level Input Voltage	4.5-5.5			0.8	V	
$I_{IN}$	Input Leakage Current	5.5			$\pm 1.0$	$\mu\text{A}$	$0 \leq V_{IN} \leq 5.5V$
$I_{OFF}$	"OFF" Leakage Current	5.5			$\pm 10.0$	$\mu\text{A}$	$0 \leq A, B \leq V_{CC}$
$R_{ON}$	Switch On Resistance (Note 4)	4.5		3	7	$\Omega$	$V_{IN} = 0V, I_{IN} = 64$ mA
		4.5		3	7	$\Omega$	$V_{IN} = 0V, I_{IN} = 30$ mA
		4.5		6	15	$\Omega$	$V_{IN} = 2.4V, I_{IN} = 15$ mA
		4.0		10	20	$\Omega$	$V_{IN} = 2.4V, I_{IN} = 15$ mA
$I_{CC}$	Quiescent Supply Current	5.5			10	$\mu\text{A}$	$V_{IN} = V_{CC}$ or GND $I_O = 0$
$\Delta I_{CC}$	Increase in $I_{CC}$ per Input (Note 5)	5.5		0.9	2.5	mA	$V_{IN} = 3.4V, I_O = 0$ , Control Input only

**Note 4:** Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

**Note 5:** Per TTL driven input ( $V_{IN} = 3.4V$ , control input only). A and B pins do not contribute to  $I_{CC}$ .

## AC Electrical Characteristics

Symbol	Parameter	V <sub>CC</sub> (V)	T <sub>A</sub> = -40°C to +85°C, C <sub>L</sub> = 50 pF, R <sub>U</sub> = R <sub>D</sub> = 500Ω			Units	Conditions	Fig. No.
			Min	Typ (Note 6)	Max			
t <sub>PHL</sub> , t <sub>PLH</sub>	Prop Delay Bus to Bus (Note 7)	4.0–5.5			0.25	ns	V <sub>I</sub> = OPEN	Figures 1, 2
t <sub>PZL</sub> , t <sub>PZH</sub>	Output Enable Time	4.5–5.5	1.0	2.5	5.0	ns	V <sub>I</sub> = 7V for t <sub>PZL</sub>	Figures 1, 2
		4.0	1.0		5.5	ns	V <sub>I</sub> = OPEN for t <sub>PZH</sub>	
t <sub>PLZ</sub> , t <sub>PHZ</sub>	Output Disable Time	4.5–5.5	1.0	2.5	5.0	ns	V <sub>I</sub> = 7V for t <sub>PLZ</sub>	Figures 1, 2
		4.0	1.0		5.5	ns	V <sub>I</sub> = OPEN for t <sub>PHZ</sub>	

## Capacitance (Note 8)

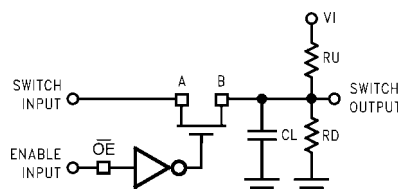
Symbol	Parameter	Typ	Max	Units	Conditions
C <sub>IN</sub>	Control Pin Input Capacitance	2	6	pF	V <sub>CC</sub> = 5.0V
C <sub>I/O</sub>	Input/Output Capacitance	4.5	10	pF	V <sub>CC</sub> , $\overline{\text{BE}}$ = 5.0V

**Note 6:** All typical values are V<sub>CC</sub> = 5.0V, T<sub>A</sub> = 25°C.

**Note 7:** This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical On resistance of the switch and the 50 pF load capacitance, when driven by an ideal voltage source (zero output impedance).

**Note 8:** T<sub>A</sub> = 25°C, f = 1 MHz.

## AC Loading and Waveforms



Input driven by 50Ω source terminated in 50Ω

C<sub>L</sub> includes load and stray capacitance

Input PRR = 1.0 MHz; t<sub>W</sub> = 500 ns

FIGURE 1. AC Test Circuit

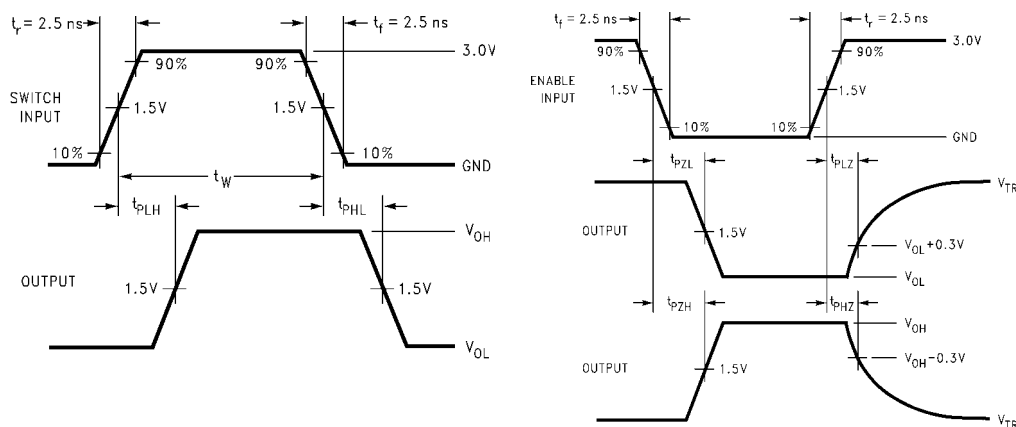
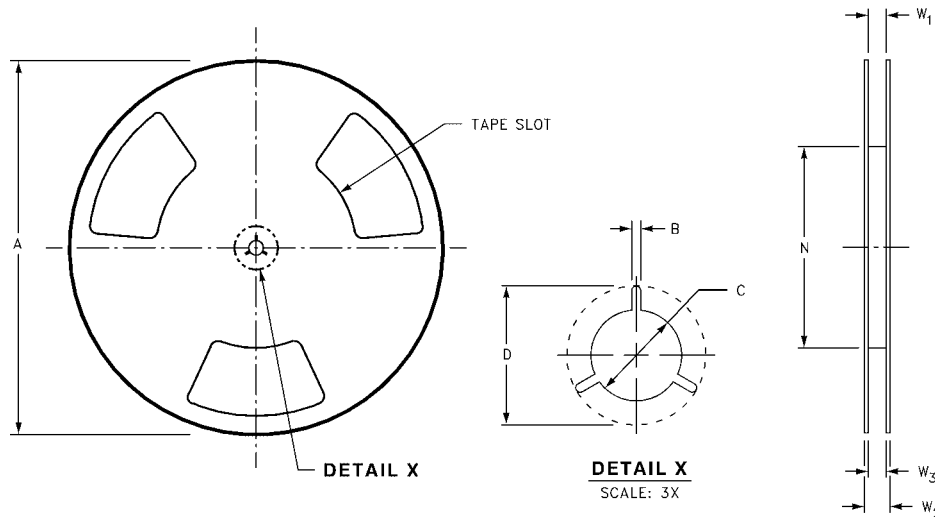


FIGURE 2. AC Waveforms



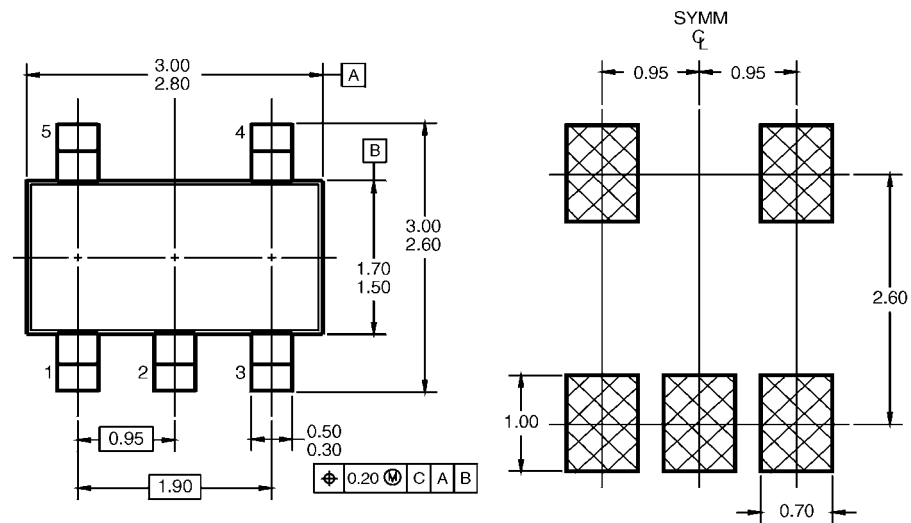
# Tape and Reel Specification (Continued)

REEL DIMENSIONS inches (millimeters)

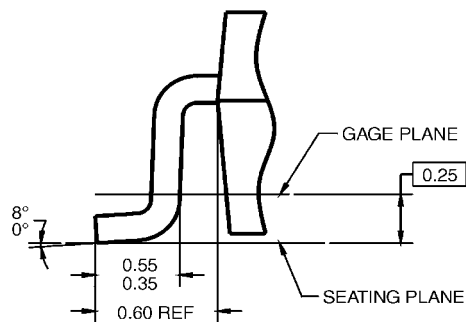
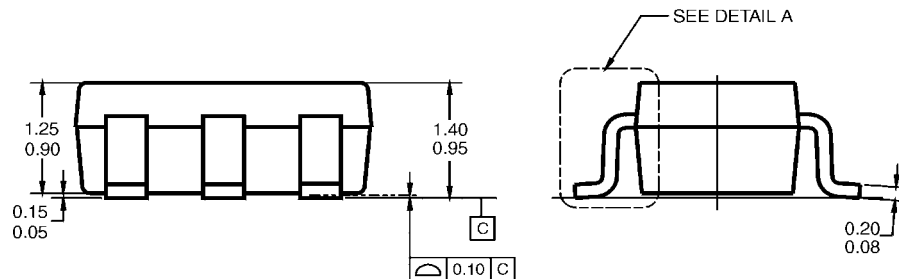


Tape Size	A	B	C	D	N	W1	W2	W3
8 mm	7.0 (177.8)	0.059 (1.50)	0.512 (13.00)	0.795 (20.20)	2.165 (55.00)	0.331 + 0.059/-0.000 (8.40 + 1.50/-0.00)	0.567 (14.40)	W1 + 0.078/-0.039 (W1 + 2.00/-1.00)

# Physical Dimensions inches (millimeters) unless otherwise noted



## LAND PATTERN RECOMMENDATION



## NOTES: UNLESS OTHERWISE SPECIFIED

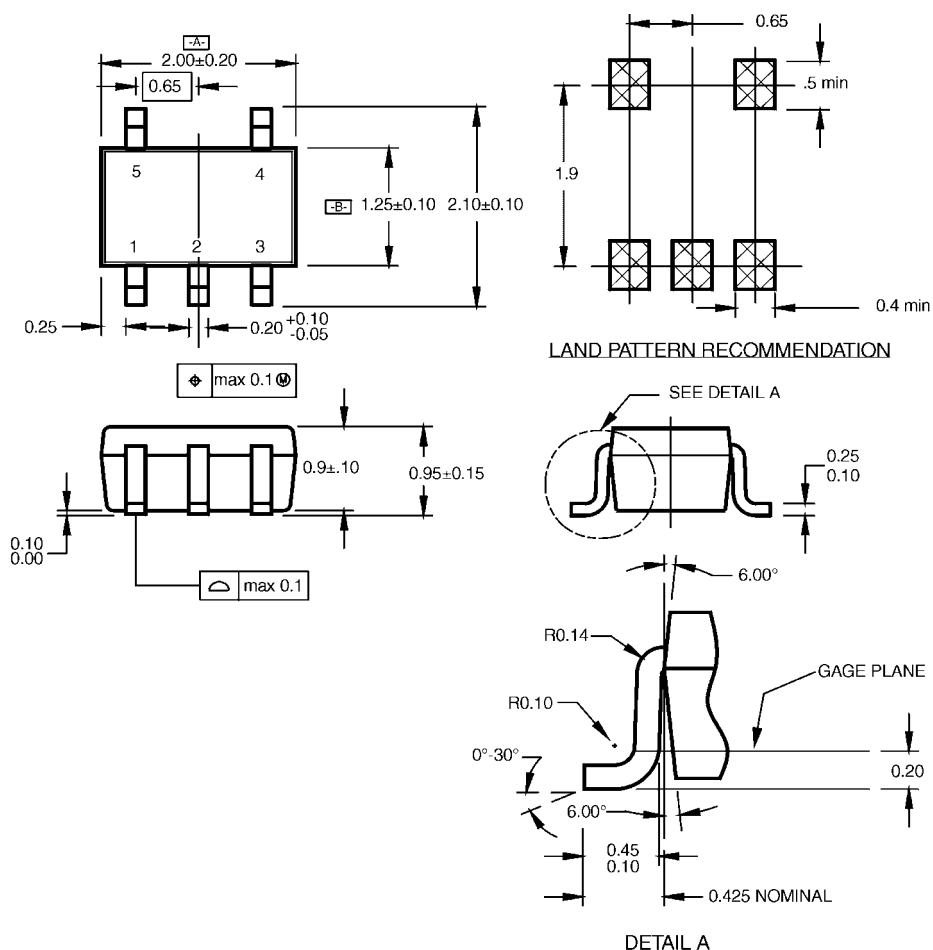
- A) THIS PACKAGE CONFORMS TO JEDEC MO-178, ISSUE B, VARIATION AA, DATED JANUARY 1999.
- B) ALL DIMENSIONS ARE IN MILLIMETERS.

MA05BRevC

## DETAIL A

5-Lead SOT23, JEDEC MO-178, 1.6mm  
Package Number MA05B

# Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



## NOTES:

- A. CONFORMS TO EIAJ REGISTERED OUTLINE DRAWING SC88A.
- B. DIMENSIONS DO NOT INCLUDE BURRS OR MOLD FLASH.
- C. DIMENSIONS ARE IN MILLIMETERS.

MAA05ARevC

## 5-Lead SC70, EIAJ SC-88a, 1.25mm Wide Package Number MAA05A

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