

Figure 1. Logic Diagram

Figure 2. Dip Pin Assignment

Figure 3. PLCC-20 Pin Assignment

Table 1. MAXIMUM RATINGS

Symbol	Characteristic	Rating	Unit
V _{CC}	Power Supply (V _{EE} = GND)	7.0	Vdc
T _A	Operating Temperature Range	0 to +75	°C
T _{stg}	Storage Temperature Range - Plastic	−55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

Table 2. ELECTRICAL CHARACTERISTICS (V_{CC} = 5.0 V \pm 5%) (Note 1)

			T _A = 0°C to 75°C		
Symbol	Characteristic		Min	Max	Unit
Icc	Power Supply Current	TTL	-	20	mA
		ECL	_	12	
,l _{IH}	Input Current High	Pin 9	-	20	μΑ
I _{INH}		Others	_	50	
I_{IL}	Input Current Low	Pin 9	_	-0.6	mA
I _{INL}		Others	_	50	μΑ
V_{IH}	Input Voltage High	Pin 9	2.0	_	Vdc
V_{IL}	Input Voltage Low	Pin 9	-	0.8	Vdc
V_{DIFF}	Differential Input Voltage (Note 1)		350	_	mV
	Pins 3–6, 11–14 (1)				
V _{CM}	Voltage Common Mode		2.8	V _{CC}	Vdc
	Pins 3–6, 11–14				
V _{OH}	Output Voltage High		2.7	-	Vdc
	I _{OH} = 3.0 mA				
V _{OL}	Output Voltage Low		_	0.5	Vdc
	I _{OL} = 20 mA				
Ios	Short Circuit Current		-60	-150	mA
	V _{OUT} = 0 V				
I _{OZH}	Output Disable Current High		_	50	μΑ
	V _{OUT} = 2.7 V				
I _{OZL}	Output Disable Current Low		-	-50	μΑ
	V _{OUT} = 0.5 V				

^{*}Positive Emitter Coupled Logic

Table 3. AC PARAMETERS (C_L = 50 pF) (V_{CC} = 5.0 \pm 5%) (T_A = 0°C to 75°C)

		T _A = 0°C to 75°C		
Symbol	Characteristic	Min	Max	Unit
t _{pd}	Propagation Delay Data (50% to 1.5 V)	1.5	5.0	ns
t _r	Rise Time (Note 4)	0.3	1.6	ns
t _f	Fall Time (Note 4)	0.3	1.6	ns
t _{pdLZ} t _{pdHZ}	Output Disable Time	2.0 2.0	6.0 6.0	ns
t _{pdZL} t _{pdZH}	Output Enable Time	2.0 2.0	8.0 8.0	ns

NOTE: Device will meet the specifications after thermal equilibrium has been established when mounted in a test socket or printed circuit board with maintained transverse airflow greater than 500 lfpm. Electrical parameters are guaranteed only over the declared operating temperature range. Functional operation of the device exceeding these conditions is not implied. Device specification limit values are applied individually under normal operating conditions and not valid simultaneously.

4. 1.0 V to 2.0 V w/50 pF into 500 Ω .

^{1.} Common mode input voltage to pins 3-4, 5-6, 11-12, 13-14 must be between the values of 2.8 V and 5.0 V. This common mode input voltage range includes the differential input swing.

For single–ended use, apply 3.75 V (V_{BB}) to either input depending on output polarity required. Signal level range to other input is 3.3 V to 4.2 V.
 Any unused gates should have the inverting inputs tied to V_{CC} and the noninverting inputs tied to ground to prevent output glitching.

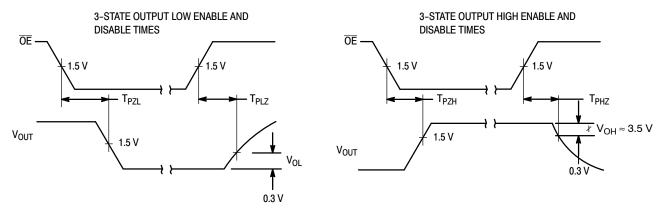
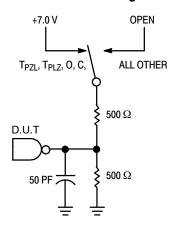


Figure 4. 3-State Switching Waveforms



*INCLUDES JIG AND PROBE CAPACITANCE

Application Note: Pin 9 is an \overline{OE} and the MC10H350 is disabled when \overline{OE} is at V_{IH} or higher.

Figure 5. Test Load

ORDERING INFORMATION

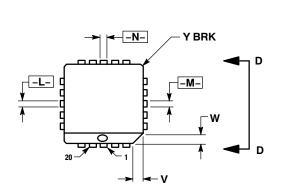
Device	Package	Shipping [†]	
MC10H350FNG	PLCC-20 (Pb-Free)	46 Units / Rail	
MC10H350FNR2G	PLCC-20 (Pb-Free)	500 / Tape & Reel	
MC10H350P	PDIP-16	25 Unit / Rail	
MC10H350PG	PDIP-16 (Pb-Free)	25 Unit / Rail	

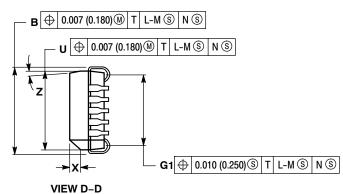
[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

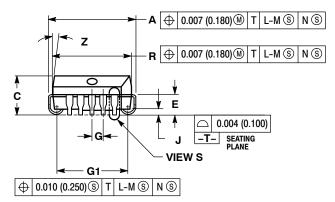
PACKAGE DIMENSIONS

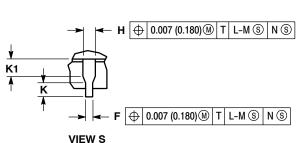
20 LEAD PLCC

CASE 775-02 ISSUE F









- NOTES:
 1. DIMENSIONS AND TOLERANCING PER ANSI Y14.5M,
- 1962.

 DIMENSIONS IN INCHES.

 DATUMS -L-, -M-, AND -N- DETERMINED WHERE TOP OF LEAD SHOULDER EXITS PLASTIC BODY AT MOLD
- PARTING LINE.

 4. DIMENSION G1, TRUE POSITION TO BE MEASURED AT DATUM -T-, SEATING PLANE.

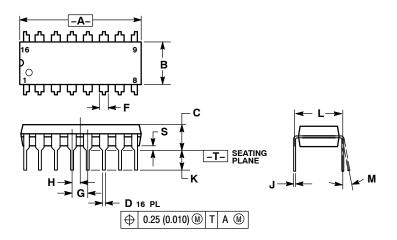
 5. DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH.
- 5. DIMENSIONS R AND U DO NOT INCLUDE MOLD FLASH.
 ALLOWABLE MOLD FLASH IS 0.010 (0.250) PER SIDE.
 6. DIMENSIONS IN THE PACKAGE TOP MAY BE SMALLER
 THAN THE PACKAGE BOTTOM BY UP TO 0.012 (0.300).
 DIMENSIONS R AND U ARE DETERMINED AT THE
 OUTERMOST EXTREMES OF THE PLASTIC BODY
 EXCLUSIVE OF MOLD FLASH, TIE BAR BURRS, GATE
 BURRS AND INTERLEAD FLASH, BUT INCLUDING ANY
 MISMATCH BETMEEN THE TOP AND ROTTOM OF THE MISMATCH BETWEEN THE TOP AND BOTTOM OF THE PLASTIC BODY.
- PLASTIC BODY.

 DIMENSION H DOES NOT INCLUDE DAMBAR
 PROTRUSION OR INTRUSION. THE DAMBAR
 PROTRUSION(S) SHALL NOT CAUSE THE H DIMENSION
 TO BE GREATER THAN 0.037 (0.940). THE DAMBAR
 INTRUSION(S) SHALL NOT CAUSE THE H DIMENSION TO
 BE SMALLER THAN 0.025 (0.635).

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.385	0.395	9.78	10.03
В	0.385	0.395	9.78	10.03
C	0.165	0.180	4.20	4.57
Е	0.090	0.110	2.29	2.79
F	0.013	0.021	0.33	0.53
G	0.050	BSC	1.27	BSC
Н	0.026	0.032	0.66	0.81
7	0.020		0.51	
K	0.025		0.64	
R	0.350	0.356	8.89	9.04
J	0.350	0.356	8.89	9.04
>	0.042	0.048	1.07	1.21
W	0.042	0.048	1.07	1.21
Х	0.042	0.056	1.07	1.42
Υ		0.020		0.50
Z	2°	10°	2°	10 °
G1	0.310	0.330	7.88	8.38
K1	0.040		1.02	

PACKAGE DIMENSIONS

PDIP-16 **P SUFFIX** CASE 648-08 **ISSUE T**



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- ANSI Y14.5M, 1982.
 CONTROLLING DIMENSION: INCH.
 DIMENSION L TO CENTER OF LEADS
 WHEN FORMED PARALLEL.
 DIMENSION B DOES NOT INCLUDE
- MOLD FLASH.

 5. ROUNDED CORNERS OPTIONAL.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN MAX	
Α	0.740	0.770	18.80	19.55
В	0.250	0.270	6.35	6.85
С	0.145	0.175	3.69	4.44
D	0.015	0.021	0.39	0.53
F	0.040	0.70	1.02	1.77
G	0.100 BSC		2.54 BSC	
Н	0.050 BSC		1.27 BSC	
J	0.008	0.015	0.21	0.38
K	0.110	0.130	2.80	3.30
L	0.295	0.305	7.50	7.74
М	0°	10 °	0 °	10 °
S	0.020	0.040	0.51	1.01

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