ABSOLUTE MAXIMUM RATINGS

V _{DD} to GND0.3V to +6V	Continuous Power Dissipation (T _A = +70°C)
CS, INC, U/D to GND0.3V to +6V	6-Pin SOT23 (derate 6.25mW/°C above +70°C)500mW
H, L, W to GND0.3V to (V _{DD} + 0.3V)	8-Pin µMAX (derate 4.1mW/°C above +70°C)330mW
Input and Output Latchup Immunity±200mA	Operating Temperature Range40°C to +85°C
Maximum Continuous Current into H, L, and W	Storage Temperature Range65°C to +150°C
MAX516E±1mA	Lead Temperature (soldering, 10s)+300° C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

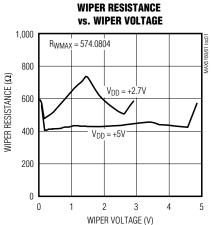
 $(V_{DD} = +2.7V \text{ to } +5.5V, V_H = V_{DD}, V_L = 0, T_A = T_{MIN} \text{ to } T_{MAX}. \text{ Typical values are at } V_{DD} = +5V, T_A = +25^{\circ}C, \text{ unless otherwise noted.})$

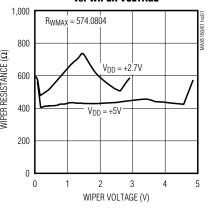
PARAMETER	SYMBOL	CONI	DITIONS	MIN	TYP	MAX	UNITS	
DC PERFORMANCE								
Resolution					3		%	
Integral Nonlinearity (Note 1)	INL					±1/2	LSB	
Differential Nonlinearity (Note 1)	DNL					±1	LSB	
End-to-End Resistor Tempco	TCR				50		ppm/°C	
Ratiometric Resistor Tempco					5		ppm/°C	
Full-Scale Error						-0.1	LSB	
Zero-Scale Error						+0.1	LSB	
Wiper Resistance	Rw				400	1700	Ω	
Wiper Capacitance	Cw				10		рF	
		MAX516_NE		150	200	250		
End-to-End Resistance	HL	MAX516_ME		75	100	125	kΩ	
		MAX516_LE		37.5	50	62.5	1	
DIGITAL INPUTS	•			<u>"</u>				
Input High Voltage	VIH			$0.7 \times V_D$	D		V	
Input Low Voltage	VIL				(0.3 × V _{DD}	V	
Input Leakage Current						±1	μΑ	
Input Capacitance					5		pF	
TIMING CHARACTERISTICS (F	igure 6)							
CS to INC Setup Time	tcı			25			ns	
CS to INC Hold Time	tıc			0			ns	
ĪNC Low Period	tıL			25			ns	
INC High Period	tıH			25			ns	
U/D to INC Hold	t _{ID}			0			ns	
U/D to INC Setup	t _{DI}			50			ns	
Wiper-Settling Time	tıw				1		μs	
INC Frequency	fIMAX					7	MHz	
POWER SUPPLIES	1	I					ı	
Supply Voltage	V _{DD}			2.7		5.5	V	
Supply Current	I _{DD}	$\overline{\text{CS}} = \overline{\text{INC}} = \overline{\text{U/D}} =$	$V_{DD} = +5V$		0.6	10	μΑ	
Supply Current		V _{DD} or GND	$V_{DD} = +2.7V$		135		nA	

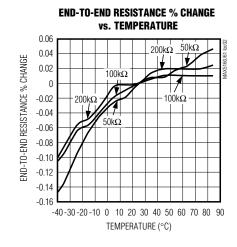
Note 1: For the MAX5160, linearity is defined in terms of H to L code-dependent resistance.

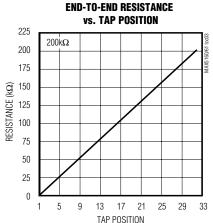
Typical Operating Characteristics

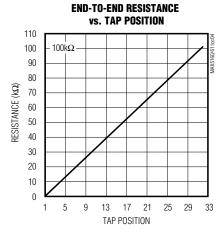
 $(V_{DD} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$

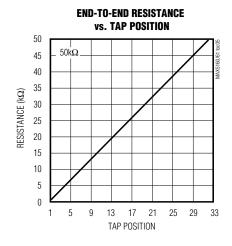








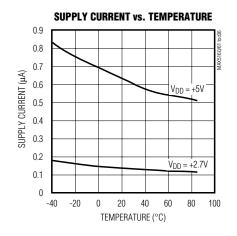


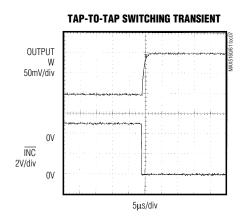


N/IXI/N

Typical Operating Characteristics (continued)

 $(V_{DD} = +5V, T_A = +25^{\circ}C, unless otherwise noted.)$





Pin Description

PIN		NAME	FUNCTION		
MAX5160	MAX5161	NAME	FUNCTION		
1	5	ĪNC	Wiper Increment Control Input. With $\overline{\text{CS}}$ low, a high-to-low transition increments (U/ $\overline{\text{D}}$ high) or decrements (U/ $\overline{\text{D}}$ low) the wiper position.		
2	4	U/D	Up/Down Control Input. With $\overline{\text{CS}}$ low, a high-to-low $\overline{\text{INC}}$ transition increments (U/ $\overline{\text{D}}$ high) or decrements (U/ $\overline{\text{D}}$ low) the wiper position.		
3	2	Н	High Terminal of Resistor		
4	3	GND	Ground		
5	_	W	Wiper Terminal of Resistor		
6	1	L	Low Terminal of Resistor		
7	_	CS	Chip-Select Input. Drive low to change the wiper position through $\overline{\text{INC}}$ and $\overline{\text{U/D}}$.		
8	6	V _{DD}	Power Supply		

+ ______ N/XI/N

Detailed Description

The MAX5160/MAX5161 consist of resistor arrays with thirty-one resistive elements. Thirty-two tap points are accessible to the wiper along the resistor string between H and L. Logic inputs \overline{CS} , U/\overline{D} , and \overline{INC} determine the position of the wiper. With \overline{CS} low and U/\overline{D} high, a high-to-low transition on \overline{INC} increments the internal counter, increasing the resistance between W and L. When both \overline{CS} and U/\overline{D} are low, a high-to-low \overline{INC} transition decrements the internal counter, decreasing the resistance between W and L. At either end (maximum or minimum positions), additional transitions in the direction of the end points will not change the counter value (the counter will not wrap around).

The H and L terminals of the MAX5160 are similar to the two end terminals of a mechanical potentiometer. The tap W is equivalent to the variable tap (wiper) of the potentiometer.

The MAX5161 is similar to the MAX5160 except that $\overline{\text{CS}}$ internally connects to ground and the wiper terminal (W) is shorted to the high terminal (H). The MAX5161 acts as

a variable resistor (a potentiometer with the wiper and one end terminal shorted together).

The MAX5160/MAX5161 feature power-on reset circuitry that sets the wiper position to midscale at power-up.

Applications Information

The MAX5160/MAX5161 are intended for circuits requiring digitally controlled adjustable voltage or adjustable gain, such as LCD contrast control, where voltage biasing adjusts the display contrast.

Controlling a Switch-Mode LCD Bias Generator

Figure 1 shows an application where the MAX5161 is used with a MAX1771 to make an adjustable positive LCD-bias circuit. The output of the MAX1771 is a positive voltage that is digitally controlled through the MAX5160/MAX5161. Similarly, Figure 2 shows the application of the MAX5161 in a digitally controlled negative LCD-bias circuit along with the MAX774/MAX775/MAX776.

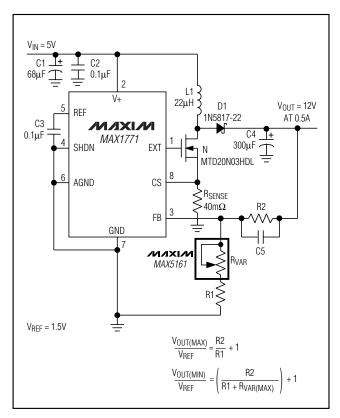


Figure 1. Adjustable Positive LCD Bias

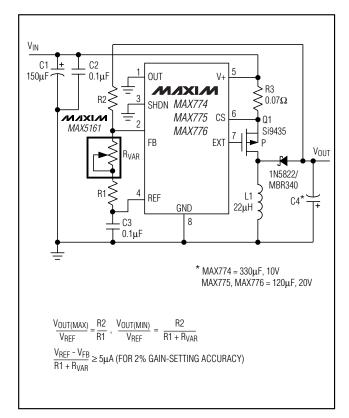


Figure 2. Adjustable Negative LCD Bias



Alternative Positive LCD Bias Control

Alternatively, use an op amp to provide buffering and gain to the output of the MAX5160/MAX5161. Connect the MAX5160 to the positive input of a noninverting op amp (Figure 3) to select a portion of the input signal by digitally controlling the wiper terminal. Figure 4 shows a similar circuit for the MAX5161.

Adjustable Gain

Figure 5 shows how to use the MAX5161 to digitally adjust the gain of a noninverting op amp configuration. Connect the MAX5161 in series with a resistor to ground to form the adjustable gain control of a noninverting amplifier. The MAX5160/MAX5161 have a low 5ppm/°C ratiometric tempco that allows for a very stable adjustable gain configuration over temperature.

Serial Interface

Figure 6 is the serial-interface timing diagram.

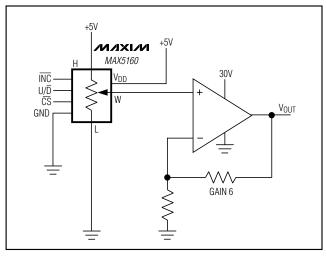


Figure 3. MAX5160 Positive LCD Bias Control

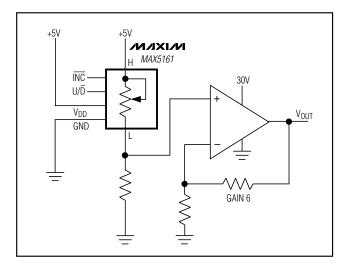


Figure 4. MAX5161 Positive LCD Bias Control

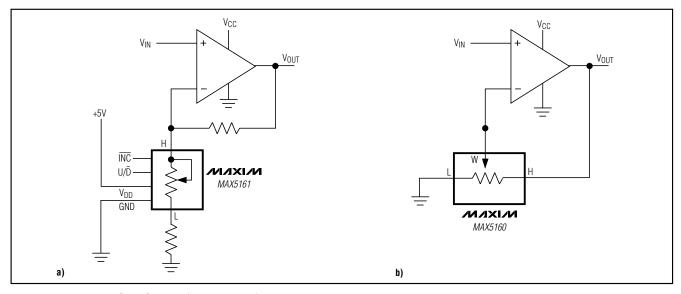


Figure 5. Adjustable Gain Circuit: a) MAX5161; b) MAX5160

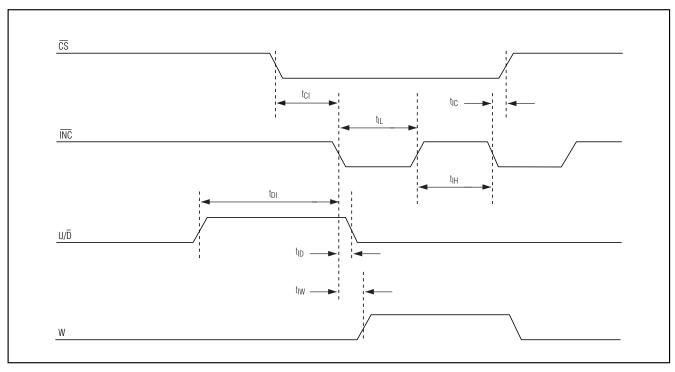


Figure 6. Serial-Interface Timing Diagram

Truth Table

<u>cs</u>	U/D	ĪNC	Rw
Н	xx-X	Х	0
L	L	1	0
L	Н	1	0
L	L	\	_
L	Н	\	+

X = Don't care

O = Previous state

+ = Increment

— = Decrement

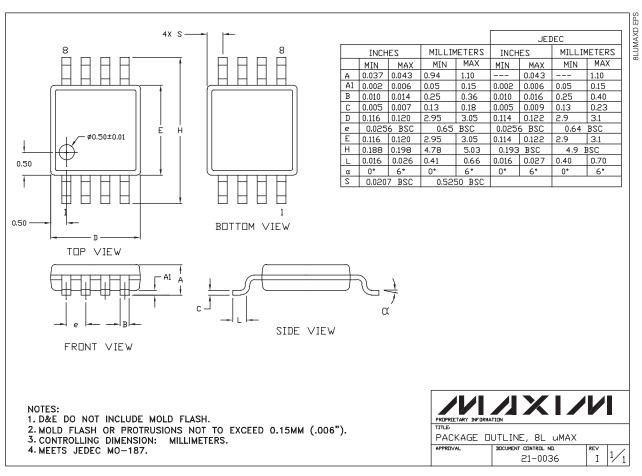
 \downarrow = High-to-Low Transition

 \uparrow = Low-to-High Transition

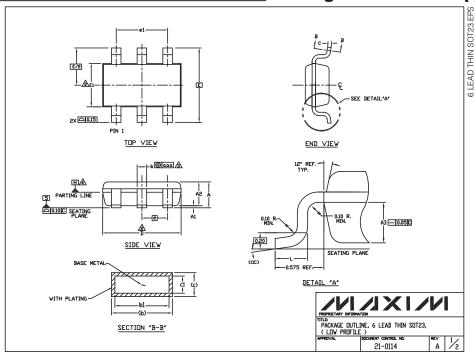
_Chip Information

TRANSISTOR COUNT: 969

Package Information



Package Information (continued)



NOTES:

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2 "D" AND "E1" ARE REFERENCE DATUM AND DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, AND ARE MEASURED AT THE BOTTOM PARTING LINE. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED 0.15mm ON "D" AND 0.25mm ON "E" PER SIDE.
- THE LEAD WIDTH DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.07mm TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION.
- AT THE BOTTOM OF PARTING LINE.
- \(\frac{\(\frac{\text{\Lambda}}{\text{\Lambda}}\) The lead tips must line within a specified tolerance zone. This tolerance zone is defined by two parallel lines. One plane is the seating plane, datum [-c-]; and the other plane is at the specified distance from [-c-] in the direction indicated. Formed leads shall be planar with respect to one another with 0.10mm at seating plane.
- 6. THIS PART IS COMPLIANT WITH JEDEC SPECIFICATION MO-193 EXCEPT FOR THE "e" DIMENSION WHICH IS 0.95mm INSTEAD OF 1.00mm. THIS PART IS IN FULL COMPLIANCE TO EIAJ SPECIFICATION SC-74.

SYMBOLS						
	MIN NOM MAX					
Α	-	-	1.10			
A1	0.05	0.075	0.10			
A2	0.85	0.88	0.90			
A3	0.50 BSC					
b	0.30	-	0.45			
b1	0.25	0.35	0.40			
С	0.15	-	0.20			
c 1	0.12	0.127	0.15			
D	2.80	2.90	3.00			
Ε	2.75 BSC					
E1	1.55	1.60	1.65			
L	0.30	0.40	0.50			
e1	1.90 BSC					
е	0.95 BSC					
œ	0-	4-	8-			
۵۵۵	0.20					



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Maxim Integrated Products, 120 San Gabriel Drive, Sunnyvale, CA 94086 408-737-7600

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