

Low-Power Digital Potentiometers

ABSOLUTE MAXIMUM RATINGS

V _{DD} to GND	-0.3V to +6V
CS, INC, U/D to GND	-0.3V to +6V
H, L, W to GND	-0.3V to (V _{DD} + 0.3V)
Input and Output Latchup Immunity	±200mA
Maximum Continuous Current into H, L, and W MAX516__E__	±1mA

Continuous Power Dissipation (T _A = +70°C)	
6-Pin SOT23 (derate 6.25mW/°C above +70°C)	500mW
8-Pin µMAX (derate 4.1mW/°C above +70°C)	330mW
Operating Temperature Range	-40°C to +85°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{DD} = +2.7V to +5.5V, V_H = V_{DD}, V_L = 0, T_A = T_{MIN} to T_{MAX}. Typical values are at V_{DD} = +5V, T_A = +25°C, unless otherwise noted.)

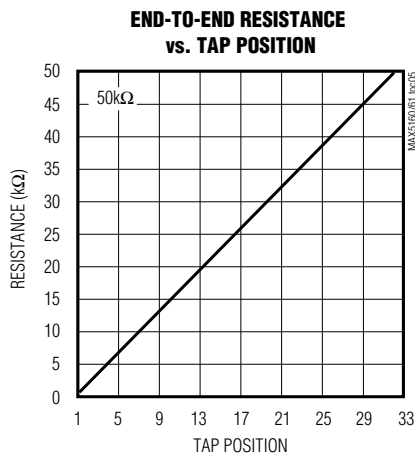
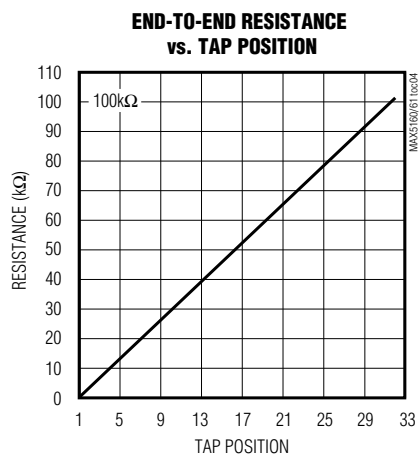
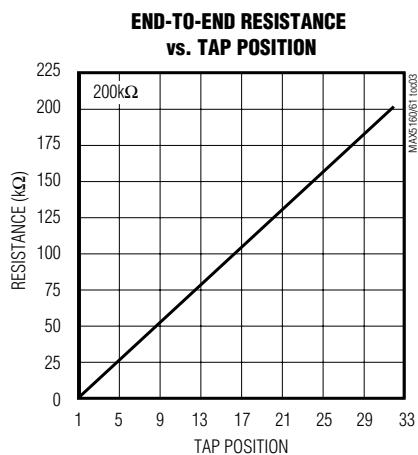
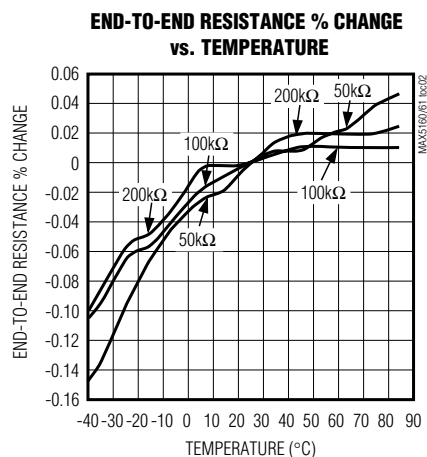
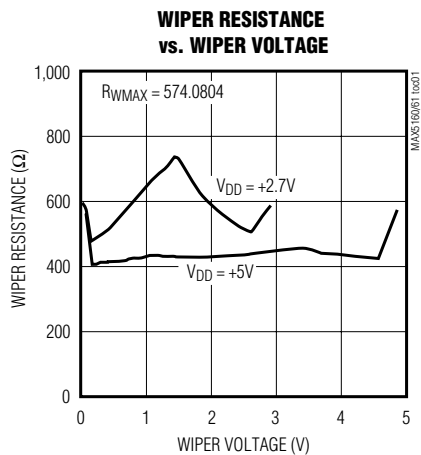
PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
DC PERFORMANCE							
Resolution				3			%
Integral Nonlinearity (Note 1)	INL			±1/2			LSB
Differential Nonlinearity (Note 1)	DNL			±1			LSB
End-to-End Resistor Tempco	TCR			50			ppm/°C
Ratiometric Resistor Tempco				5			ppm/°C
Full-Scale Error				-0.1			LSB
Zero-Scale Error				+0.1			LSB
Wiper Resistance	R _W			400	1700		Ω
Wiper Capacitance	C _W			10			pF
End-to-End Resistance	HL	MAX516_NE__		150	200	250	kΩ
		MAX516_ME__		75	100	125	
		MAX516_LE__		37.5	50	62.5	
DIGITAL INPUTS							
Input High Voltage	V _{IH}			0.7 × V _{DD}			V
Input Low Voltage	V _{IL}			0.3 × V _{DD}			V
Input Leakage Current				±1			μA
Input Capacitance				5			pF
TIMING CHARACTERISTICS (Figure 6)							
CS to INC Setup Time	t _{CI}			25			ns
CS to INC Hold Time	t _{IC}			0			ns
INC Low Period	t _{IL}			25			ns
INC High Period	t _{IH}			25			ns
U/D to INC Hold	t _{ID}			0			ns
U/D to INC Setup	t _{DI}			50			ns
Wiper-Settling Time	t _{IW}			1			μs
INC Frequency	f _{IMAX}			7			MHz
POWER SUPPLIES							
Supply Voltage	V _{DD}			2.7		5.5	V
Supply Current	I _{DD}	CS = INC = U/D = V _{DD} or GND	V _{DD} = +5V	0.6		10	μA
			V _{DD} = +2.7V	135		nA	

Note 1: For the MAX5160, linearity is defined in terms of H to L code-dependent resistance.

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Typical Operating Characteristics

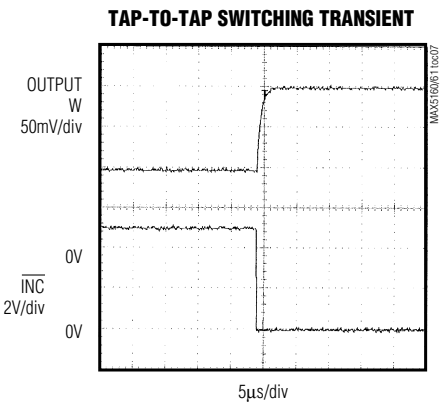
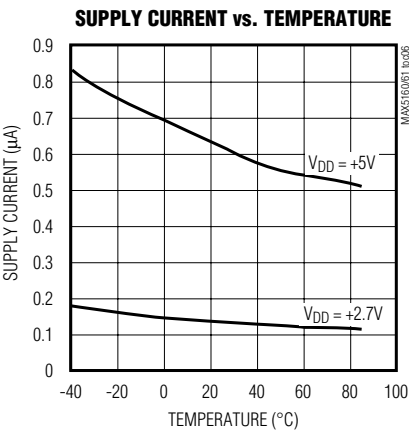
($V_{DD} = +5V$, $T_A = +25^\circ C$, unless otherwise noted.)



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Typical Operating Characteristics (continued)

(V_{DD} = +5V, T_A = +25°C, unless otherwise noted.)



Pin Description

PIN		NAME	FUNCTION
MAX5160	MAX5161		
1	5	$\overline{\text{INC}}$	Wiper Increment Control Input. With $\overline{\text{CS}}$ low, a high-to-low transition increments ($\text{U}/\overline{\text{D}}$ high) or decrements ($\text{U}/\overline{\text{D}}$ low) the wiper position.
2	4	$\text{U}/\overline{\text{D}}$	Up/Down Control Input. With $\overline{\text{CS}}$ low, a high-to-low $\overline{\text{INC}}$ transition increments ($\text{U}/\overline{\text{D}}$ high) or decrements ($\text{U}/\overline{\text{D}}$ low) the wiper position.
3	2	H	High Terminal of Resistor
4	3	GND	Ground
5	—	W	Wiper Terminal of Resistor
6	1	L	Low Terminal of Resistor
7	—	$\overline{\text{CS}}$	Chip-Select Input. Drive low to change the wiper position through $\overline{\text{INC}}$ and $\text{U}/\overline{\text{D}}$.
8	6	V _{DD}	Power Supply

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Alternative Positive LCD Bias Control

Alternatively, use an op amp to provide buffering and gain to the output of the MAX5160/MAX5161. Connect the MAX5160 to the positive input of a noninverting op amp (Figure 3) to select a portion of the input signal by digitally controlling the wiper terminal. Figure 4 shows a similar circuit for the MAX5161.

Adjustable Gain

Figure 5 shows how to use the MAX5161 to digitally adjust the gain of a noninverting op amp configuration. Connect the MAX5161 in series with a resistor to ground to form the adjustable gain control of a noninverting amplifier. The MAX5160/MAX5161 have a low 5ppm/°C ratiometric tempco that allows for a very stable adjustable gain configuration over temperature.

Serial Interface

Figure 6 is the serial-interface timing diagram.

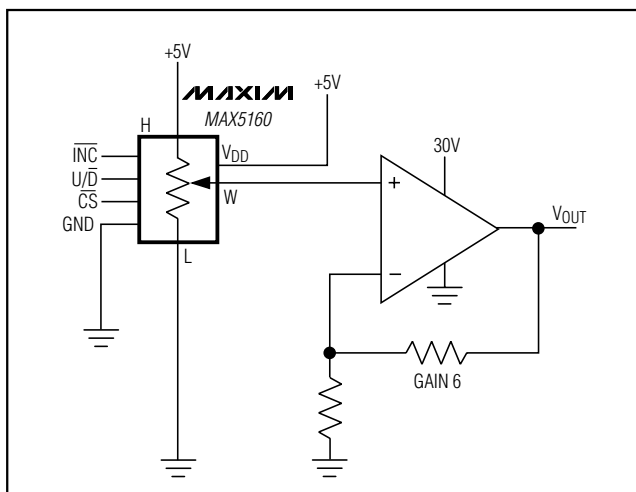


Figure 3. MAX5160 Positive LCD Bias Control

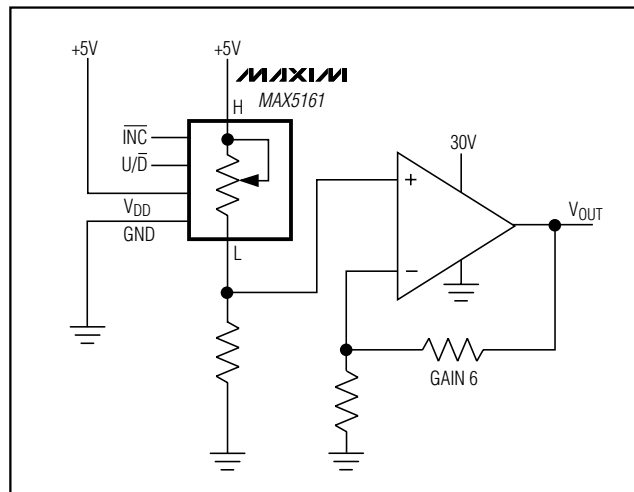


Figure 4. MAX5161 Positive LCD Bias Control

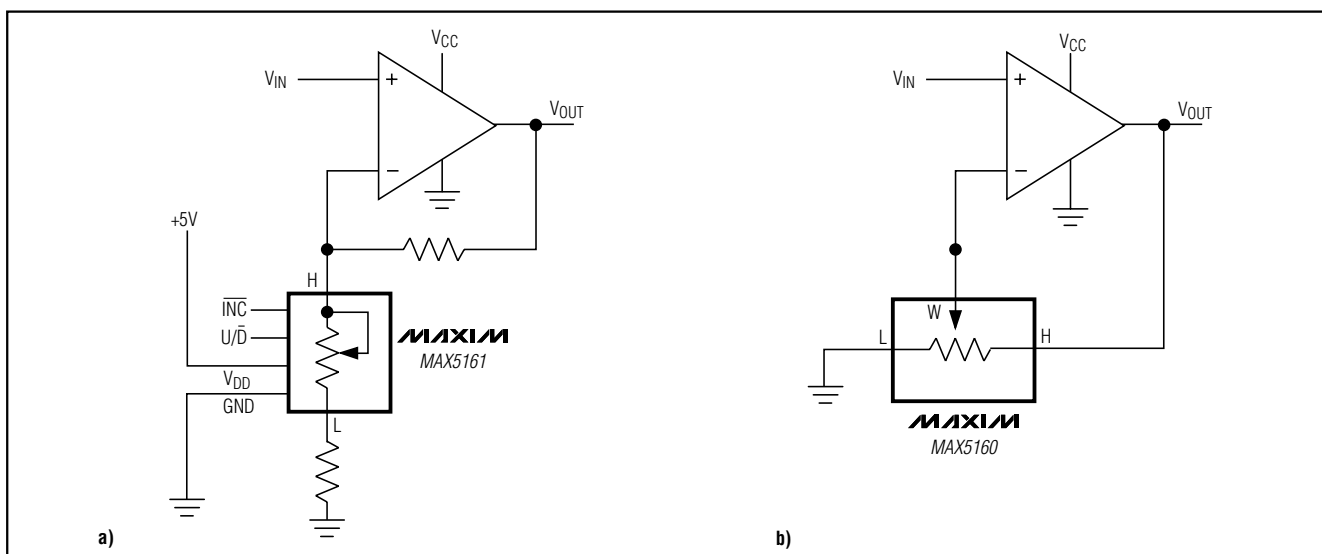


Figure 5. Adjustable Gain Circuit: a) MAX5161; b) MAX5160

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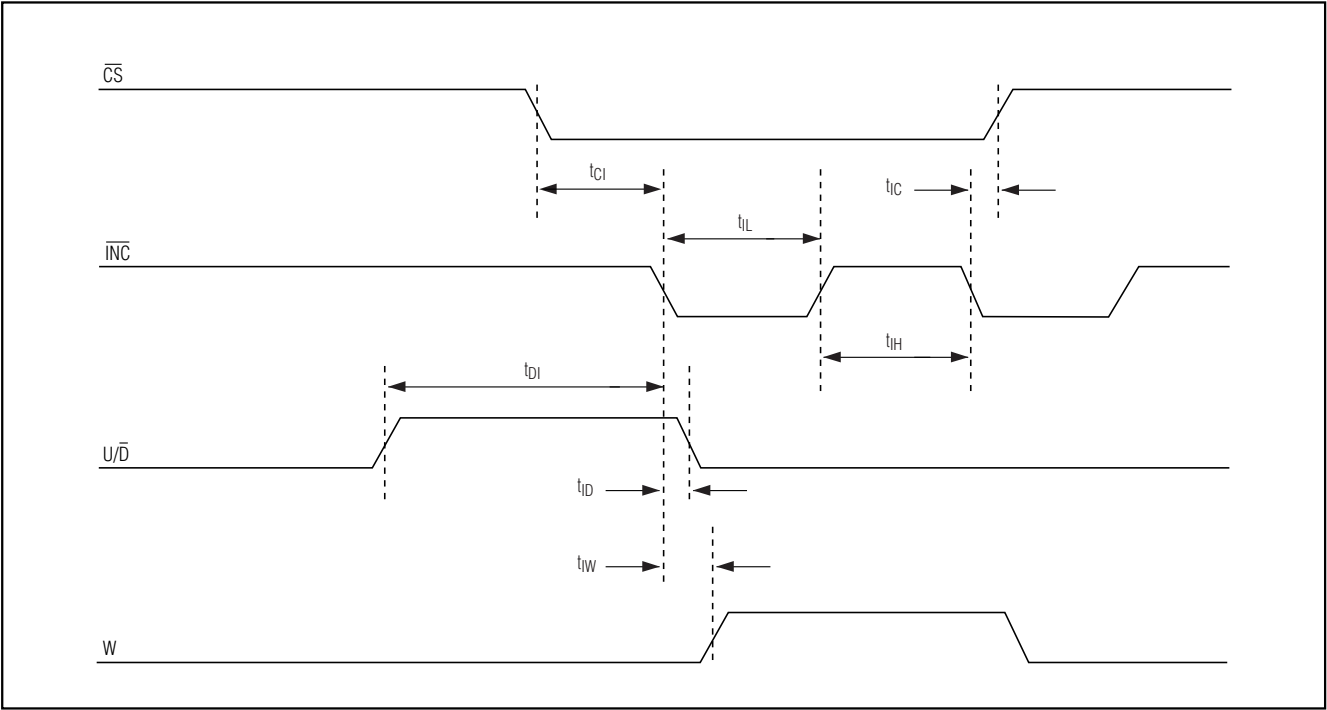


Figure 6. Serial-Interface Timing Diagram

Truth Table

\overline{CS}	U/\overline{D}	\overline{INC}	R_W
H	xx-X	X	O
L	L	\uparrow	O
L	H	\uparrow	O
L	L	\downarrow	—
L	H	\downarrow	+

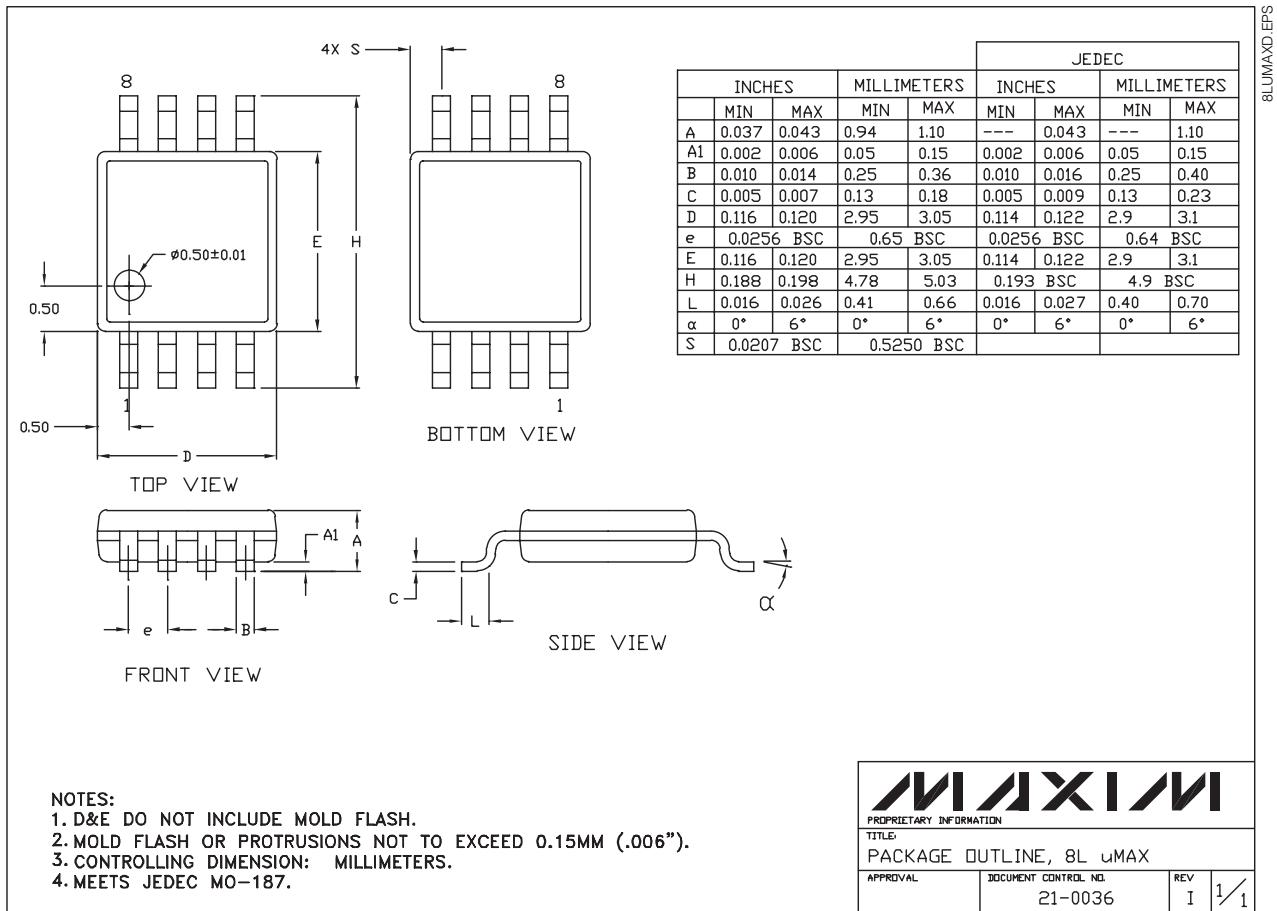
X = Don't care
O = Previous state
+ = Increment
— = Decrement
 \downarrow = High-to-Low Transition
 \uparrow = Low-to-High Transition

Chip Information

TRANSISTOR COUNT: 969

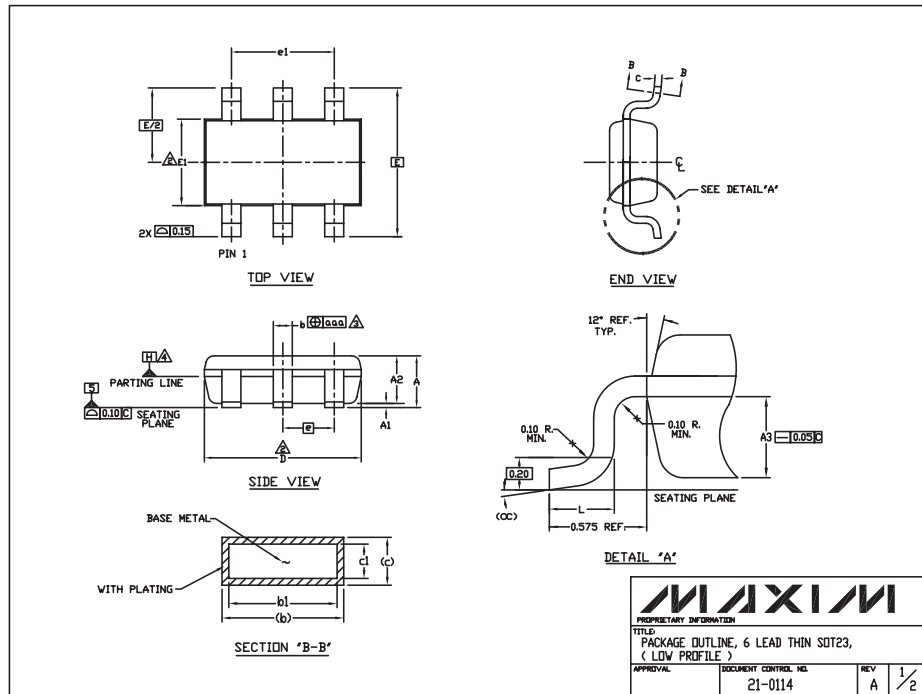
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Package Information



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Package Information (continued)



NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS.

2. "D" AND "E1" ARE REFERENCE DATUM AND DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, AND ARE MEASURED AT THE BOTTOM PARTING LINE. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED 0.15mm ON "D" AND 0.25mm ON "E" PER SIDE.

3. THE LEAD WIDTH DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.07mm TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION.

4. DATUM PLANE "H" LOCATED AT MOLD PARTING LINE AND COINCIDENT WITH LEAD, WHERE LEAD EXITS PLASTIC BODY AT THE BOTTOM OF PARTING LINE.

5. THE LEAD TIPS MUST LINE WITHIN A SPECIFIED TOLERANCE ZONE. THIS TOLERANCE ZONE IS DEFINED BY TWO PARALLEL LINES. ONE PLANE IS THE SEATING PLANE, DATUM [-C-]; AND THE OTHER PLANE IS AT THE SPECIFIED DISTANCE FROM [-C-] IN THE DIRECTION INDICATED. FORMED LEADS SHALL BE PLANAR WITH RESPECT TO ONE ANOTHER WITH 0.10mm AT SEATING PLANE.

6. THIS PART IS COMPLIANT WITH JEDEC SPECIFICATION MO-193 EXCEPT FOR THE "e" DIMENSION WHICH IS 0.95mm INSTEAD OF 1.00mm. THIS PART IS IN FULL COMPLIANCE TO EIAJ SPECIFICATION SC-74.

SYMBOLS			
	MIN	NOM	MAX
A	-	-	1.10
A1	0.05	0.075	0.10
A2	0.85	0.88	0.90
A3	0.50 BSC		
b	0.30	-	0.45
b1	0.25	0.35	0.40
c	0.15	-	0.20
c1	0.12	0.127	0.15
D	2.80	2.90	3.00
E	2.75 BSC		
E1	1.55	1.60	1.65
L	0.30	0.40	0.50
e1	1.90 BSC		
e	0.95 BSC		
OC	0°	4°	8°
o.a.a.	0.20		

MAXIM			
PROPRIETARY INFORMATION			
TITLE: PACKAGE OUTLINE, 6 LEAD THIN SOT23, (LOW PROFILE)			
APPROVAL:	DOCUMENT CONTROL NO.	REV	
	21-0114	A	2/2

Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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MAX5160/MAX5161

6 LEAD THIN SOT23.EPS