ABSOLUTE MAXIMUM RATINGS

IN, OUT, FB, POK, SHDN to GND0.3V to +6V
PGND to GND±0.3V
CXN to GND0.3V to (V _{IN} + 0.3V)
CXP to GND0.3V to (the greater of V _{IN} or V _{OUT}) + 1V
OUT Short to GNDIndefinite
Continuous Power Dissipation ($T_A = +70^{\circ}C$)
10-Pin µMAX (derate 5.6mW/°C above +70°C)444mW

Operating Temperature Range	40°C to +85°C
Junction Temperature	+150°C
Storage Temperature Range	
Lead Temperature (soldering, 10s)	+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(Circuit of Figure 1, $V_{IN} = V_{\overline{SHDN}} = 2V$, FB = PGND = GND, $C_{IN} = 10\mu F$, $C_X = 0.33\mu F$, $C_{OUT} = 10\mu F$, $T_A = 0^{\circ}C$ to +85°C, unless otherwise noted. Typical values are at $T_A = +25^{\circ}C$.)

PARAMETER	METER SYMBOL CONDITIONS				MAX	UNITS
Input Voltage Range	VIN		1.6		5.5	V
Input Undervoltage Lockout Voltage	V _U VLO		0.6	1.0	1.4	V
Output Voltage Adjustment Range		1.6V ≤ V _{IN} ≤ 5.5V	2.5 5.5			
Output Voltage	Va=	$2V \le V_{IN} \le 5.5V$, $1mA \le I_{LOAD} \le 50mA$	3.17	3.3	3.43	V
Output Voltage	V _{OUT}	$2.5V \le V_{IN} \le 5.5V$, $1mA \le I_{LOAD} \le 100mA$	3.17	3.3	3.43	
Maximum Output Current	I _{LOAD,MAX}	$2.5V \le V_{\text{IN}} \le 5.5V$	100			mA
Transient Load Current		I _{LOAD} ≤ 100mA (RMS)		200		mA
Quiescent Supply Current	lo	V _{IN} = V SHDN = 4V, V _{FB} = 0, stepping down		50	90	μA
Quiescent Supply Current	lQ	V _{IN} = V SHDN = 2V, V _{FB} = 0, stepping up		85	180	μΑ
Shutdown Supply Current	IQ, SHDN	1.6V ≤ V _{IN} ≤ 5.5V, V SHDN = 0		1	5	μΑ
Leakage Current into OUT in Shutdown $V_{IN} = 2V, V_{OUT} = 3.3V, V_{\overline{SHDN}} = 0$			1	5	μΑ	
CUDN Logic Input Voltage	VIL	1.6V ≤ V _{IN} ≤ 5.5V	0.25 • V _{IN}		V	
SHDN Logic Input Voltage	V _{IH}	1.6V ≤ V _{IN} ≤ 5.5V	0.7 • V _{IN}			V
SHDN Input Leakage Current	ISHDN	V <u>SHDN</u> = 5.5V	-1		1	μΑ
FB Regulation Voltage	V _{FB}	V _{IN} = 1.65V, V _{OUT} = 3.3V	1.205	1.235	1.265	V
FB Input Current		V _{FB} = 1.27V		25	200	nA
FB Dual-Mode Threshold		Internal feedback		100	50	mV
rb Duai-Mode Threshold		External feedback	200 100			mV
POK Trip Voltage		Falling edge at FB	1.0	1.1	1.2	V
POK Output Low Voltage	V _{OL}	$I_{SINK} = 0.5 \text{mA}, V_{IN} = 2V$		5	100	mV
POK Leakage Current		$V_{POK} = 5.5V, V_{FB} = 1.27V$		0.01	0.2	μΑ
Switching Frequency	fosc	$1.6V \le V_{IN} \le 5.5V$, $V_{FB} = 1V$	1.2	1.5	1.8	MHz
Output Short-Circuit Current		$V_{OUT} = 0$, $2.5V \le V_{IN} \le 5.5V$, foldback current limit		110		mA
Thermal Shutdown Temperature		Rising temperature		160		°C
Thermal Shutdown Hysteresis				20		°C
Efficiency		$V_{IN} = 3.6V$, $I_{LOAD} = 10$ mA		90		%

ELECTRICAL CHARACTERISTICS

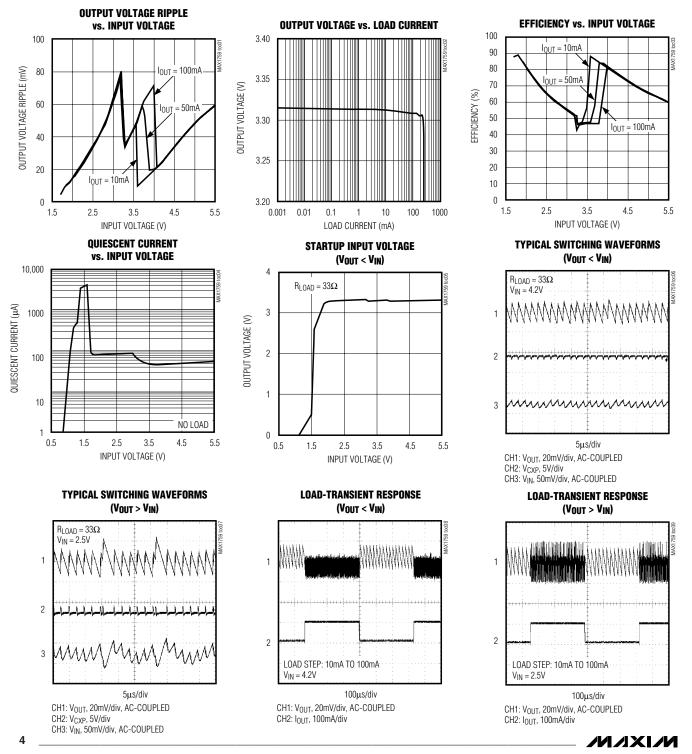
(Circuit of Figure 1, $V_{IN} = V_{\overline{SHDN}} = 2V$, FB = PGND = GND, $C_{IN} = 10\mu\text{F}$, $C_X = 0.33\mu\text{F}$, $C_{OUT} = 10\mu\text{F}$, $\textbf{T_A} = \textbf{-40}^{\circ}\textbf{C}$ to $\textbf{+85}^{\circ}\textbf{C}$, unless otherwise noted.) (Note 1)

PARAMETER SYMB		PARAMETER SYMBOL CONDITIONS		MAX	UNITS	
Input Voltage Range	V _{IN}		1.6	5.5	V	
Input Undervoltage Lockout Voltage	V _U VLO	0.6		1.4	V	
Output Valtage	\/a.u.=	$2V \le V_{IN} \le 5.5V$, $0 \le I_{LOAD} \le 50mA$	3.15	3.45	V	
Output Voltage	Vout	$2.5V \le V_{IN} \le 5.5V$, $0 \le I_{LOAD} \le 100mA$	3.15	3.45	V	
Output Voltage Adjustment Range		1.6V ≤ V _{IN} ≤ 5.5V	2.5	5.5	V	
Maximum Output Current	ILOAD,MAX	$2.5V \le V_{IN} \le 5.5V$	100		mA	
Quiescent Supply Current	lo	V _{IN} = V SHDN = 4V, V _{FB} = 0		90	μA	
Quiescent Supply Current	ΙQ	$V_{IN} = V \overline{SHDN} = 2.5V, V_{FB} = 0$		180	μΑ	
Shutdown Supply Current	IQ, SHDN	$1.6V \le V_{IN} \le 5.5V$, $V \overline{SHDN} = 0$		6	μΑ	
Leakage Current into OUT in Shutdown		$V_{IN} = 2V$, $V_{OUT} = 3.3V$, $V_{\overline{SHDN}} = 0$		5	μΑ	
CLIDNI Input Logic Voltage	V _I L	1.6V ≤ V _{IN} ≤ 5.5V		0.2 • V _{IN}	V	
SHDN Input Logic Voltage	VIH	$1.6V \le V_{\text{IN}} \le 5.5V$	0.7 • V _{IN}		V	
SHDN Input Leakage Current	ISHDN	V <u>SHDN</u> = 5.5V	-1	1	μΑ	
FB Regulation Voltage	V _{FB}	V _{IN} = 1.65V, V _{OUT} = 3.3V	1.205	1.265	V	
FB Input Bias Current		V _{FB} = 1.27V		200	nA	
FB Dual Mode Threshold		Internal feedback		40	mV	
T B Buai Mode Tilleshold		External feedback	200		mV	
POK Trip Voltage		Falling edge at FB	1.0	1.2	V	
POK Output Low Voltage	V _{OL}	$I_{SINK} = 0.5 \text{mA}, V_{IN} = 2V$		100	mV	
POK Leakage Current		V _{POK} = 5.5V		0.2	μΑ	
Switching Frequency	fosc	$1.6V \le V_{IN} \le 5.5V$, $V_{FB} = 1V$	1.1	1.9	MHz	

Note 1: Specifications to -40°C are guaranteed by design and are not production tested.

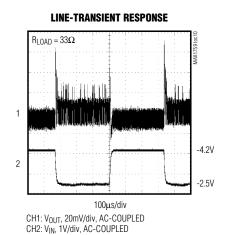
Typical Operating Characteristics

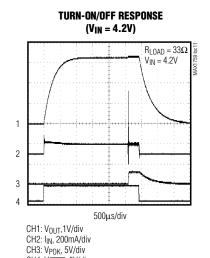
(Circuit of Figure 1, C_{IN} = 10 μ F, C_X = 0.33 μ F, C_{OUT} = 10 μ F, V_{OUT} = 3.3V, V_{IN} = 2.5V, T_A = +25°C, unless otherwise noted.)



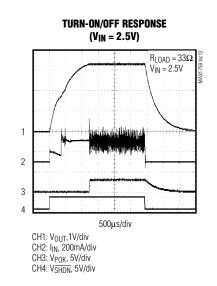
Typical Operating Characteristics (continued)

(Circuit of Figure 1, C_{IN} = 10 μ F, C_X = 0.33 μ F, C_{OUT} = 10 μ F, V_{OUT} = 3.3V, V_{IN} = 2.5V, T_A = +25°C, unless otherwise noted.)





CH4: V_{SHDN}, 5V/div



Pin Description

PIN	NAME	FUNCTION
1	POK	Open-Drain Power-OK Output. POK is high impedance when output voltage is in regulation. POK sinks current when V_{FB} falls below 1.1V. Connect a $10k\Omega$ to $1M\Omega$ pull-up resistor from POK to V_{OUT} for a logic signal. Ground POK or leave unconnected if not used. POK is high impedance in shutdown.
2	SHDN	Shutdown Input. Drive high for normal operation; drive low for shutdown mode. OUT is high impedance in shutdown.
3, 4	IN	Input Supply. Connect both pins together and bypass to GND with a ceramic capacitor (see Capacitor Selection section).
5	GND	Ground. Connect GND to PGND with a short trace.
6	PGND	Power Ground. Charge-pump current flows through this pin.
7	CXN	Negative Terminal of the Charge-Pump Transfer Capacitor
8	CXP	Positive Terminal of the Charge-Pump Transfer Capacitor
9	OUT	Power Output. Bypass to GND with an output filter capacitor.
10	FB	Dual-Mode Feedback. Connect FB to GND for 3.3V output. Connect to an external resistor divider to adjust the output voltage from 2.5V to 5.5V.

Detailed Description

The MAX1759's unique charge-pump architecture allows the input voltage to be higher or lower than the regulated output voltage. Internal circuitry senses V_{IN} and V_{OUT} and determines whether V_{IN} must be stepped up or stepped down to produce the regulated output. When V_{IN} is lower than V_{OUT} , the charge pump operates as a regulated step-up voltage doubler. When V_{IN} is higher than V_{OUT} , the charge pump operates as a step-down gated switch.

In voltage step-down mode (i.e., the input voltage is greater than the output voltage) with a light load, the controller connects CXN to PGND, and shuttles charge to the output by alternately connecting CXP from IN to OUT (see Figures 1 and 2). Although V_{IN} is greater than V_{OUT}, this scheme may not allow the MAX1759 to regulate the output under heavy loads. In this case, the MAX1759 will automatically switch to step-up mode. In step-up mode, the output is kept in regulation by modulating the charge delivered by the transfer capacitor (C_X) to the load (see Figure 2). When lightly loaded, the charge pump switches only as necessary to supply the load, resulting in low quiescent current. Output voltage ripple does not increase with light loads.

Shutdown Mode

Driving $\overline{\text{SHDN}}$ low places the MAX1759 in shutdown mode. This disables the charge-pump switches, oscillator, and control logic, reducing quiescent current to 1µA. The output is high impedance in shutdown and is disconnected from the input. The POK output is high impedance in shutdown.

Undervoltage Lockout

The MAX1759 undervoltage lockout feature deactivates the device when the input voltage falls below 1V.

Power-OK Output

POK is an open-drain output that sinks current when the regulator feedback voltage falls below 1.1V. The feedback voltage can be either the internal resistor-divider feedback voltage when in fixed output mode (FB tied to GND) or an external feedback voltage from an external resistive divider in adjustable output mode. A $10\text{k}\Omega$ to $1\text{M}\Omega$ pull-up resistor from POK to OUT may be used to provide a logic output. Connect POK to GND or leave unconnected if not used.

Soft-Start and Short-Circuit Protection

The MAX1759 features foldback short-circuit protection. This circuitry provides soft-start by limiting inrush current during startup and limits the output current to 110mA (typ) if the output is short-circuited to ground.

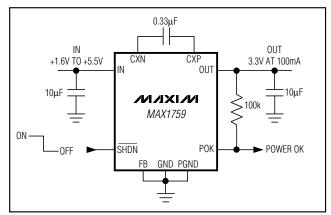


Figure 1. Typical Application Circuit

Thermal Shutdown

The MAX1759 features thermal shutdown with temperature hysteresis. When the die temperature exceeds 160°C, the device shuts down. When the die cools by 20°C, the MAX1759 turns on again. If high die temperature is caused by output overload and the load is not removed, the device will turn off and on, resulting in a pulsed output.

Design Procedure

Setting the Output Voltage

The MAX1759 dual-mode feedback controller selects between the internally set 3.3V regulated output or an external resistive divider that allows adjustment of the output voltage from 2.5V to 5.5V. Connect FB to GND for a regulated 3.3V output. For an adjustable output, connect a resistive divider between OUT and GND. To ensure feedback-loop stability and to minimize error due to FB pin bias currents, the resistive divider current should be approximately $15\mu A$. In the following equation, choose R2 in the $50k\Omega$ to $100k\Omega$ range, and calculate R1 from the following formula (Figure 3):

$$R1 = R2 [(V_{OUT} / V_{FB}) - 1]$$

and

$$V_{OUT} = V_{FB} (R1 + R2) / R2$$

where V_{OUT} is the desired output voltage from 2.5V to 5.5V, and V_{FB} is the internal regulation voltage, nominally 1.235V.

The circuit of Figure 3 generates a regulated 2.5V, using external standard 1% resistor values. Surface-mount resistors should be placed close to the MAX1759, less than 5mm away from FB (see the *PC Board Layout* section).

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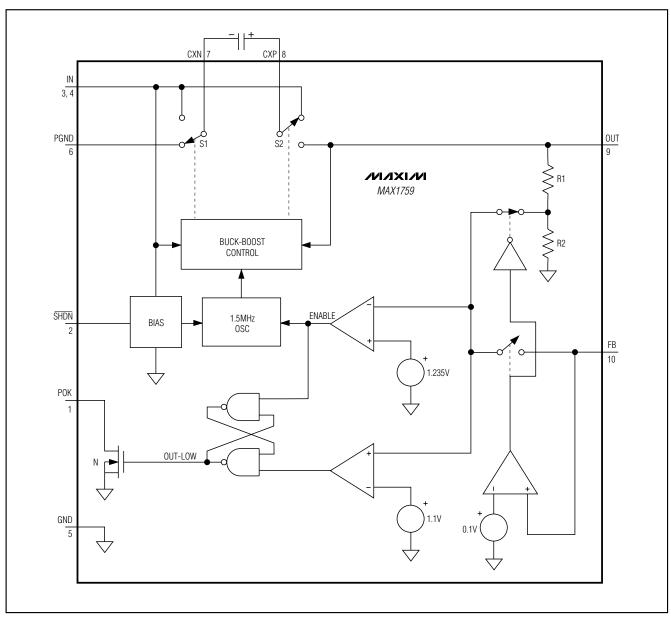


Figure 2. Functional Diagram

Capacitor Selection

Optimize the charge-pump circuit for physical size, output current, and output ripple by selecting capacitors C_{IN} , C_{X} , and C_{OUT} . See Table 1 for suggested capacitor values.

Note that capacitors must have low ESR ($\leq 20 m\Omega$) to maintain low output ripple. Ceramic capacitors are recommended. In cost-sensitive applications where high output current is needed, the output capacitor may be a combination of a 1µF ceramic in parallel with a 10µF tantalum capacitor. The ceramic capacitor's low ESR will help keep output ripple within acceptable levels.

Output Voltage Ripple

The MAX1759 proprietary control scheme automatically chooses between voltage doubling and voltage stepdown to maintain output voltage regulation over various load currents and V_{IN} to V_{OUT} voltage differentials.

When V_{IN} is lower than V_{OUT} , the charge pump always operates in voltage-doubler mode. It regulates the output voltage by modulating the charge delivered by the transfer capacitor.

When V_{IN} is higher than V_{OUT} , the charge pump operates in voltage step-down mode, but may revert to voltage-doubler mode if necessary to maintain regulation under load. While operating in step-down mode, the output voltage ripple is typically much lower than it is in voltage-doubler mode (see *Typical Operating Characteristics*).

Output Current

The MAX1759 is guaranteed to deliver a regulated 3.3V at 100mA continuous, from a +2.5V input. Peaks up to 200mA are acceptable as long as the current is \leq 100mA (RMS).

Applications Information

PC Board Layout

The MAX1759 is a high-frequency switched-capacitor voltage regulator. For best circuit performance, use a ground plane and keep C_{IN}, C_X, C_{OUT}, and feedback resistors (if used) close to the device. If using external feedback, keep the feedback node as small as possible by positioning the feedback resistors very close to FB. Suggested PC component placement and board layout are shown in Figures 4a and 4b.

_Chip Information

TRANSISTOR COUNT: 1802

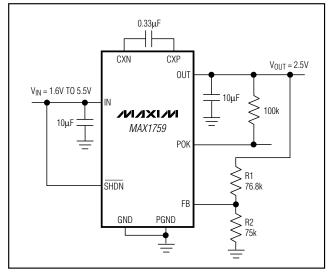


Figure 3. Using External Feedback for Regulated 2.5V Output

Table 1. Capacitor Selection

OUTPUT	CAPACITOR VALUE			OUTPUT (m	
(mA)	C _{IN} (µF)	C _X (μF)	C _{OUT} (µF)	V _{IN} = 2.5V	V _{IN} = 4.2V
100	10	0.33	10	40	20
100	4.7	0.22	4.7	80	60
50	2.2	0.1	2.2	100	80

/U/IXI/U

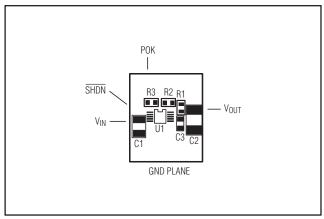


Figure 4a. MAX1759 Component Placement Guide

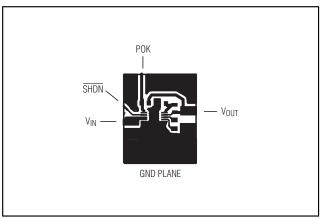
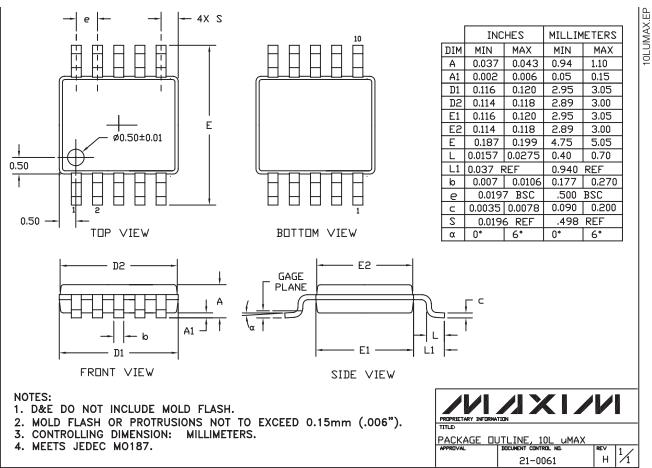


Figure 4b. MAX1759 Recommended PC Board Layout

Package Information



Note: The MAX1759 does not have an exposed pad.

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