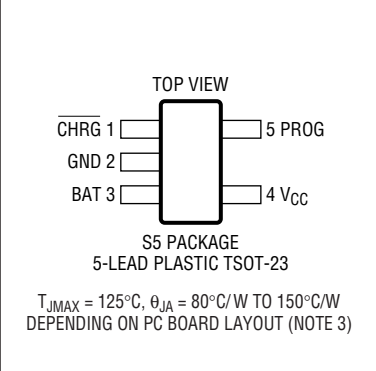


ABSOLUTE MAXIMUM RATINGS

(Note 1)

Input Supply Voltage (V_{CC})	–0.3V to 10V
PROG	–0.3V to $V_{CC} + 0.3V$
CHRG	–0.3V to 10V
BAT	–0.3V to 7V
BAT Short-Circuit Duration	Continuous
BAT Pin Current	200mA
PROG Pin Current	1.5mA
Maximum Junction Temperature	125°C
Operating Temperature Range (Note 2)	–40°C to 85°C
Storage Temperature Range	–65°C to 125°C
Lead Temperature (Soldering, 10 sec)	300°C

PACKAGE/ORDER INFORMATION

	ORDER PART NUMBER
	LTC4054LES5-4.2
	S5 PART MARKING
	LTAFA

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS The ● denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_{CC} = 5V$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
V _{CC}	Supply Voltage		●	4.25		6.5	V
I _{CC}	Supply Current	Charge Mode (Note 4), R _{PROG} = 1k Standby Mode (Charge Terminated) Shutdown Mode (R _{PROG} Not Connected, V _{CC} < V _{BAT} , or V _{CC} < V _{UV})	● ● ●		1200 200 25	2000 500 50	μA μA μA
V _{FLOAT}	Regulated Output (Float) Voltage	0°C ≤ T _A ≤ 85°C, I _{BAT} = 40mA		4.158	4.2	4.242	V
I _{BAT}	BAT Pin Current	R _{PROG} = 15k, Current Mode R _{PROG} = 1k, Current Mode Standby Mode, V _{BAT} = 4.2V Shutdown Mode (R _{PROG} Not Connected) Sleep Mode, V _{CC} = 0V	● ● ●	9.3 142.5 0	10 150 −2.5	10.7 157.5 −6	mA mA μA μA μA
I _{TRIKL}	Trickle Charge Current	V _{BAT} < V _{TRIKL} , R _{PROG} = 1k (I _{BAT} = 150mA)	●	5	15	25	mA
V _{TRIKL}	Trickle Charge Threshold Voltage	R _{PROG} = 15k, V _{BAT} Rising		2.8	2.9	3	V
V _{TRHYS}	Trickle Charge Hysteresis Voltage	R _{PROG} = 15k		60	80	110	mV
V _{UV}	V _{CC} Undervoltage Lockout Threshold Voltage	From V _{CC} Low to High	●	3.7	3.8	3.92	V
V _{UVHYS}	V _{CC} Undervoltage Lockout Hysteresis Voltage		●	150	200	300	mV
V _{MSD}	Manual Shutdown Threshold Voltage	PROG Pin Rising PROG Pin Falling	● ●	1.15 0.9	1.21 1.0	1.30 1.1	V V
V _{ASD}	V _{CC} − V _{BAT} Lockout Threshold Voltage	V _{CC} from Low to High V _{CC} from High to Low		70 5	100 30	140 50	mV mV
I _{TERM}	C/10 Termination Current Threshold	R _{PROG} = 15k (I _{BAT} = 10mA) (Note 5) R _{PROG} = 1k (I _{BAT} = 150mA) (Note 5)	● ●	0.085 0.088	0.10 0.10	0.115 0.112	mA/mA mA/mA
V _{PROG}	PROG Pin Voltage	R _{PROG} = 1k, Current Mode	●	0.93	1	1.07	V
I _{CHRG}	CHRG Pin Weak Pull-Down Current	V _{CHRG} = 5V		8	20	35	μA
V _{CHRG}	CHRG Pin Output Low Voltage	I _{CHRG} = 5mA			0.35	0.6	V
ΔV _{RECHRG}	Recharge Battery Hysteresis Voltage	V _{FLOAT} − V _{RECHRG}		100	150	200	mV

ELECTRICAL CHARACTERISTICS

The ● denotes specifications which apply over the full operating temperature range, otherwise specifications are at $T_A = 25^\circ\text{C}$. $V_{CC} = 5\text{V}$, unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
T_{LIM}	Junction Temperature in Constant Temperature Mode			120		$^\circ\text{C}$
R_{ON}	Power FET "ON" Resistance (Between V_{CC} and BAT)			1.5		Ω
t_{SS}	Soft-Start Time	$I_{BAT} = 0$ to $I_{BAT} = 150\text{V}/R_{PROG}$		100		μs
$t_{RECHARGE}$	Recharge Comparator Filter Time	V_{BAT} High to Low	0.75	2	4.5	ms
t_{TERM}	Termination Comparator Filter Time	I_{BAT} Drops Below $I_{CHG}/10$	400	1000	2500	μs
I_{PROG}	PROG Pin Pull-Up Current		1.5	3	5	μA

Note 1: Absolute Maximum Ratings are those values beyond which the life of the device may be impaired.

Note 2: The LTC4054LE-4.2 is guaranteed to meet performance specifications from 0°C to 70°C . Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

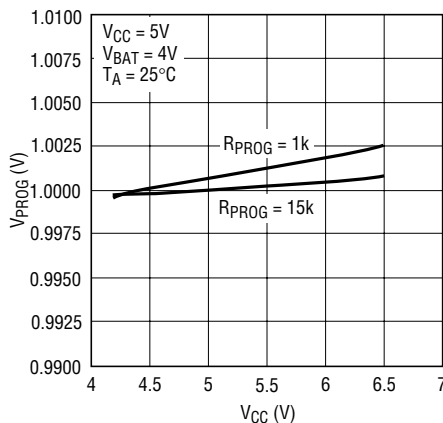
Note 3: See Thermal Considerations.

Note 4: Supply current includes PROG pin current ($\approx 1\text{mA}$) but does not include any current delivered to the battery through the BAT pin.

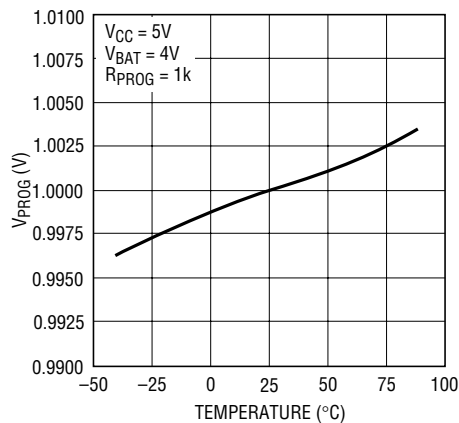
Note 5: I_{TERM} is expressed as a fraction of measured full charge current with indicated PROG resistor.

TYPICAL PERFORMANCE CHARACTERISTICS

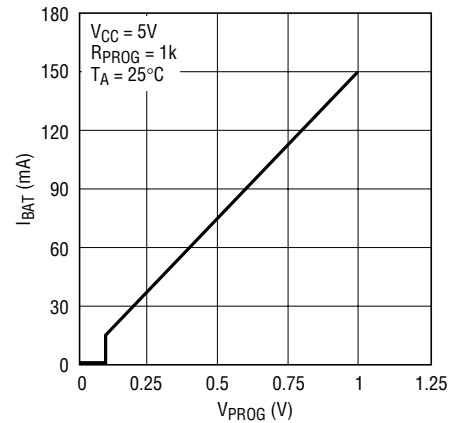
PROG Pin Voltage vs Supply Voltage (Constant Current Mode)



PROG Pin Voltage vs Temperature (Constant Current Mode)

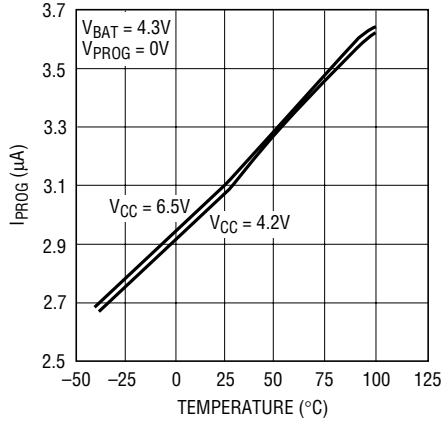


Charge Current vs PROG Pin Voltage

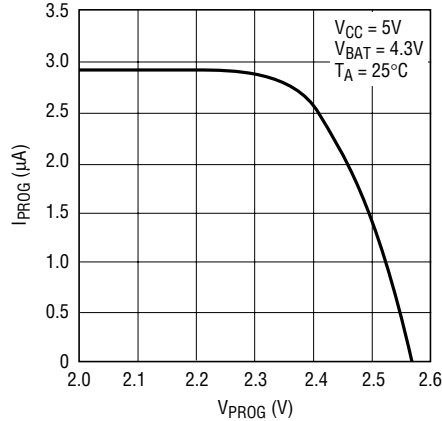


TYPICAL PERFORMANCE CHARACTERISTICS

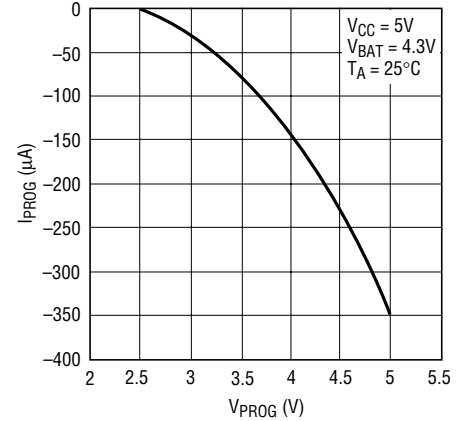
PROG Pin Pull-Up Current vs Temperature and Supply Voltage



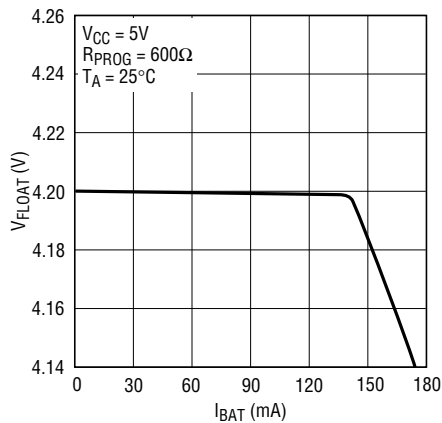
PROG Pin Current vs PROG Pin Voltage (Pull-Up Current)



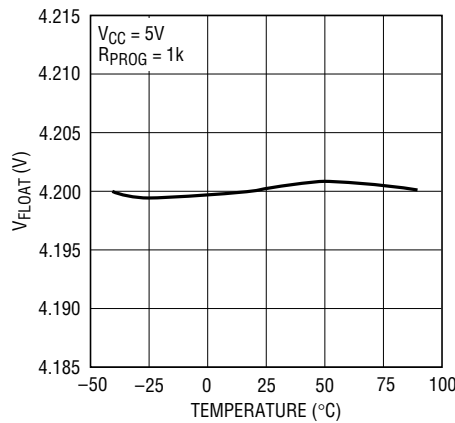
PROG Pin Current vs PROG Pin Voltage (Clamp Current)



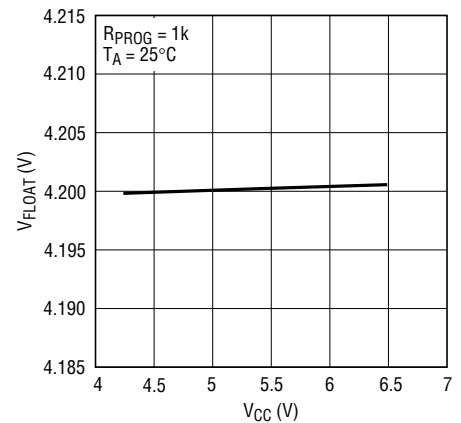
Regulated Output (Float) Voltage vs Charge Current



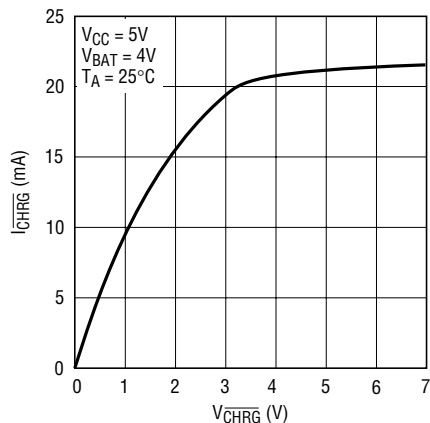
Regulated Output (Float) Voltage vs Temperature



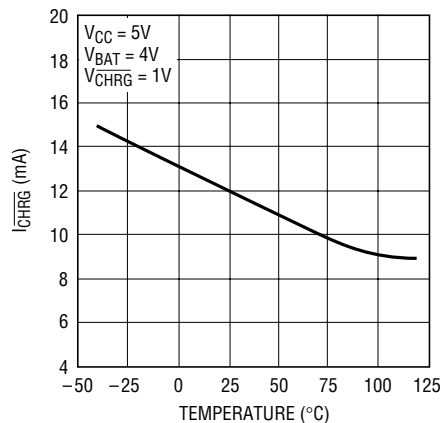
Regulated Output (Float) Voltage vs Supply Voltage



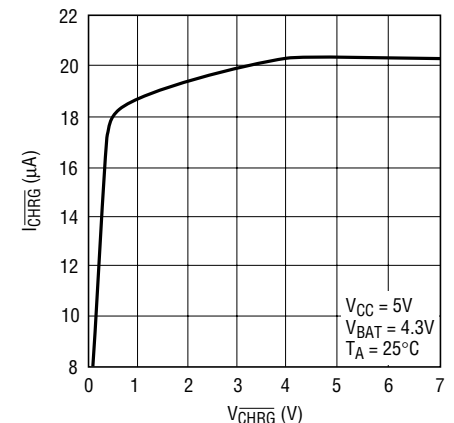
CHRG Pin I-V Curve (Strong Pull-Down State)



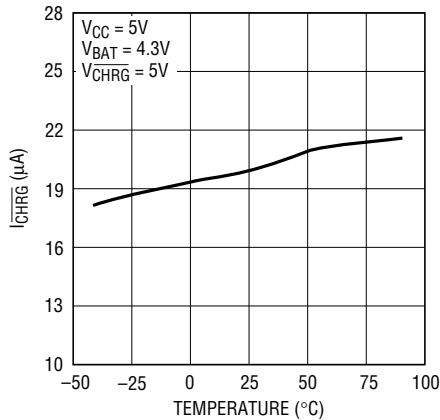
CHRG Pin Current vs Temperature (Strong Pull-Down State)



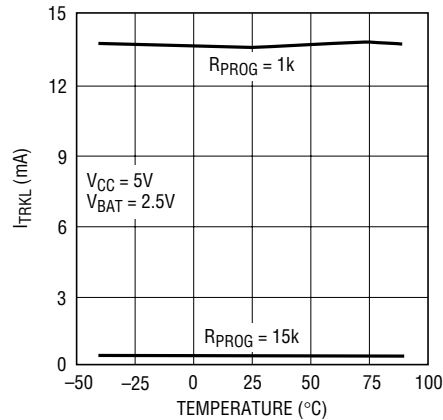
CHRG Pin I-V Curve (Weak Pull-Down State)



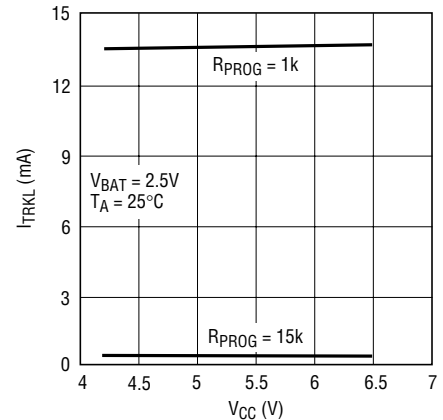
TYPICAL PERFORMANCE CHARACTERISTICS

CHRG Pin Current vs Temperature
(Weak Pull-Down State)

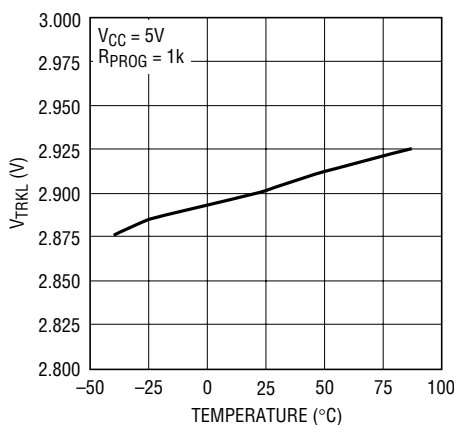
Trickle Charge Current vs Temperature



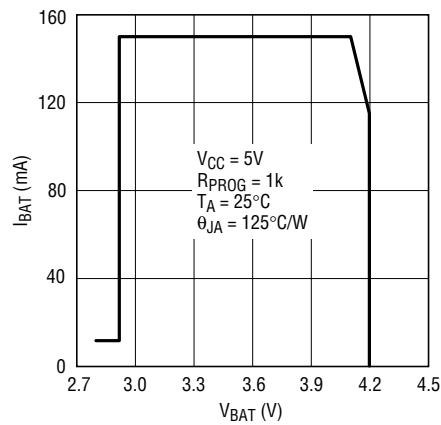
Trickle Charge Current vs Supply Voltage



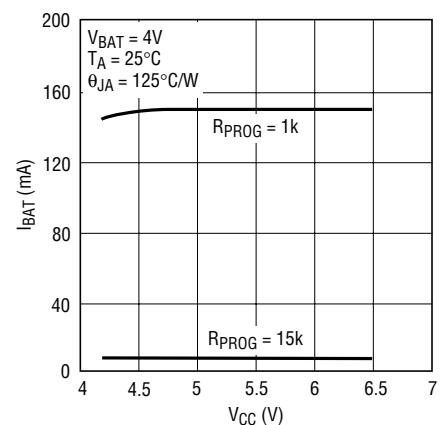
Trickle Charge Threshold vs Temperature



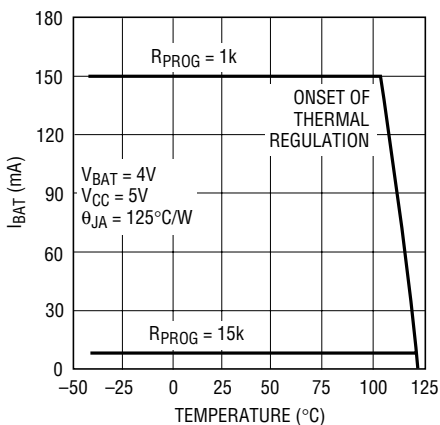
Charge Current vs Battery Voltage



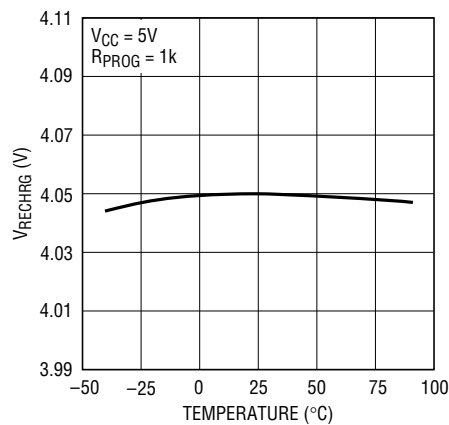
Charge Current vs Supply Voltage



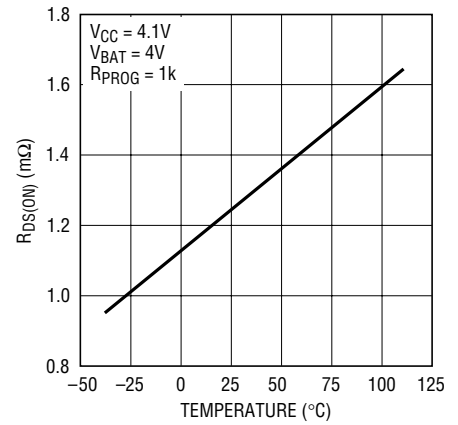
Charge Current vs Ambient Temperature



Recharge Voltage Threshold vs Temperature



Power FET "ON" Resistance vs Temperature



PIN FUNCTIONS

CHRG (Pin 1): Open-Drain Charge Status Output. When the battery is charging, the CHRG pin is pulled low by an internal N-channel MOSFET. When the charge cycle is completed, a weak pull-down of approximately 20 μ A is connected to the CHRG pin, indicating an “AC present” condition. When the LTC4054L detects an undervoltage lockout condition, CHRG is forced high impedance.

GND (Pin 2): Ground.

BAT (Pin 3): Charge Current Output. Provides charge current to the battery and regulates the final float voltage to 4.2V. An internal precision resistor divider from this pin sets this float voltage and is disconnected in shutdown mode.

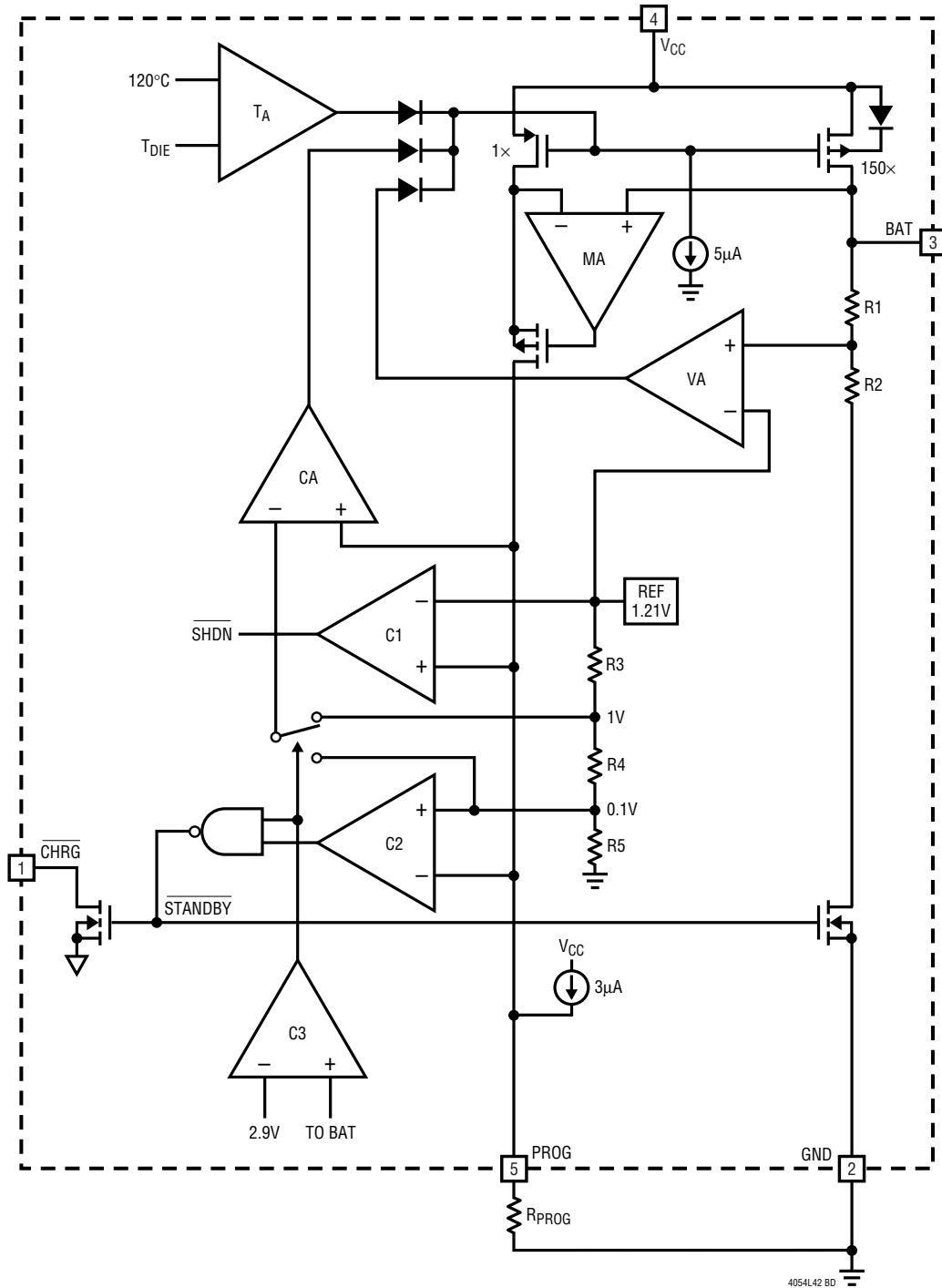
V_{CC} (Pin 4): Positive Input Supply Voltage. Provides power to the charger. V_{CC} can range from 4.25V to 6.5V and should be bypassed with at least a 1 μ F capacitor. When V_{CC} drops to within 30mV of the BAT pin voltage, the LTC4054L enters shutdown mode, dropping I_{BAT} to less than 2 μ A.

PROG (Pin 5): Charge Current Program, Charge Current Monitor and Shutdown Pin. The charge current is programmed by connecting a 1% resistor, R_{PROG}, to ground. When charging in constant-current mode, this pin serves to 1V. In all modes, the voltage on this pin can be used to measure the charge current using the following formula:

$$I_{BAT} = (V_{PROG}/R_{PROG}) \cdot 150$$

The PROG pin is also used to shut down the charger. Disconnecting the program resistor from ground allows a 3 μ A current to pull the PROG pin high. When it reaches the 1.21V shutdown threshold voltage, the charger enters shutdown mode, charging stops and the input supply current drops to 25 μ A. This pin is also clamped to approximately 2.4V. Driving this pin to voltages beyond the clamp voltage will draw currents as high as 1.5mA. Reconnecting R_{PROG} to ground will return the charger to normal operation.

BLOCK DIAGRAM



OPERATION

The LTC4054L is a single cell lithium-ion battery charger using a constant-current/constant-voltage algorithm. Its ability to control charge currents as low as 10mA make it well-suited for charging low capacity lithium-ion coin cell batteries. The LTC4054L includes an internal P-channel power MOSFET and thermal regulation circuitry. No blocking diode or external sense resistor is required; thus, the basic charger circuit requires only three external components. Furthermore, the LTC4054L is capable of operating from a USB power source.

Normal Charge Cycle

The charge cycle begins when the voltage at the V_{CC} pin rises above the UVLO level and a 1% program resistor is connected from the PROG pin to ground. If the BAT pin is less than 2.9V, the charger enters trickle charge mode. In this mode, the LTC4054L supplies approximately 1/10 the programmed charge current in order to bring the battery voltage up to a safe level for full current charging.

When the BAT pin voltage rises above 2.9V, the charger enters constant-current mode, where the programmed charge current is supplied to the battery. When the BAT pin approaches the final float voltage (4.2V), the LTC4054L enters constant-voltage mode and the charge current begins to decrease. When the charge current drops to 1/10 of the programmed value, the charge cycle ends.

Programming Charge Current

The charge current is programmed using a single resistor from the PROG pin to ground. The battery charge current is 150 times the current out of the PROG pin. The program resistor and the charge current are calculated using the following equations:

$$R_{\text{PROG}} = \frac{150V}{I_{\text{CHG}}}, \quad I_{\text{CHG}} = \frac{150V}{R_{\text{PROG}}}$$

The charge current out of the BAT pin can be determined at any time by monitoring the PROG pin voltage using the following equation:

$$I_{\text{BAT}} = \frac{V_{\text{PROG}}}{R_{\text{PROG}}} \cdot 150$$

Charge Termination

The charge cycle is terminated when the charge current falls to 1/10th the programmed value after the final float voltage is reached. This condition is detected by using an internal, filtered comparator to monitor the PROG pin. When the PROG pin voltage falls below 100mV¹ for longer than t_{TERM} (typically 1ms), charging is terminated. The charge current is latched off and the LTC4054L enters standby mode, where the input supply current drops to 200μA. (Note: C/10 termination is disabled in trickle charging and thermal limiting modes.)

While charging, transient loads on the BAT pin can cause the PROG pin to fall below 100mV for short periods of time before the DC charge current has dropped to 1/10th the programmed value. The 1ms filter time (t_{TERM}) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the *average* charge current drops below 1/10th the programmed value for longer than t_{TERM} , the LTC4054L terminates the charge cycle and ceases to provide any current through the BAT pin. In this state, all loads on the BAT pin must be supplied by the battery.

The LTC4054L constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the 4.05V recharge threshold (V_{RECHRG}), another charge cycle begins and current is once again supplied to the battery. To manually restart a charge cycle when in standby mode, the input voltage must be removed and reapplied, or the charger must be shut down and restarted using the PROG pin. Figure 1 shows the state diagram of a typical charge cycle.

¹Any external sources that hold the PROG pin above 100mV will prevent the LTC4054L from terminating a charge cycle.

OPERATION

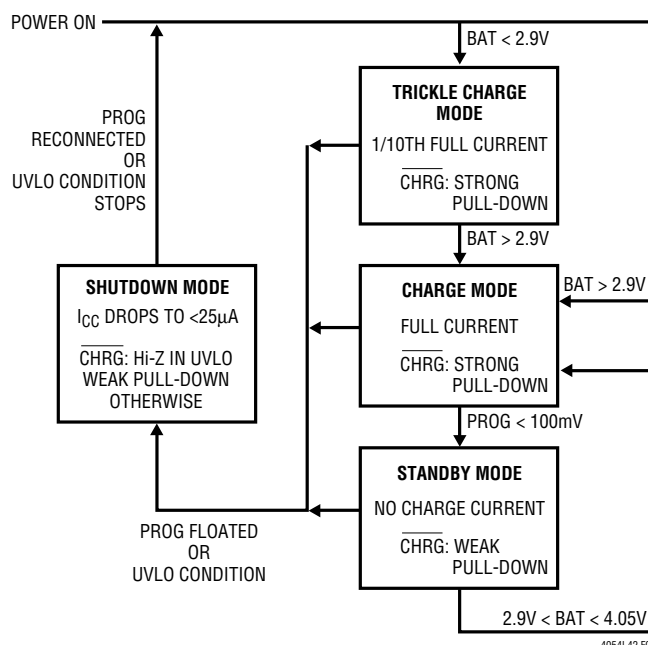


Figure 1. State Diagram of a Typical Charge Cycle

Charge Status Indicator (CHRG)

The charge status output has three different states: strong pull-down ($\sim 10\text{mA}$), weak pull-down ($\sim 20\mu\text{A}$), and high impedance. The strong pull-down state indicates that the LTC4054L is in a charge cycle. Once the charge cycle has terminated, the pin state is determined by undervoltage lockout conditions. A weak pull-down indicates that V_{CC} meets the UVLO conditions and the LTC4054L is ready to charge. High impedance indicates that LTC4054L is in undervoltage lock-out mode: either V_{CC} is within 100mV of the BAT pin voltage or insufficient voltage is applied to the V_{CC} pin. A microprocessor can be used to distinguish between these three states—this method is discussed in the Applications Information section.

Thermal Limiting

An internal thermal feedback loop reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 120°C . This feature protects the LTC4054L from excessive temperature and

allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the LTC4054L. The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions. ThinSOT power considerations are discussed further in the Applications Information section.

Undervoltage Lockout (UVLO)

An internal undervoltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until V_{CC} rises above the undervoltage lockout threshold. The UVLO circuit has a built-in hysteresis of 200mV. Furthermore, to protect against reverse current in the power MOSFET, the UVLO circuit keeps the charger in shutdown mode if V_{CC} falls to within 30mV of the battery voltage. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until V_{CC} rises 100mV above the battery voltage.

OPERATION

Manual Shutdown

At any point in the charge cycle, the LTC4054L can be put into shutdown mode by removing R_{PROG} thus floating the PROG pin. This reduces the battery drain current to less than $2\mu\text{A}$ and the supply current to less than $50\mu\text{A}$. A new charge cycle can be initiated by reconnecting the program resistor.

In manual shutdown, the $\overline{\text{CHRG}}$ pin is in a weak pull-down state as long as V_{CC} is high enough to exceed the UVLO conditions. The $\overline{\text{CHRG}}$ pin is in a high impedance state if the LTC4054L is in undervoltage lockout mode: either V_{CC} is within 100mV of the BAT pin voltage or insufficient voltage is applied to the V_{CC} pin.

Automatic Recharge

Once the charge cycle is terminated, the LTC4054L continuously monitors the voltage on the BAT pin using a comparator with a 2ms filter time (t_{RECHARGE}). A charge cycle restarts when the battery voltage falls below 4.05V (which corresponds to approximately 80% to 90% battery capacity). This ensures that the battery is kept at or near a fully charged condition and eliminates the need for periodic charge cycle initiations. $\overline{\text{CHRG}}$ output enters a strong pull-down state during recharge cycles.

APPLICATIONS INFORMATION

Stability Considerations

The constant-voltage mode feedback loop is stable without an output capacitor provided a battery is connected to the charger output. With no battery present, an output capacitor is recommended to reduce ripple voltage. When using high value, low ESR ceramic capacitors, it is recommended to add a 1Ω resistor in series with the capacitor. No series resistor is needed if tantalum capacitors are used.

In constant-current mode, the PROG pin is in the feedback loop, not the battery. The constant-current mode stability is affected by the impedance at the PROG pin. With no additional capacitance on the PROG pin, the charger is stable with program resistor values as high as 20k. However, additional capacitance on this node reduces the maximum allowed program resistor. The pole frequency at the PROG pin should be kept above 100kHz. Therefore, if the PROG pin is loaded with a capacitance, C_{PROG} , the following equation can be used to calculate the maximum resistance value for R_{PROG} :

$$R_{\text{PROG}} \leq \frac{1}{2\pi \cdot 10^5 \cdot C_{\text{PROG}}}$$

Average, rather than instantaneous, charge current may be of interest to the user. For example, if a switching power supply operating in low current mode is connected in parallel with the battery, the average current being pulled out of the BAT pin is typically of more interest than the instantaneous current pulses. In such a case, a simple RC filter can be used on the PROG pin to measure the average battery current as shown in Figure 2. A 10k resistor has been added between the PROG pin and the filter capacitor to ensure stability.

Power Dissipation

The conditions that cause the LTC4054L to reduce charge current through thermal feedback can be approximated by considering the power dissipated in the IC. Nearly all of this power dissipation is generated from the internal MOSFET—this is calculated to be approximately:

$$P_D = (V_{\text{CC}} - V_{\text{BAT}}) \cdot I_{\text{BAT}}$$

where P_D is the power dissipated, V_{CC} is the input supply voltage, V_{BAT} is the battery voltage and I_{BAT} is the charge current. The approximate ambient temperature at which the thermal feedback begins to protect the IC is:

$$T_A = 120^\circ\text{C} - P_D \theta_{\text{JA}}$$

$$T_A = 120^\circ\text{C} - (V_{\text{CC}} - V_{\text{BAT}}) \cdot I_{\text{BAT}} \cdot \theta_{\text{JA}}$$

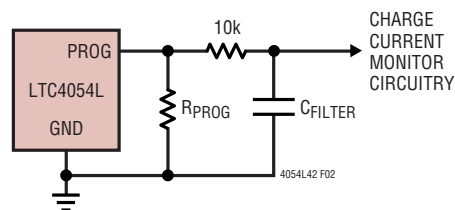


Figure 2. Isolating Capacitive Load on PROG Pin and Filtering

APPLICATIONS INFORMATION

Example: An LTC4054L operating from a 6V wall adapter is programmed to supply 150mA full-scale current to a discharged Li-Ion battery with a voltage of 3.75V. Assuming θ_{JA} is 200°C/W, the ambient temperature at which the LTC4054L will begin to reduce the charge current is approximately:

$$T_A = 120^\circ\text{C} - (6\text{V} - 3.75\text{V}) \cdot (150\text{mA}) \cdot 200^\circ\text{C/W}$$

$$T_A = 120^\circ\text{C} - 0.3375\text{W} \cdot 200^\circ\text{C/W} = 120^\circ\text{C} - 67.5^\circ\text{C}$$

$$T_A = 52.5^\circ\text{C}$$

The LTC4054L can be used above 52.5°C, but the charge current will be reduced from 150mA. The approximate current at a given ambient temperature can be approximated by:

$$I_{\text{BAT}} = \frac{120^\circ\text{C} - T_A}{(V_{\text{CC}} - V_{\text{BAT}}) \cdot \theta_{JA}}$$

Using the previous example with an ambient temperature of 60°C, the charge current will be reduced to approximately:

$$I_{\text{BAT}} = \frac{120^\circ\text{C} - 60^\circ\text{C}}{(6\text{V} - 3.75\text{V}) \cdot 200^\circ\text{C/W}} = \frac{60^\circ\text{C}}{450^\circ\text{C/A}}$$

$$I_{\text{BAT}} = 133\text{mA}$$

Moreover, when thermal feedback reduces the charge current, the voltage at the PROG pin is also reduced proportionally as discussed in the Operation section.

It is important to remember that LTC4054L applications do not need to be designed for worst-case thermal conditions since the IC will automatically reduce power dissipation when the junction temperature reaches approximately 120°C.

Thermal Considerations

Because of the small size of the ThinSOT package, it is very important to use a good thermal PC board layout to maximize the available charge current. The thermal path for the heat generated by the IC is from the die to the copper lead frame, through the package leads, (especially the ground lead) to the PC board copper. The PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Feedthrough vias to inner or backside copper layers are also useful in improving the overall thermal performance of the charger. Other heat sources on the board, not related to the charger, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum charge current.

The following table lists thermal resistance for several different board sizes and copper areas. All measurements were taken in still air on 3/32" FR-4 board with the device mounted on topside.

Table 1. Measured Thermal Resistance (2-Layer Board*)

COPPER AREA		BOARD AREA	THERMAL RESISTANCE JUNCTION-TO-AMBIENT
TOPSIDE	BACKSIDE		
2500mm ²	2500mm ²	2500mm ²	125°C/W
1000mm ²	2500mm ²	2500mm ²	125°C/W
225mm ²	2500mm ²	2500mm ²	130°C/W
100mm ²	2500mm ²	2500mm ²	135°C/W
50mm ²	2500mm ²	2500mm ²	150°C/W

*Each layer uses one ounce copper

Table 2. Measured Thermal Resistance (4-Layer Board)**

COPPER AREA (EACH SIDE)	BOARD AREA	THERMAL RESISTANCE JUNCTION-TO-AMBIENT
2500mm ^{2***}	2500mm ²	80°C/W

**Top and bottom layers use two ounce copper, inner layers use one ounce copper.

***10,000mm² total copper area

APPLICATIONS INFORMATION

V_{CC} Bypass Capacitor

Many types of capacitors can be used for input bypassing, however, caution must be exercised when using multi-layer ceramic capacitors. Because of the self resonant and high Q characteristics of some types of ceramic capacitors, high voltage transients can be generated under some start-up conditions, such as connecting the charger input to a live power source. Adding a 1.5Ω resistor in series with an X5R ceramic capacitor will minimize start-up voltage transients. For more information, refer to Application Note 88.

Charge Current Soft-Start

The LTC4054L includes a soft-start circuit to minimize the inrush current at the start of a charge cycle. When a charge cycle is initiated, the charge current ramps from zero to the full-scale current over a period of approximately 100μs. This has the effect of minimizing the transient current load on the power supply during start-up.

CHRG Status Output Pin

The CHRG pin can provide an indication that the input voltage is greater than the undervoltage lockout threshold level. A weak pull-down current of approximately 20μA indicates that sufficient voltage is applied to V_{CC} to begin charging. When a discharged battery is connected to the charger, the constant current portion of the charge

cycle begins and the CHRG pin pulls to ground. The CHRG pin can sink up to 10mA to drive an LED that indicates that a charge cycle is in progress.

When the battery is nearing full charge, the charger enters the constant-voltage portion of the charge cycle and the charge current begins to drop. When the charge current drops below 1/10 of the programmed current, the charge cycle ends, and the strong pull-down is replaced by the 20μA pull-down, indicating that the charge cycle has ended. If the input voltage is removed or drops below the undervoltage lockout threshold, the CHRG pin becomes high impedance. Figure 3 shows that by using two different value pull-up resistors, a microprocessor can detect all three states from this pin.

To detect when the LTC4054L is in charge mode, force the digital output pin (OUT) high and measure the voltage at the CHRG pin. The N-channel MOSFET will pull the pin voltage low even with the 2k pull-up resistor. Once the charge cycle terminates, the N-channel MOSFET is turned off and a 20μA current source is connected to the CHRG pin. The IN pin will then be pulled high by the 2k pull-up resistor. To determine if there is a weak pull-down current, the OUT pin should be forced to a high impedance state. The weak current source will pull the IN pin low through the 800k resistor; if CHRG is high impedance, the IN pin will be pulled high, indicating that the part is in a UVLO state.

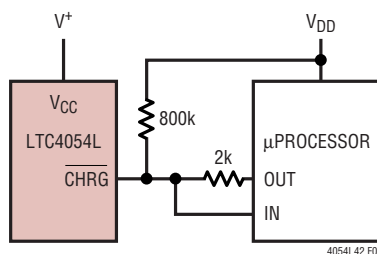


Figure 3. Using a Microprocessor to Determine CHRG State

APPLICATIONS INFORMATION

Reverse Polarity Input Voltage Protection

In some applications, protection from reverse polarity voltage on V_{CC} is desired. If the supply voltage is high enough, a series blocking diode can be used. In other cases, where the voltage drop must be kept low a P-channel MOSFET can be used (as shown in Figure 4).

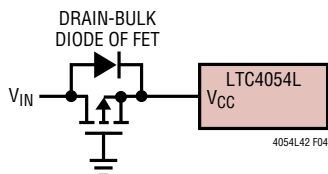


Figure 4. Low Loss Input Reverse Polarity Protection

USB and Wall Adapter Power

The LTC4054L allows charging from both a wall adapter and a USB port. Figure 5 shows an example of how to combine wall adapter and USB power inputs. A P-channel MOSFET, MP1, is used to prevent back conducting into the USB port when a wall adapter is present and a Schottky diode, D1, is used to prevent USB power loss through the 1k pull-down resistor.

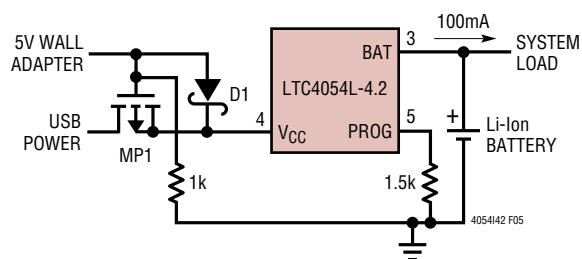
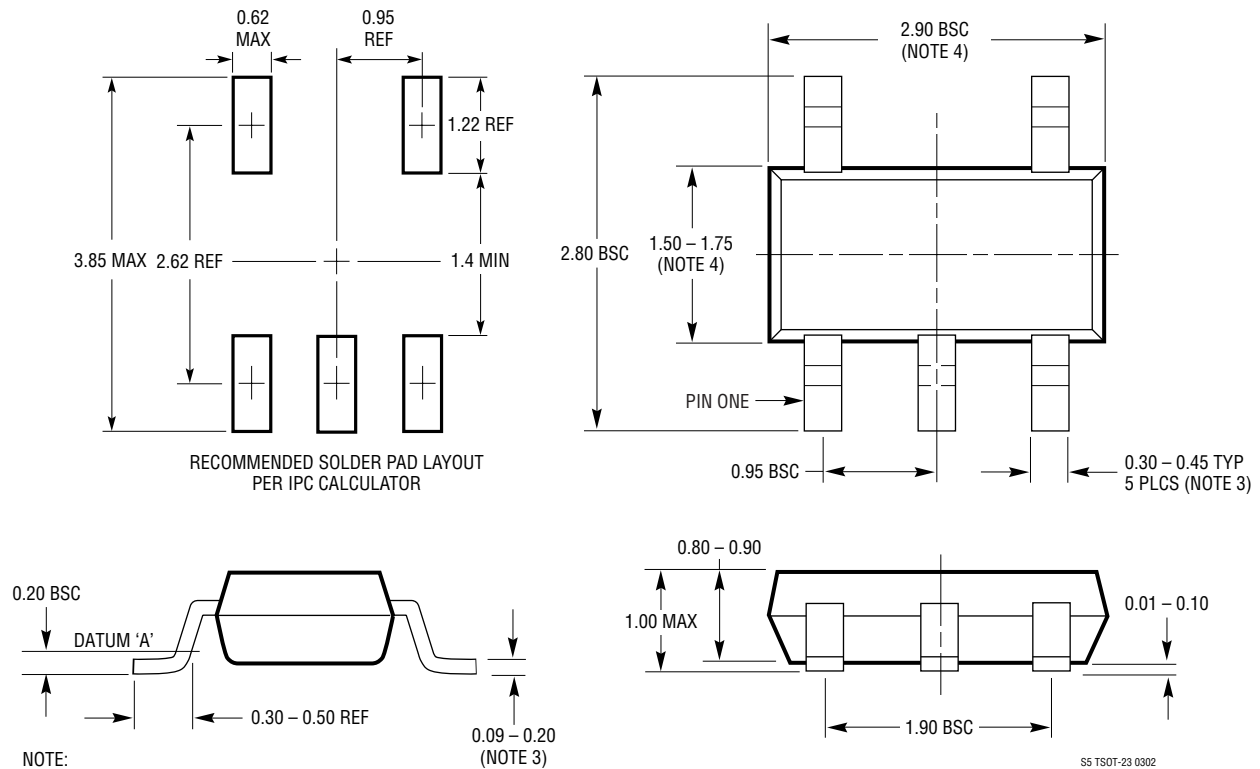


Figure 5. Combining Wall Adapter and USB Power

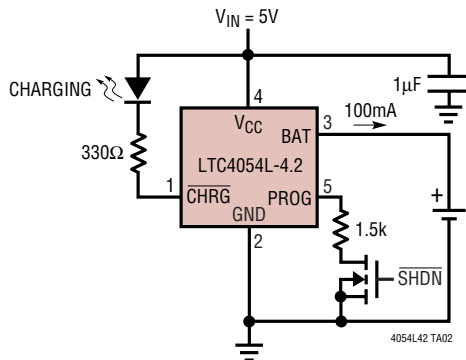
PACKAGE DESCRIPTION

S5 Package
5-Lead Plastic TSOT-23
 (Reference LTC DWG # 05-08-1635)

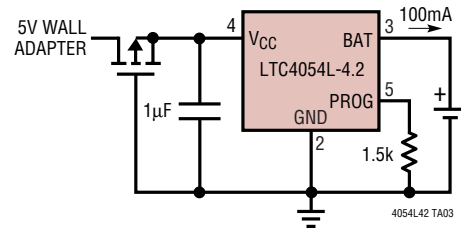


TYPICAL APPLICATIONS

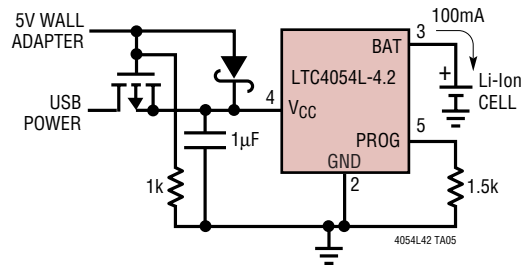
Full Featured Single Cell Li-Ion Charger



Basic Li-Ion Battery Charger with Reverse Polarity Input Protection



USB/Wall Adapter Power Li-Ion Charger



RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC1731	Lithium-Ion Linear Battery Charger Controller	Simple Charger uses External FET, Features Preset Voltages, C/10 Charger Detection and Programmable Timer
LTC1732	Lithium-Ion Linear Battery Charger Controller	Simple Charger uses External FET, Features Preset Voltages, C/10 Charger Detection and Programmable Timer, Input Power Good Indication
LTC1733	Monolithic Lithium-Ion Linear Battery Charger	Standalone Charger with Programmable Timer, Up to 1.5A Charge Current
LTC1734	Lithium-Ion Linear Battery Charger in ThinSOT	Simple ThinSOT Charger, No Blocking Diode, No Sense Resistor Needed
LTC1734L	Lithium-Ion Linear Battery Charger in ThinSOT	Low Current Version of LTC1734
LTC1998	Lithium-Ion Low Battery Detector	1% Accurate 2.5μA Quiescent Current, SOT-23
LTC4050	Lithium-Ion Linear Battery Charger Controller	Simple Charger uses External FET, Features Preset Voltages, C/10 Charger Detection and Programmable Timer, Input Power Good Indication, Thermistor Interface
LTC4052	Monolithic Lithium-Ion Battery Pulse Charger	No Blocking Diode or External Power FET Required, Safety Current Limit
LTC4053	USB Compatible Monolithic Li-Ion Battery Charger	Standalone Charger with Programmable Timer, Up to 1.25A Charge Current
LTC4054	800mA Standalone Linear Li-Ion Battery Charger with Thermal Regulation in ThinSOT	No External MOSFET, Sense Resistor or Blocking Diode Required, Charge Current Monitor for Gas Gauging, C/10 Charge Termination
LTC4056	Standalone Lithium-Ion Linear Battery Charger in ThinSOT	Standalone Charger with Programmable Timer, No Blocking Diode, No Sense Resistor Needed
LTC4057	Monolithic Lithium-Ion Linear Battery Charger with Thermal Regulation in ThinSOT	No External MOSFET, Sense Resistor or Blocking Diode Required, Charge Current Monitor for Gas Gauging
LTC4058	950mA Standalone Li-Ion Charger in 3mm × 3mm DFN	USB Compatible, Thermal Regulation Protects Against Overheating
LTC4410	USB Power Manager	For Simultaneous Operation of USB Peripheral and Battery Charging from USB Port, Keeps Current Drawn from USB Port Constant, Keeps Battery Fresh, Use with the LTC4053, LTC1733, or LTC4054

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