

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage $\pm 22V$
 Differential Input Voltage $\pm 30V$
 Input Voltage Equal to Positive Supply Voltage
 Input Voltage 5V Below Negative Supply Voltage
 Output Short-Circuit Duration Indefinite

Operating Temperature Range

LT1077AM/LT1077M (**OBSOLETE**).... $-55^{\circ}C$ to $125^{\circ}C$

LT1077AI/LT1077I..... $-40^{\circ}C$ to $85^{\circ}C$

LT1077AC/LT1077C/LT1077S8 $0^{\circ}C$ to $70^{\circ}C$

Storage Temperature Range $-65^{\circ}C$ to $150^{\circ}C$

Lead Temperature (Soldering, 10 sec) $300^{\circ}C$

PACKAGE/ORDER INFORMATION

TOP VIEW H PACKAGE 8-LEAD TO-5 METAL CAN $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 150^{\circ}C/W$, $\theta_{JC} = 45^{\circ}C/W$	TOP VIEW J8 PACKAGE 8-LEAD CERDIP $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 100^{\circ}C/W$	TOP VIEW N8 PACKAGE 8-LEAD PDIP $T_{JMAX} = 100^{\circ}C$, $\theta_{JA} = 130^{\circ}C/W$	TOP VIEW S8 PACKAGE 8-LEAD PLASTIC SO $T_{JMAX} = 150^{\circ}C$, $\theta_{JA} = 190^{\circ}C/W$
ORDER PART NUMBER	ORDER PART NUMBER	ORDER PART NUMBER	S8 PART MARKING
LT1077AMH LT1077MH LT1077ACH LT1077CH	LT1077AMJ8 LT1077MJ8 LT1077ACJ8 LT1077CJ8	LT1077S8 LT1077IS8 LT1077AIN8 LT1077IN8 LT1077ACN8 LT1077CN8	1077 1077I
OBSOLETE PACKAGE Consider the N8 or S8 Package for Alternate Source			LT1077 • P0101

Consult LTC Marketing for parts specified with wider operating temperature ranges.

ELECTRICAL CHARACTERISTICS

$V_S = 5V$, $0V$, $V_{CM} = 0.1V$, $V_O = 1.4V$, $T_A = 25^{\circ}C$ unless noted.

SYMBOL	PARAMETER	CONDITIONS	LT1077AM/AI/AC			LT1077M/I/C/S8			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	LT1077S8		9	40	10	60		μV
						12	150		μV
$\frac{\Delta V_{OS}}{\Delta Time}$	Long Term Input Offset Voltage Stability			0.4		0.4			$\mu V/Mo$
I_{OS}	Input Offset Current			0.06	0.35	0.06	0.45		nA
I_B	Input Bias Current			7	9	7	11		nA
e_n	Input Noise Voltage	0.1Hz to 10Hz (Note3)		0.5	1.1	0.5			μV_{p-p}
	Input Noise Voltage Density	$f_0 = 10Hz$ (Note 3)		28	43	28			$nV/\sqrt{Hz}$
		$f_0 = 1000Hz$ (Note3)		27	35	27			$nV/\sqrt{Hz}$
i_n	Input Noise Current	0.1Hz to 10Hz (Note3)		2.5	4.5	2.5			pA_{p-p}

ELECTRICAL CHARACTERISTICS

$V_S = 5V$, $0V$, $V_{CM} = 0.1V$, $V_O = 1.4V$, $T_A = 25^\circ C$ unless noted.

SYMBOL	PARAMETER	CONDITIONS	LT1077AM/AI/AC			LT1077M/I/C/S8			UNITS
			MIN	TYP	MAX	MIN	TYP	MAX	
	Input Noise Current Density	$f_O = 10Hz$ (Note 3) $f_O = 1000Hz$		0.065 0.02	0.11		0.065 0.02		$pA/\sqrt{Hz}$ $pA/\sqrt{Hz}$
	Input Resistance Differential Mode Common Mode	(Note 4)	350	700 6		270	700 6		$M\Omega$ $G\Omega$
	Input Voltage Range		3.5 0	3.8 -0.3		3.5 0	3.8 -0.3		V V
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0V$ to $3.5V$	97	106		94	105		dB
PSRR	Power Supply Rejection Ratio	$V_S = 2.3V$ to $12V$	102	118		100	117		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = 0.03V$ to $4V$, No Load $V_O = 0.03V$ to $3.5V$, $R_L = 50k$	300 250	1000 1000		240 200	1000 1000		V/mV V/mV
	Maximum Output Voltage Swing	Output Low, No Load Output Low, $2k$ to GND Output Low, $I_{SINK} = 100\mu A$ Output High, No Load Output High, $2k$ to GND		3.5 0.7 90 4.2 3.5	6 1.1 130		3.5 0.7 90 4.2 3.5	6 1.1 130	mV mV mV V V
SR	Slew Rate	(Note 2)	0.05	0.08		0.05	0.08		V/ μs
GBW	Gain Bandwidth Product	$f_O \leq 20kHz$		230			230		kHz
I_S	Supply Current			48	60		48	68	μA
	Offset Adjustment Range	$R_{pot} = 10k$, Wiper to V^+	± 500	± 900		± 500	± 900		μV
	Minimum Supply Voltage	(Note 5)		2.2	2.3		2.2	2.3	V

The ● denotes the specifications which apply over the temperature range of $-55^\circ C \leq T_A \leq 125^\circ C$ for AM/M grades, $-40^\circ C \leq T_A \leq 85^\circ C$ for AI/I grades. $V_S = 5V$, $0V$, $V_{CM} = 0.1V$, $V_O = 1.4V$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		LT1077AM/AI			LT1077M/I			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage		●		50	200		60	260	μV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift	LT1077IS8 (Note 6)	●					1	2.5	$\mu V/^\circ C$
I_{OS}	Input Offset Current		●		0.08	0.60		0.08	0.80	nA
I_B	Input Bias Current		●		8	11		8	13	nA
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0.05V$ to $3.2V$	●	92	104		88	103		dB
PSRR	Power Supply Rejection Ratio	$V_S = 3.1V$ to $12V$	●	98	114		94	113		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = 0.05V$ to $3.5V$, $R_L = 50k$	●	120	600		100	600		V/mV
	Maximum Output Voltage Swing	Output Low, No Load Output Low, $I_{SINK} = 100\mu A$ Output High, No Load Output High, $2k$ to GND	● ● ● ●		4.5 120 3.9 3	8 170		4.5 120 3.9 3	8 170	mV mV V V
I_S	Supply Current		●		54	80		54	90	μA

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the temperature range of $0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$ otherwise, specifications are at $T_A = 25^{\circ}\text{C}$. $V_S = 5\text{V}$, 0V , $V_{CM} = 0.1\text{V}$, $V_O = 1.4\text{V}$ unless noted.

SYMBOL	PARAMETER	CONDITIONS		LT1077AC			LT1077C/S8			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	LT1077S8	●		30	110		35	150	μV
			●					40	280	μV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift	(Note 6) LT1077S8 (Note 6)	●		0.4	1.6		0.5	2.0	$\mu\text{V}/^{\circ}\text{C}$
								0.7	3.0	$\mu\text{V}/^{\circ}\text{C}$
I_{OS}	Input Offset Current		●		0.07	0.45		0.07	0.60	nA
I_B	Input Bias Current		●		7	10		7	12	nA
CMRR	Common Mode Rejection Ratio	$V_{CM} = 0\text{V}$ to 3.4V	●	94	105		90	104		dB
PSRR	Power Supply Rejection Ratio	$V_S = 2.6\text{V}$ to 12V	●	100	116		97	115		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = 0.05\text{V}$ to 3.5V , $R_L = 50\text{k}$	●	180	800		150	800		V/mV
	Maximum Output Voltage Swing	Output Low, No Load	●		4.0	7		4.0	7	mV
		Output Low, $I_{SINK} = 100\mu\text{A}$	●		100	150		100	150	mV
		Output High, No Load	●	4.1	4.3		4.1	4.3		V
		Output High, 2k to GND	●	3.3	3.8		3.3	3.8		V
I_S	Supply Current		●		52	70		52	80	μA

$V_S = \pm 15\text{V}$, $T_A = 25^{\circ}\text{C}$ unless noted.

SYMBOL	PARAMETER	CONDITIONS		LT1077AM/AI/AC			LT1077M/I/C/S8			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	LT1077S8			20	150		25	200	μV
								30	300	μV
I_{OS}	Input Offset Current				0.06	0.35		0.06	0.45	nA
I_B	Input Bias Current				7	9		7	11	nA
	Input Voltage Range			13.5	13.8		13.5	13.8		V
				-15.0	-15.3		-15.0	-15.3		V
CMRR	Common Mode Rejection Ratio	$V_{CM} = 13.5\text{V}$ to -15V		100	109		97	108		dB
PSRR	Power Supply Rejection Ratio	$V_S = 5\text{V}$, 0V to $\pm 18\text{V}$		106	122		103	120		dB
A_{VOL}	Large-Signal Voltage Gain	$V_O = \pm 10\text{V}$, $R_L = 50\text{k}$		1000	8000		800	8000		V/mV
		$V_O = \pm 10\text{V}$, $R_L = 2\text{k}$		400	1500		300	1500		V/mV
V_{OUT}	Maximum Output Voltage Swing	$R_L = 50\text{k}$		± 13.0	± 14.0		± 13.0	± 14.0		V
		$R_L = 2\text{k}$		± 11.0	± 13.2		± 11.0	± 13.2		V
SR	Slew Rate			0.07	0.12		0.07	0.12		V/ μs
I_S	Supply Current				56	75		56	85	μA

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the temperature range of $-55^{\circ}\text{C} \leq T_A \leq 125^{\circ}\text{C}$ for AM/M grades, $-40^{\circ}\text{C} \leq T_A \leq 85^{\circ}\text{C}$ for AI/I grades. $V_S = \pm 15\text{V}$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		LT1077AM/AI			LT1077M/I			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage		●	60	330		75	450		μV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift	LT1077IS8 (Note 6)	●				1.1	3		$\mu\text{V}/^{\circ}\text{C}$
I_{OS}	Input Offset Current		●	0.08	0.60		0.08	0.80		nA
I_B	Input Bias Current		●	8	11		8	13		nA
A_{VOL}	Large-Signal Voltage Gain	$V_O = \pm 10\text{V}$, $R_L = 5\text{k}$	●	300	1000		250	1000		V/mV
CMRR	Common Mode Rejection Ratio	$V_{CM} = 13\text{V}$, -14.9V	●	94	107		90	106		dB
PSRR	Power Supply Rejection Ratio	$V_S = 5\text{V}$, 0V to $\pm 18\text{V}$	●	100	118		97	116		dB
	Maximum Output Voltage Swing	$R_L = 5\text{k}$	●	± 11	± 13.5		± 11	± 13.5		V
I_S	Supply Current		●	60	95		60	105		μA

The ● denotes the specifications which apply over the temperature range of $0^{\circ}\text{C} \leq T_A \leq 70^{\circ}\text{C}$. $V_S = \pm 15\text{V}$ unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		LT1077AC			LT1077C/S8			UNITS
				MIN	TYP	MAX	MIN	TYP	MAX	
V_{OS}	Input Offset Voltage	LT1077S8	● ●	40	230		50 65	320 450		μV μV
$\Delta V_{OS}/\Delta T$	Input Offset Voltage Drift	(Note 6) LT1077S8 (Note 6)	● ●	0.4	1.8		0.5 0.8	2.5 3.5		$\mu\text{V}/^{\circ}\text{C}$ $\mu\text{V}/^{\circ}\text{C}$
I_{OS}	Input Offset Current		●	0.07	0.45		0.07	0.60		nA
I_B	Input Bias Current		●	7	10		7	12		nA
A_{VOL}	Large-Signal Voltage Gain	$V_O = \pm 10\text{V}$, $R_L = 5\text{k}$	●	500	2000		400	2000		V/mV
CMRR	Common Mode Rejection Ratio	$V_{CM} = 13\text{V}$, -15V	●	97	108		94	107		dB
PSRR	Power Supply Rejection Ratio	$V_S = 5\text{V}$, 0V to $\pm 18\text{V}$	●	103	120		100	118		dB
	Maximum Output Voltage Swing	$R_L = 5\text{k}$	●	± 11	± 13.6		± 11	± 13.6		V
I_S	Supply Current		●	59	85		59	95		μA

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

Note 2: Slew rate at 5V , 0V is guaranteed by inference from the slew rate measurement at $\pm 15\text{V}$.

Note 3: This parameter is tested on a sample basis only. All noise parameters are tested with $V_S = \pm 2.5\text{V}$, $V_O = 0\text{V}$.

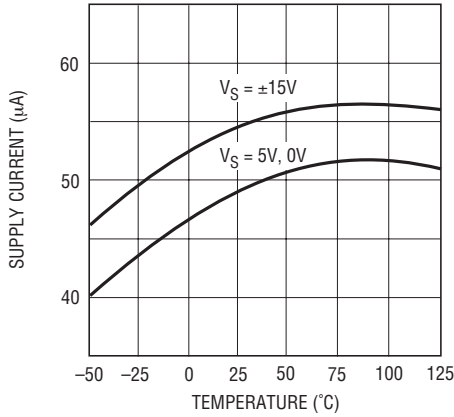
Note 4: This parameter is guaranteed by design and is not tested.

Note 5: Power supply rejection ratio is measured at the minimum supply voltage. The op amps actually work at 1.8V supply but with a typical offset skew of $-300\mu\text{V}$.

Note 6: This parameter is not 100% tested.

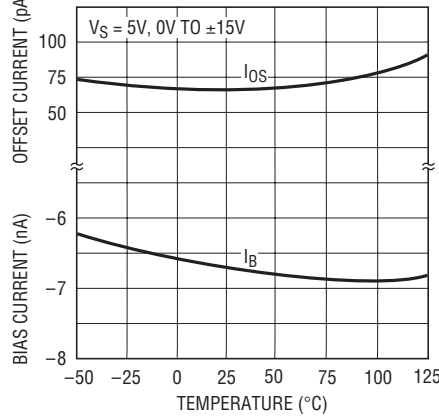
TYPICAL PERFORMANCE CHARACTERISTICS

Supply Current vs Temperature



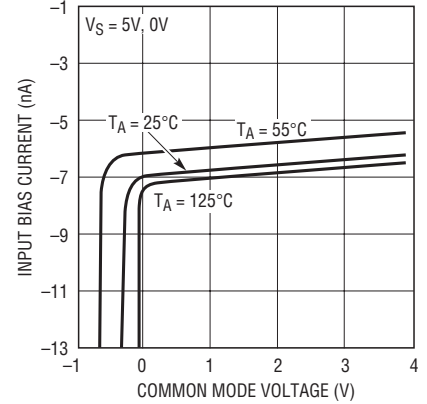
LT1077 • TPC01

Input Bias and Offset Currents vs Temperature



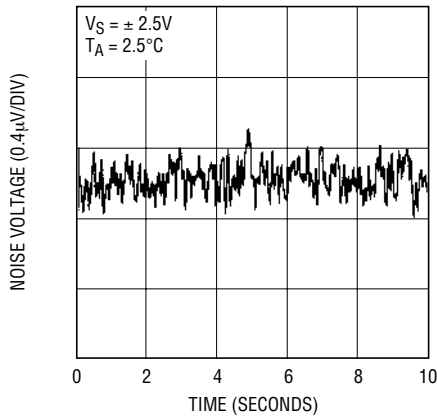
LT1077 • TPC02

Input Bias Currents vs Common Mode Voltage



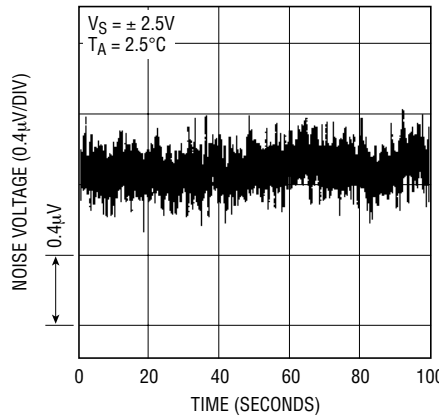
LT1077 • TPC03

0.1Hz to 10Hz Noise



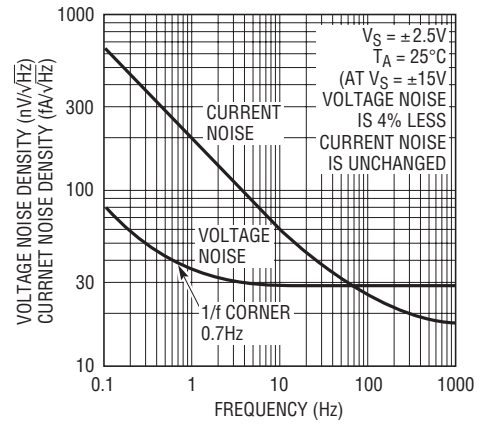
LT1077 • TPC04

0.01Hz to 10Hz Noise



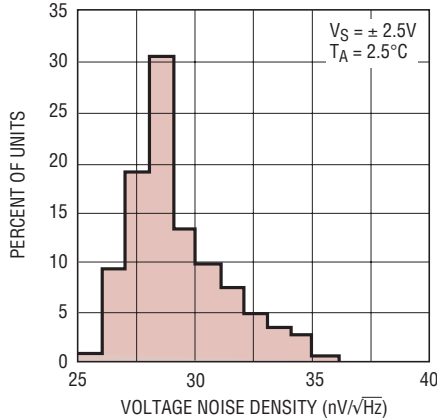
LT1077 • TPC05

Noise Spectrum



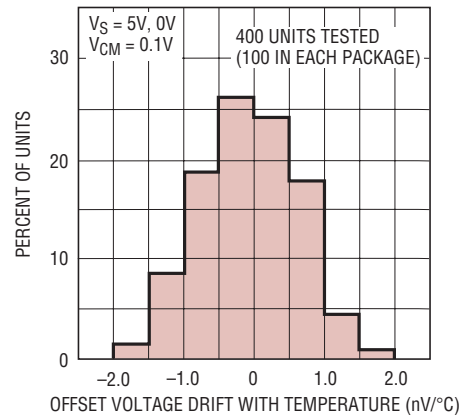
LT1077 • TPC06

10Hz Voltage Noise Distribution



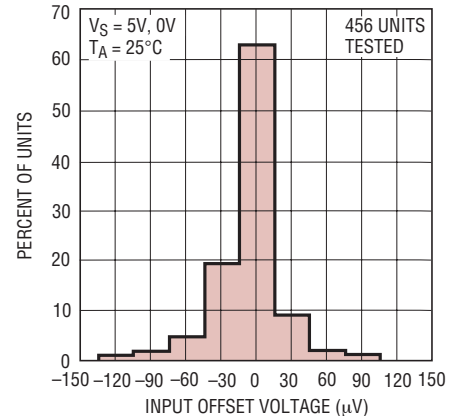
LT1077 • TPC07

Distribution of Offset Voltage Drift with Temperature (In All Packages)



LT1077 • TPC08

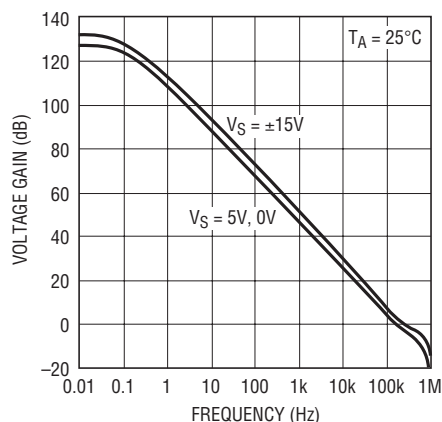
Distribution of Input Offset Voltage in Small Outline (S8) Package



LT1077 • TPC09

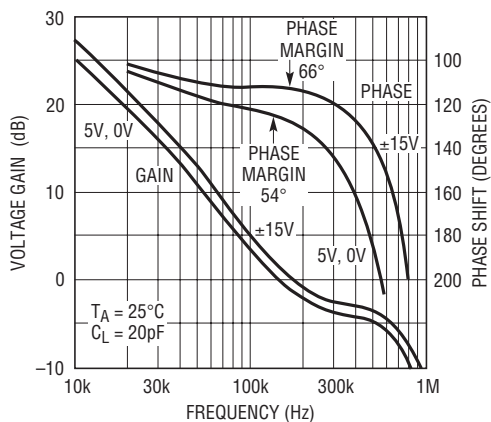
TYPICAL PERFORMANCE CHARACTERISTICS

Voltage Gain vs Frequency



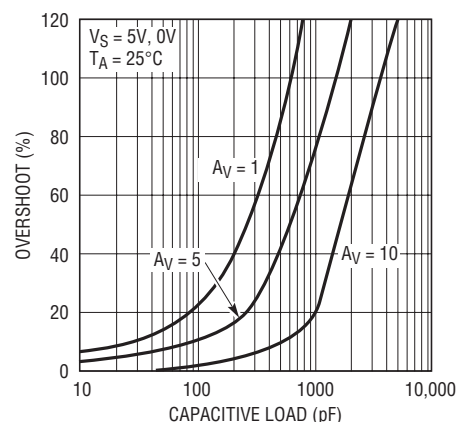
LT1077 • TPC10

Gain, Phase vs Frequency



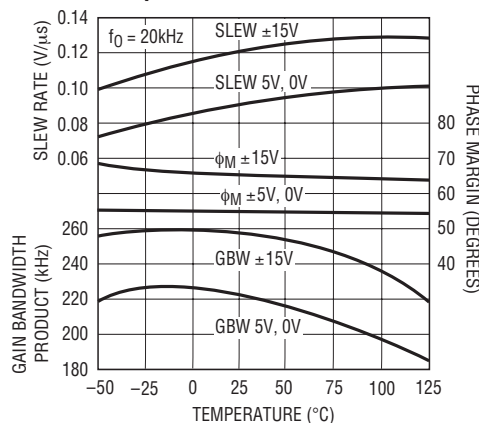
LT1077 • TPC11

Capacitive Load Handling



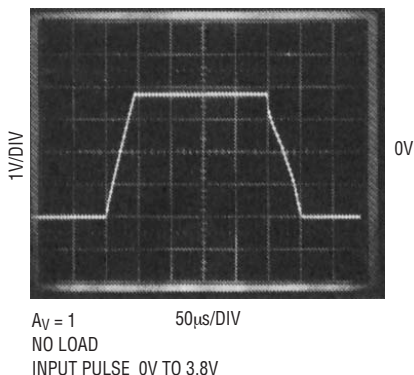
LT1077 • TPC12

Slew Rate, Gain Bandwidth Product and Phase Margin vs Temperature

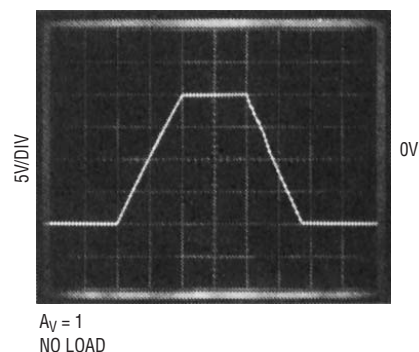


LT1077 • TPC13

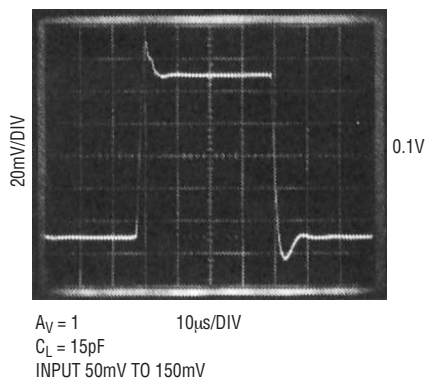
Large-Signal Transient Response $V_S = 5V, 0V$



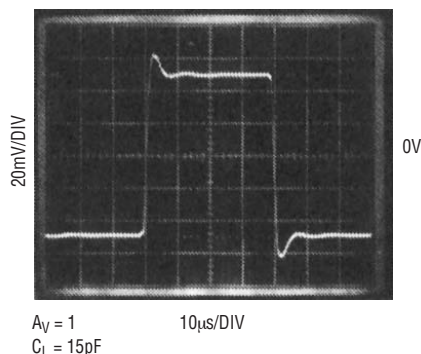
Large-Signal Transient Response $V_S = \pm 15V$



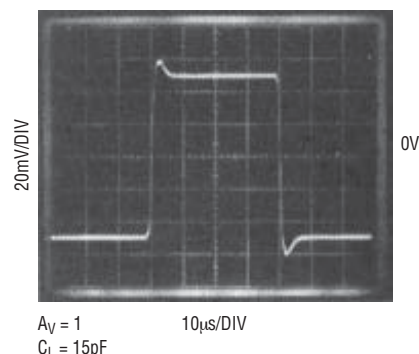
Small-Signal Transient Response $V_S = 5V, 0V$



Small-Signal Transient Response $V_S = \pm 2.5V$

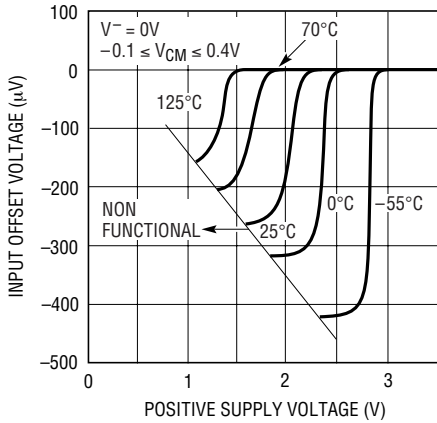


Small-Signal Transient Response $V_S = \pm 15V$

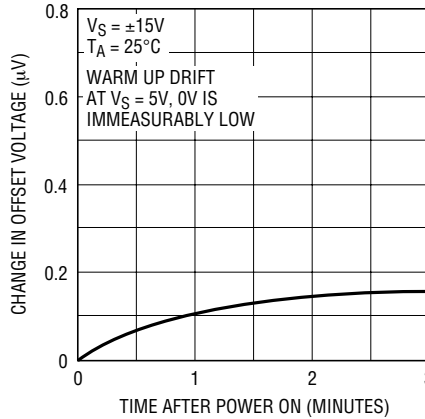


TYPICAL PERFORMANCE CHARACTERISTICS

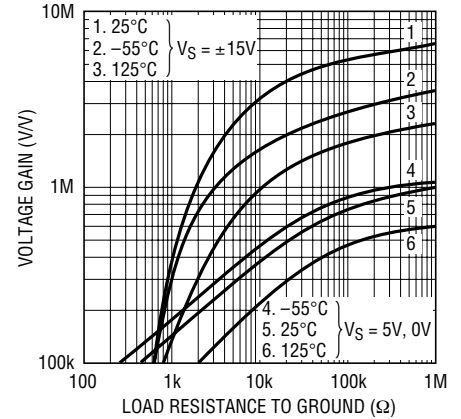
Minimum Supply Voltage



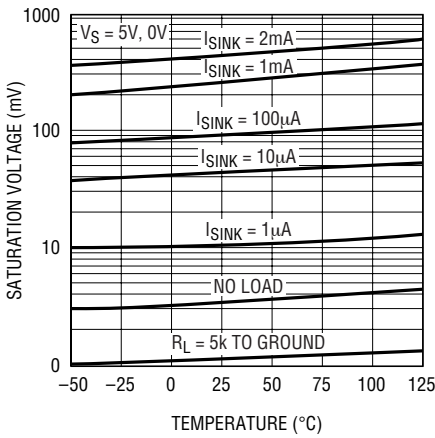
Warm-Up Drift



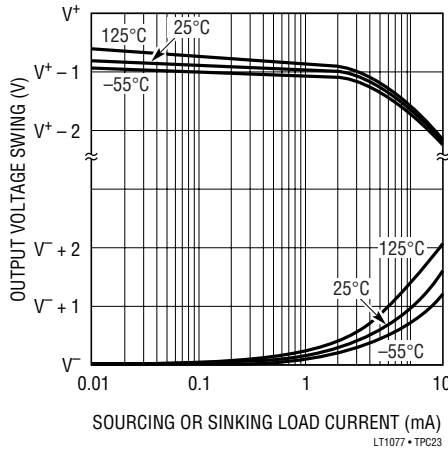
Voltage Gain vs Load Resistance



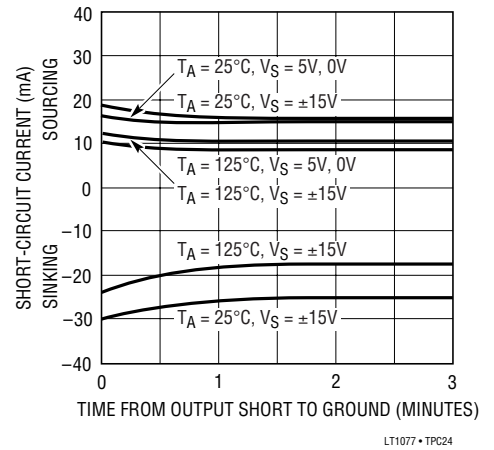
Output Saturation vs Temperature vs Sink Current



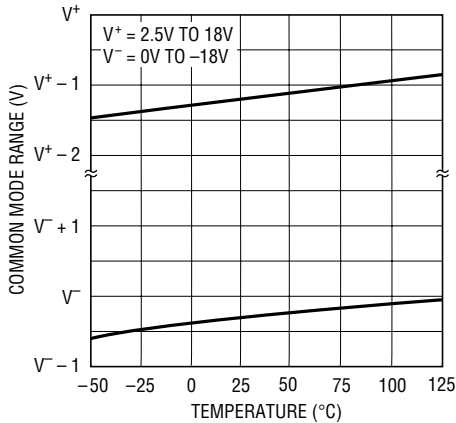
Output Voltage Swing vs Load Current



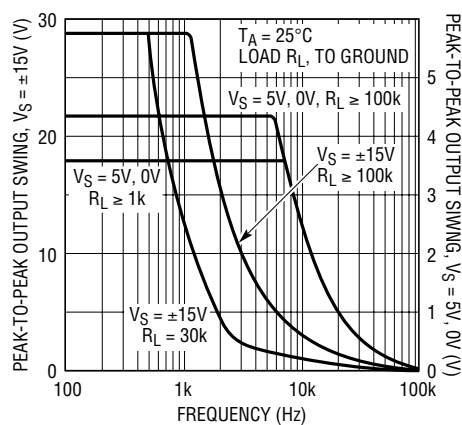
Short-Circuit Current vs Time



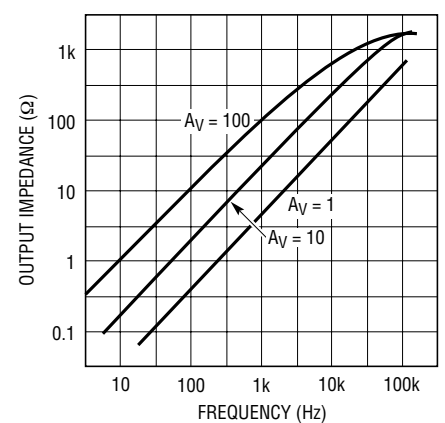
Common Mode Range vs Temperature



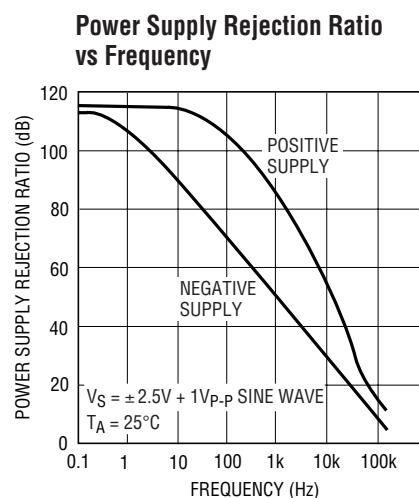
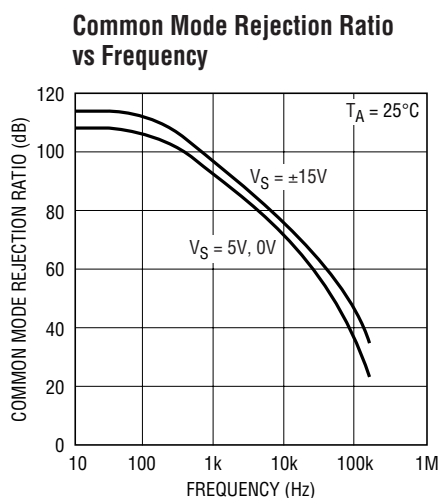
Undistorted Output Swing vs Frequency



Closed Loop Output Impedance



TYPICAL PERFORMANCE CHARACTERISTICS



APPLICATIONS INFORMATION

The LT1077 is fully specified with $V^+ = 5\text{V}$, $V^- = 0\text{V}$, $V_{\text{CM}} = 0.1\text{V}$. This set of operating conditions appears to be the most representative for battery powered micropower circuits. Offset voltage is internally trimmed to a minimum value at these supply voltages. When 9V or 3V batteries, or $\pm 2.5\text{V}$ dual supplies are used, bias and offset current changes will be minimal. Offset voltage changes will be just a few microvolts as given by the PSRR and CMRR specifications. For example, if $\text{PSRR} = 114\text{dB}$ ($= 2\mu\text{V/V}$), at 9V the offset voltage change will be $8\mu\text{V}$. Similarly, $V_S \pm 2.5\text{V}$, $V_{\text{CM}} = 0$ is equivalent to a common mode voltage change of 2.4V or a V_{OS} change of $7\mu\text{V}$ if $\text{CMRR} = 110\text{dB}$ ($3\mu\text{V/V}$).

A full set of specifications is also provided at $\pm 15\text{V}$ supply voltages for comparison with other devices and for completeness.

The LT1077 is pin compatible to, and directly replaces, such precision op amps as the OP-07, OP-77, AD707 and LT1001 with 30 to 60 times savings in supply current. The LT1077 is also a direct plug-in replacement for LT1012 and OP-97 devices with 10 times lower dissipation. Compatibility includes externally nulling the offset voltage, as all of the devices above are trimmed with a potentiometer between Pins 1 and 8 and the wiper tied to V^+ .

The LT1077 replaces and upgrades such micropower op amps as the OP-20, LM4250, and OP-90, provided that the external nulling circuitry (and set resistor in the case of the LM4250) are removed. Since the offset voltage of the LT1077 is extremely low, nulling will be unnecessary in most applications.

Single Supply Operation

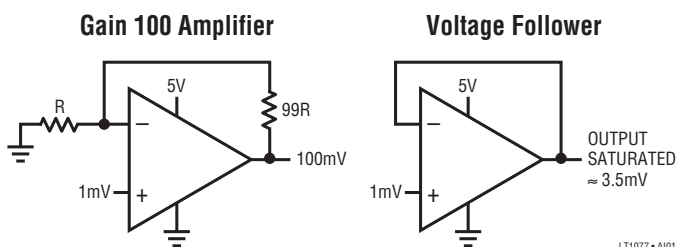
The LT1077 is fully specified for single supply operation, (i.e., when the negative supply is 0V). Input common mode range goes below ground and the output swings within a few millivolts of ground while sinking current. All competing micropower op amps either cannot swing to within 600mV of ground (OP-20, OP-220, OP-420) or need a pull-down resistor connected to the output to swing to ground (OP-90, OP-290, OP-490, HA5141/42/44). This difference is critical because in many applications these competing devices cannot be operated as micropower op amps and swing to ground simultaneously.

Consider the difference amplifiers shown in Typical Applications as an example. When the common mode signal is high and the output low, the amplifier has to sink current. In the gain of 10 circuit, the competing devices require a $30\text{k}\Omega$ pull-down resistor at the output to handle the specified signals. (The LT1077 does not need pull-down

APPLICATIONS INFORMATION

resistors.) When the output is high the pull-down resistor draws $80\mu\text{A}$ which dominates the micropower current budget. This situation is much worse in the gain of one circuit with $V^- = 0\text{V}$. At 100V common mode, the output has to sink $2\mu\text{A}$. At a minimum output voltage of 20mV competing devices require a 10k pull-down resistor. As the output now swings to 10V , this resistor draws 1mA of current.

Since the output of the LT1077 cannot go exactly to ground, but can only approach ground to within a few millivolts, care should be exercised to ensure that the output is not saturated. For example, a 1mV input signal will cause the amplifier to set up in its linear region in the gain 100 configuration shown below; however, it is not enough to make the amplifier function properly in the voltage follower mode.

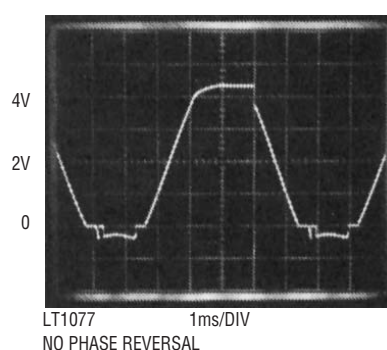
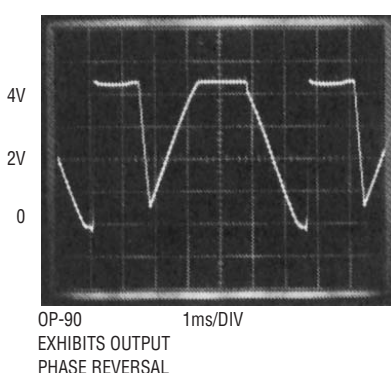
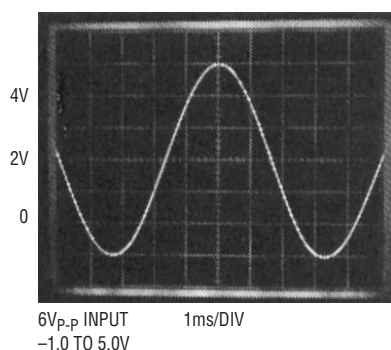


Single supply operation can also create difficulties at the input. The driving signal can fall below 0V —inadvertently or on a transient basis. If the input is more than a few hundred millivolts below ground, two distinct problems can occur on previous single supply designs, such as the LM124, LM158, OP-20, OP-21, OP-220, OP-221, OP-420 (a and b), OP-90/290/490 (b only):

a) When the input is more than a diode drop below ground, unlimited current will flow from the substrate (V^- terminal) to the input (this can destroy the unit). On the LT1077, resistors in series with the input protect the device even when the input is 5V below ground.

b) When the input is more than 400mV below ground (at 25°C), the input stage saturates and phase reversal occurs at the output (this can cause lock-up in servo systems). Due to a unique phase reversal protection circuitry, the LT1077's output does not reverse, as illustrated below, even when the input is at -1.0V .

Voltage Follower with Input Exceeding the Negative Common Mode Range ($V_S = 5\text{V}$, 0V)



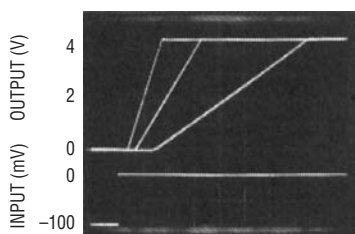
APPLICATIONS INFORMATION

Comparator Applications

The single supply operation of the LT1077 and its ability to swing close to ground while sinking current,

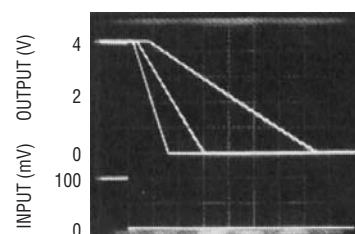
lends itself to use as a precision comparator with TTL compatible output.

**Comparator Rise Response Time
to 10mV, 5mV, 2mV Overdrive**



$V_S = 5V, 0V$ 200µs/DIV

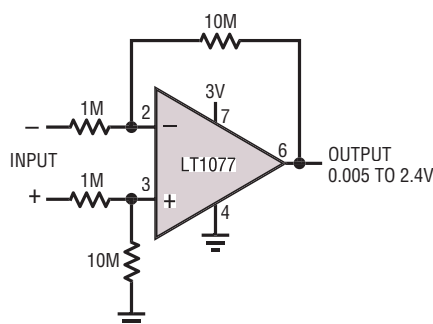
**Comparator Fall Response Time
to 10mV, 5mV, 2mV Overdrive**



$V_S = 5V, 0V$ 200µs/DIV

TYPICAL APPLICATIONS

**Megaohm Input Impedance
Gain of 10 Difference Amplifier**



BANDWIDTH = 20KHZ
 OUTPUT OFFSET = 0.7mV
 OUTPUT NOISE = 80µVPP (0.1Hz TO 10Hz)
 260µVRMS OVER FULL BANDWIDTH
 SUPPLY CURRENT = 45µA

THE USEFULNESS OF DIFFERENCE AMPLIFIERS IS LIMITED BY THE FACT THAT THE INPUT RESISTANCE IS EQUAL TO THE SOURCE RESISTANCE. THE PICO-AMPERE OFFSET CURRENT AND LOW CURRENT NOISE OF THE LT1077 ALLOWS THE USE OF 1M SOURCE RESISTORS WITHOUT DEGRADATION IN PERFORMANCE. IN ADDITION, WITH MEGAOhm RESISTORS MICROPPOWER OPERATION CAN BE MAINTAINED

LT1077 • TA03

COMMON MODE INPUT $\pm 250V$

- IN [3] R1 1M 4 R2 20k 1 50k OPTIONAL CMRR TRIM

3V TO 18V 2 7

LT1077

+ IN [7] R3 1M 6 R4 19.608k 4

0V TO -18V

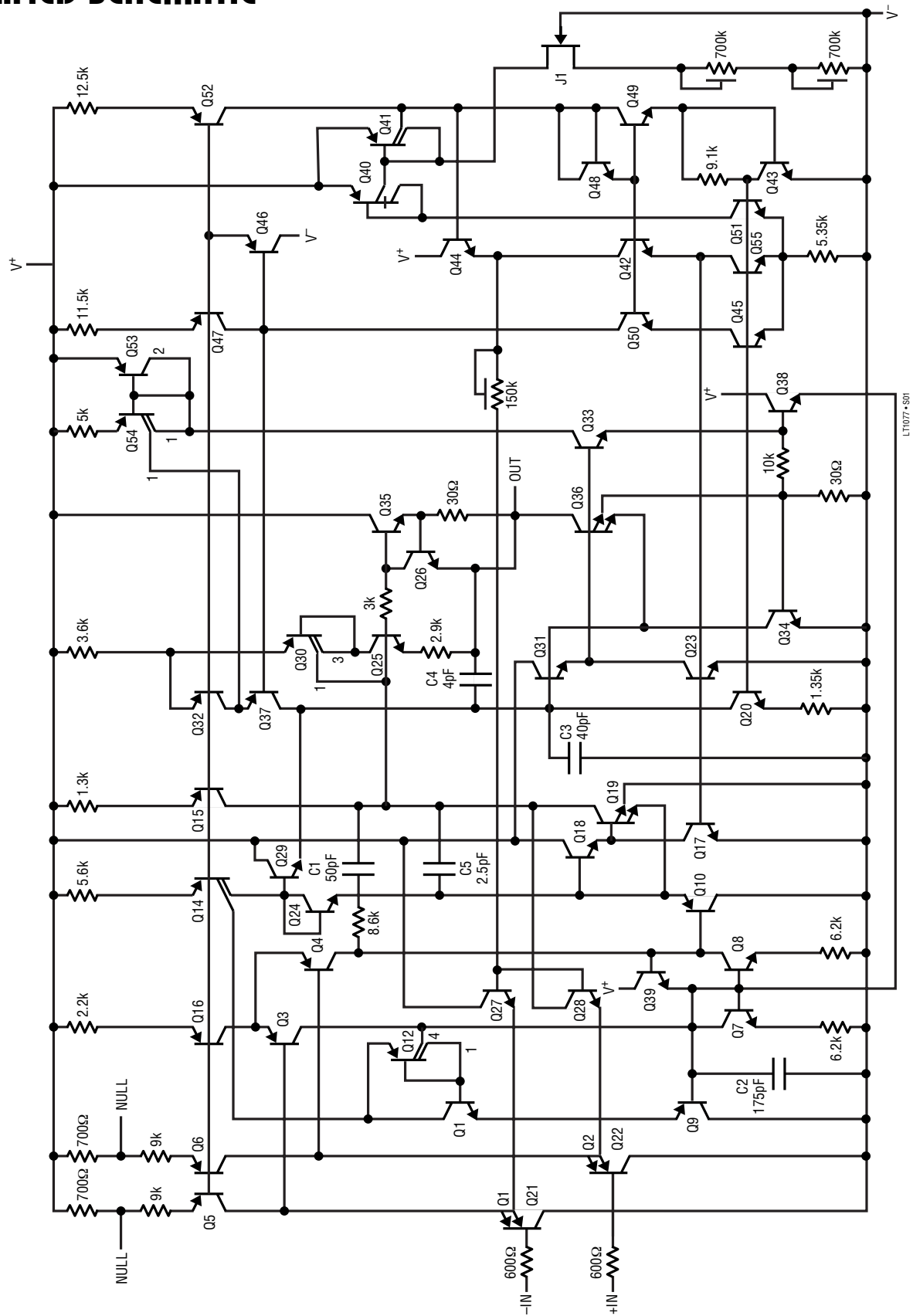
OUT [6]

R1 TO R6: VISHAY 444 ACCUTRACE THIN FILM SIP NETWORK

[X] :VISHAY 444 PIN NUMBERS

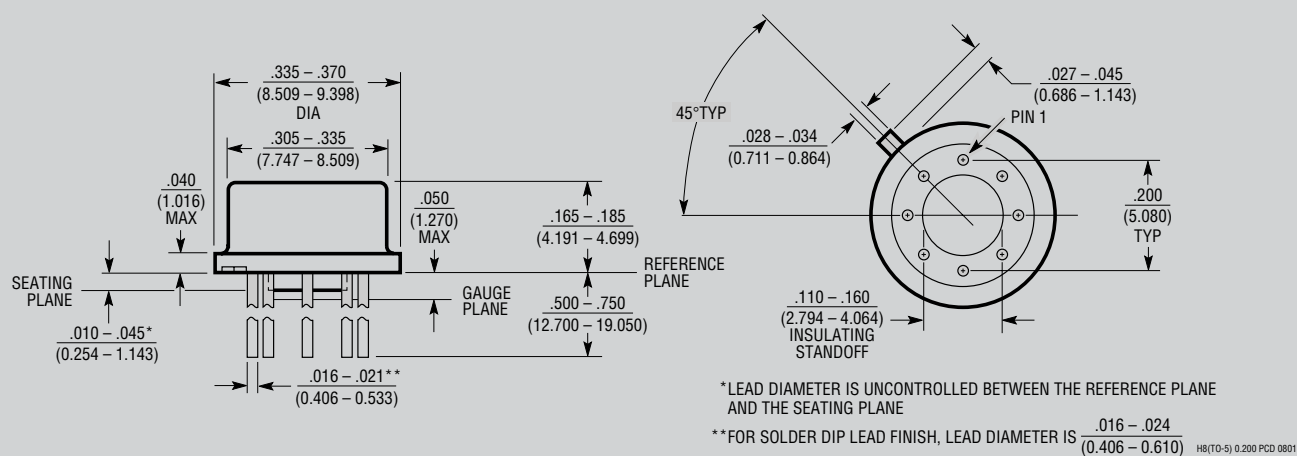
COMMON MODE REJECTION RATIO = 74dB (RESISTOR LIMITED)
 WITH OPTIONAL TRIM = 108dB
 OUTPUT OFFSET (TRIMMABLE TO ZERO) = 500μV
 OUTPUT OFFSET DRIFT = 25μV/°C
 INPUT RESISTANCE = 1M
 COMMON MODE RANGE = ±250V, V⁺ = 6.2V TO 18V, V⁻ = -4.7V TO -18V
 = ±100V, V⁺ ≥ 3.2V, V⁻ ≤ -1.8V
 = 100V, -13V, V⁺ ≥ 3.2V, V⁻ = 0V

SIMPLIFIED SCHEMATIC

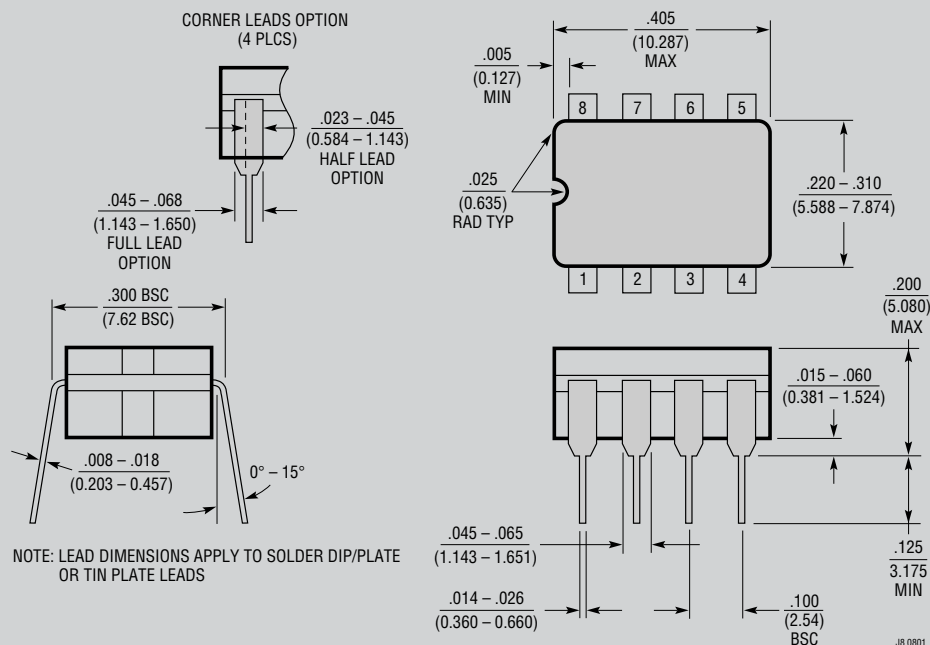


PACKAGE DESCRIPTION

H Package
8-Lead TO-5 Metal Can (.200 Inch PCD)
 (Reference LTC DWG # 05-08-1320)



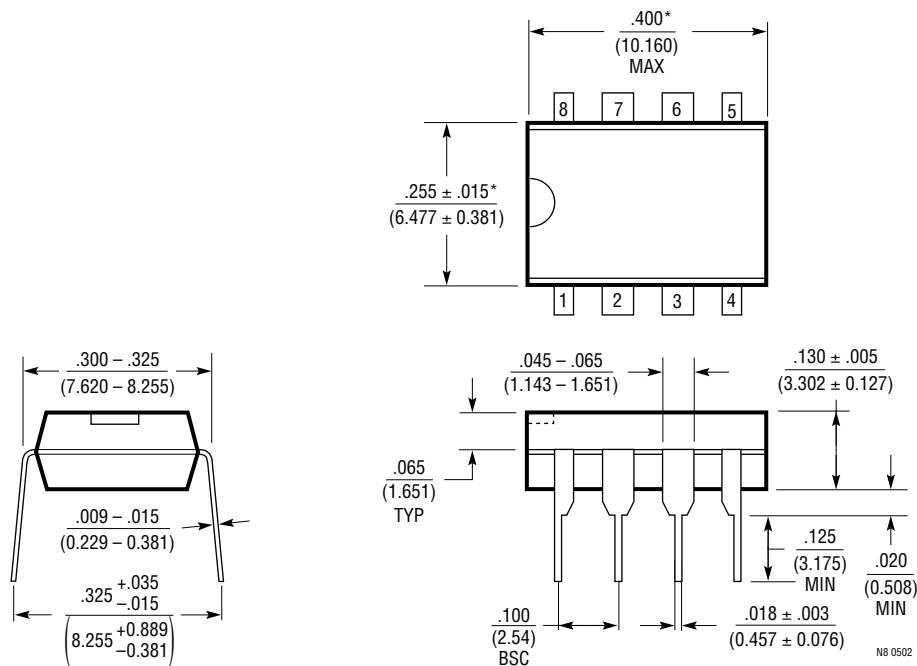
J8 Package
8-Lead CERDIP (Narrow .300 Inch, Hermetic)
 (Reference LTC DWG # 05-08-1110)



OBSOLETE PACKAGES

PACKAGE DESCRIPTION

N8 Package 8-Lead PDIP (Narrow .300 Inch) (Reference LTC DWG # 05-08-1510)



NOTE:

1. DIMENSIONS ARE $\frac{\text{INCHES}}{\text{MILLIMETERS}}$

*THESE DIMENSIONS DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS.
MOLD FLASH OR PROTRUSIONS SHALL NOT EXCEED .010 INCH (0.254mm)

PACKAGE DESCRIPTION

S8 Package 8-Lead Plastic Small Outline (Narrow .150 Inch) (Reference LTC DWG # 05-08-1610)

