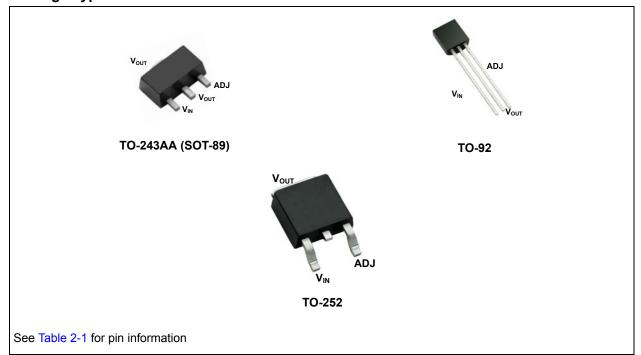
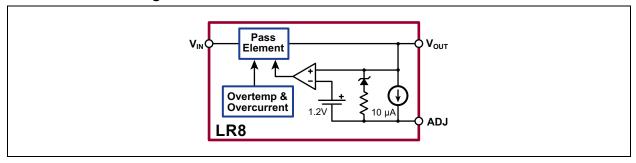
Package Type



Functional Block Diagram



1.0 ELECTRICAL CHARACTERISTICS

ABSOLUTE MAXIMUM RATINGS[†]

V _{IN} Input voltage (voltages ref to ADJ)	0.5 to +480V
Output voltage range	
Operating ambient temperature range	
Operating junction temperature range	
Storage temperature	

† Notice: Stresses above those listed under "Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and functional operation of the device at those or any other conditions above those indicated in the operational listings of this specification is not implied. Exposure to maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS 1

Parameter	Sym.	Min.	Тур.	Max.	Units	Conditions
Input to output voltage difference	V _{IN} - V _{OUT}	12		450	V	
Overall output voltage regulation	V _{OUT}	1.14	1.20	1.26	٧	13.2V < V_{IN} < 400V, R_1 = 2.4 k Ω , R_2 = 0
		375	400	425	>	$R_1 = 2.4 \text{ k}\Omega, R_2 = 782 \text{ k}\Omega$
Line regulation			0.003	0.01	%/V	17V < V _{IN} < 400V, V _{OUT} = 5V, I _{OUT} = 0.5mA
Load regulation	ΔV_{OUT}		1.4	3.0	%	V _{IN} = 17V, V _{OUT} = 5V, 0.5 mA < I _{OUT} < 10 mA
Temperature regulation		-1		+1	%	V _{IN} = 17V, V _{OUT} = 5V, I _{OUT} = 10 mA, -40°C < T _A < 85°C
Output current limit	1.	10		30	mA	T_J < 85°C, V_{IN} - V_{OUT} = 12V
Output current innit	I _{OUT}			0.5	mA	T _J > 125°C, V _{IN} - V _{OUT} = 450V
Minimum output current	I _{OUT}		0.3	0.5	mA	Includes R ₁ and load current
Adjust output current	I_{ADJ}	5.0	10	15	μΑ	
Minimum output load capacitance	C_{LOAD}	1.0			μF	
Ripple rejection ratio	$\Delta V_{OUT}/\Delta V_{IN}$	50	60		dB	120 Hz, V _{OUT} = 5V
Junction temperature limit	T _{LIMIT}	125			°C	

¹ Test Conditions unless otherwise specified: -40°C <T_A < 85°C.

TABLE 1-1: TYPICAL THERMAL RESISTANCE

Package	$\theta_{ m ja}$
TO-252 (D-PAK)	81°C/W
TO-92	132°C/W
TO-243AA (SOT-89)	133°C/W

TABLE 1-2: THERMAL CHARACTERISTICS

Package	Power Dissipation @T _A = 2.5°C	θ _{jc} °C/W	θ _{ja} °C/W
TO-92	0.74W	125	170
TO-243AA (SOT-89)	1.6W	15	78 ¹
TO-252 (D-PAK)	2.5W	6.25	50 ¹

¹ Mounted on FR4 board, 25 mm x 2 mm x 1.57 mm

2.0 PIN DESCRIPTION

The locations of the pins are listed in Package Type.

TABLE 2-1: PIN DESCRIPTION

Function	Description
V _{IN}	Regulator input. 13.2-450V.
V _{OUT}	Regulator output.
ADJ	Output voltage adjust.

3.0 TYPICAL APPLICATION CIRCUITS

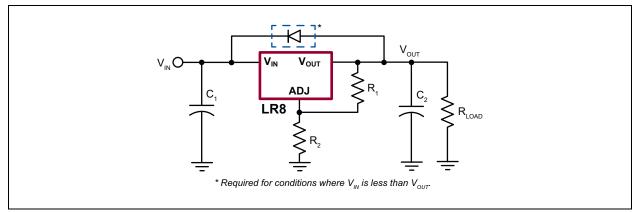


FIGURE 3-1: Typical Application Circuit.

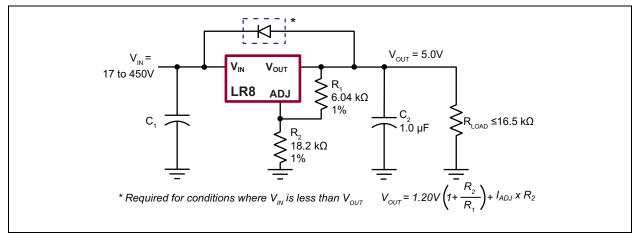


FIGURE 3-2: High-input Voltage, 5.0V Output Linear Regulator.

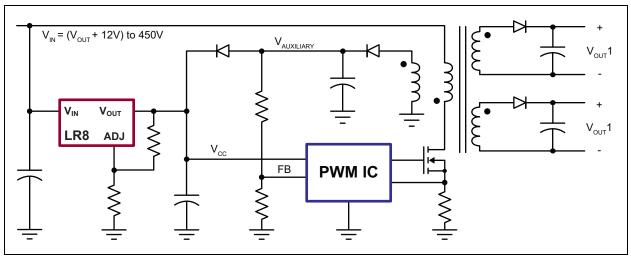


FIGURE 3-3: SMPS Start-Up Circuit.

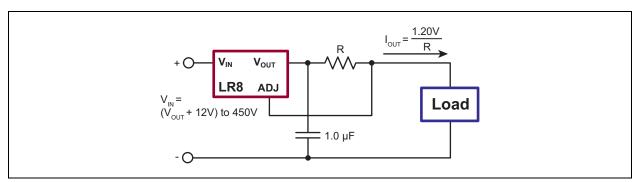


FIGURE 3-4: High-voltage, Adjustable, Constant-Current Source.

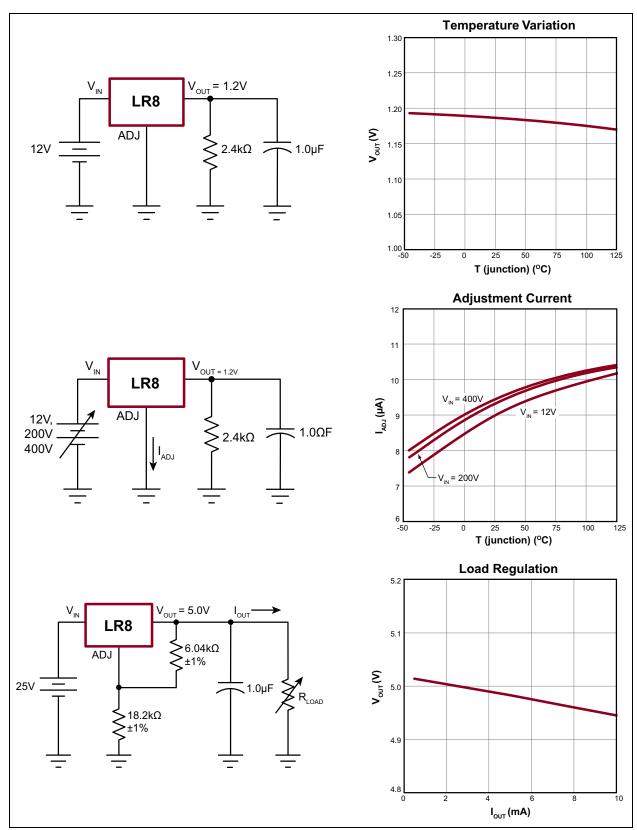


FIGURE 3-5: Typical Performance Curves 1 of 3.

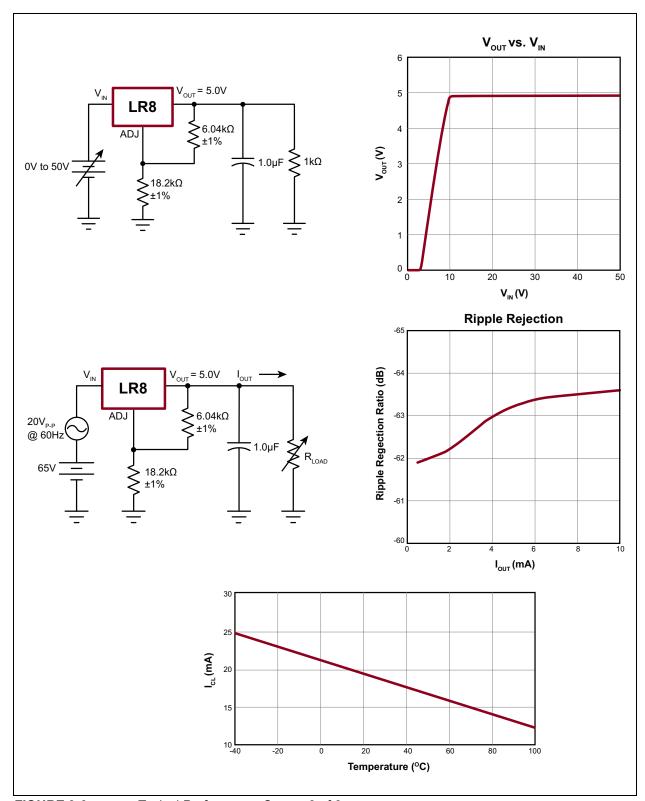


FIGURE 3-6: Typical Performance Curves 2 of 3.

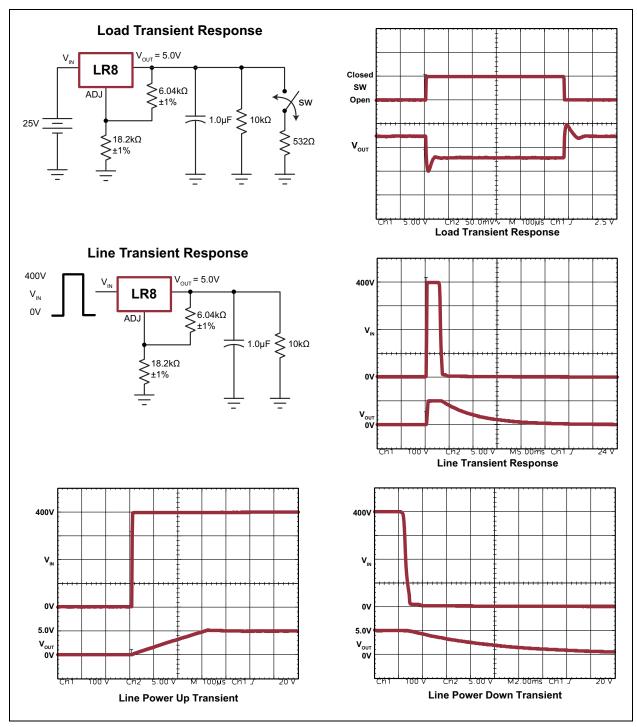
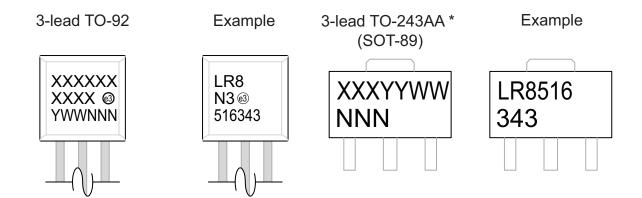
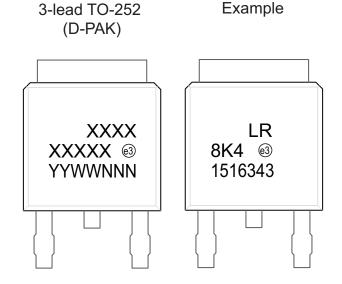


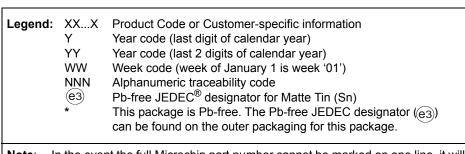
FIGURE 3-7: Typical Performance Curves 3 of 3.

4.0 PACKAGING INFORMATION

4.1 Package Marking Information

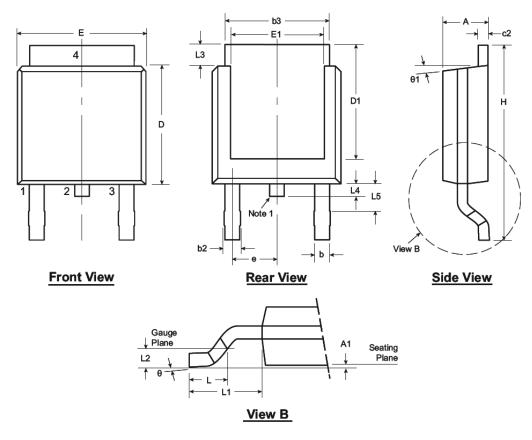






Note: In the event the full Microchip part number cannot be marked on one line, it will be carried over to the next line, thus limiting the number of available characters for product code or customer-specific information. Package may or not include the corporate logo.

3-Lead TO-252 (D-PAK) Package Outline (K4)



Note: For the most current package drawings, see the Microchip Packaging Specification at www.microchip.com/packaging.

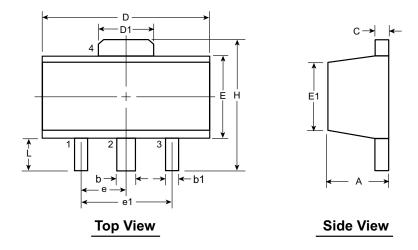
Although 4 terminal locations are shown, only 3 are functional. Lead number 2 was removed. 1.

Symb	ol	Α	A1	b	b2	b3	c2	D	D1	E	E1	e	Н	L	L1	L2	L3	L4	L5	θ	θ1
Dimen-	MIN	.086	.000*	.025	.030	.195	.018	.235	.205	.250	.170		.370	.055			.035	.025*	.035 [‡]	00	0°
sion	NOM	-	-	-	-	-	-	.240	-	-	-	.090 BSC	-	.060	.108 REF	.020 BSC	-	-	-	-	-
(inches)	MAX	.094	.005	.035	.045	.215	.035	.245	.217*	.265	.200*	-20	.410	.070			.050	.040	.060	10°	15º

JEDEC Registration TO-252, Variation AA, Issue E, June 2004.
* This dimension is not specified in the JEDEC drawing.
† This dimension differs from the JEDEC drawing.

Drawings not to scale.

3-Lead TO-243AA (SOT-89) Package Outline (N8)

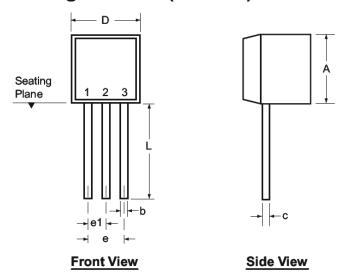


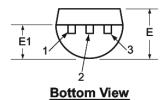
Note: For the most current package drawings, see the Microchip Packaging Specification at www.microchip.com/packaging.

Symbo	ol	Α	b	b1	С	D	D1	Е	E1	е	e1	Н	L
	MIN	1.40	0.44	0.36	0.35	4.40	1.62	2.29	2.00 [†]			3.94	0.73 [†]
Dimensions (mm)	NOM	-	-	-	-	-	-	-	-	1.50 BSC	3.00 BSC	-	-
()	MAX	1.60	0.56	0.48	0.44	4.60	1.83	2.60	2.29	200	200	4.25	1.20

JEDEC Registration TO-243, Variation AA, Issue C, July 1986. † This dimension differs from the JEDEC drawing Drawings not to scale.

3-Lead TO-92 Package Outline (L/LL/N3)





Note: For the most current package drawings, see the Microchip Packaging Specification at www.microchip.com/packaging.

Symb	ol	Α	b	С	D	E	E1	е	e1	L
	MIN	.170	.014 [†]	.014 [†]	.175	.125	.080	.095	.045	.500
Dimensions (inches)	NOM	-	-	-	-	-	-	-	-	-
()	MAX	.210	.022 [†]	.022 [†]	.205	.165	.105	.105	.055	.610*

Drawings not to scale.

JEDEC Registration TO-92.
* This dimension is not specified in the JEDEC drawing.

[†] This dimension differs from the JEDEC drawing.

LR8

NOTES:

APPENDIX A: REVISION HISTORY

Revision B (November 2017)

The following is the list of modifications:

- 1. Updated Figure 3-2.
- 2. Various typographical edits.

Revision A (June 2015)

• Original Release of this Document.

PRODUCT IDENTIFICATION SYSTEM

 $\underline{\text{To order or obtain information, e.g., on pricing or delivery, refer to the factory or the listed sales of fice.}\\$

	 ckage Env otions	ironmental Media	a)	LR8N3-G	TO-92 package, 1000/bag
Oţ.	dions	Туре	b)	LR8N3-G-P003:	TO-92 package, 2000/reel.
Device:	LR8	= High-Input Voltage, Adjustable, 3-Termina	, c)	LR8K4-G	TO-252 package, 2000/reel
		Linear Regulator	d)	LR8N8-G	TO-243AA package, 2000/reel
Package:	N3 K4 N8	= TO-92 = TO-252 (D-PAK) = TO-243AA (SOT-89)			
Environmental	G	= Lead (Pb)-free/ROHS-compliant package			
Media Type:	(blank)	= 1000/Bag for N3 packages= 2000/Reel for K4 packages= 2000/Reel for N8 packages			
	P003	= 2000/Reel for N3 package			

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