

KSA1156

High Voltage Switching Low Power Switching Regulator DC-DC Converter

- High Breakdown Voltage
- Low Collector Saturation Voltage
- · High Speed Switching



PNP Silicon Transistor

Absolute Maximum Ratings $T_C=25^{\circ}C$ unless otherwise noted

Symbol Parameter		Ratings	Units	
V _{CBO}	Collector-Base Voltage	- 400	V	
V _{CEO}	Collector-Emitter Voltage	- 400	V	
V _{EBO}	Emitter-Base Voltage	- 7	V	
I _B	Base Current	- 0.25	Α	
I _C	Collector Current (DC)	- 0.5	Α	
I _{CP}	Collector Current (Pulse)	- 1	Α	
P _C	Collector Dissipation (T _a =25°C)	1	W	
P _C	Collector Dissipation (T _C =25°C)	10	W	
TJ	Junction Temperature	150	°C	
T _{STG}	Storage Temperature	- 55 ~ 150	°C	

$\textbf{Electrical Characteristics} \ \textbf{T}_{\textbf{C}} = 25 ^{\circ} \textbf{C} \ \text{unless otherwise noted}$

Symbol	Parameter	Test Condition	Min.	Max.	Units
V _{CEO} (sus)	Collector-Emitter Sustaining Voltage	I _C = - 100mA, I _B = - 10mA - 40 L = - 20mH			V
V _{CEX} (sus)	Collector-Emitter Sustaining Voltage	$I_C = -200 \text{mA}, I_{B1} = -I_{B2} = -20 \text{mA}$ $V_{BE}(\text{off}) = 5 \text{V}, L = 10 \text{mH}$	- 400		V
I _{CBO}	Collector Cut-off Current	V _{CB} = - 400V, I _E = 0		- 100	μΑ
I _{EBO}	Emitter Cut-off Current	$V_{EB} = -5V, I_{C} = 0$		- 10	μΑ
I _{CEX1}	Collector Cut-off Current	V _{CE} = - 400V, V _{BE} (off) = 1.5V		- 100	μΑ
I _{CEX2}	Collector Cut-off Current	$V_{CE} = -400V, V_{BE}(off) = 1.5V$ $T_{C} = 125^{\circ}C$		- 1	mA
h _{FE}	DC Current Gain	V _{CE} = - 5V, I _C = - 100mA	30	200	
V _{CE} (sat)	Collector-Emitter Saturation Voltage	I _C = - 100mA, I _B = - 10mA		- 1	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	I _C = - 100mA, I _B = - 10mA		- 1.2	V
t _{ON}	Turn On Time	V _{CC} = - 150V, I _C = - 100mA		1	μs
t _{STG}	Storage Time	$I_{B1} = -10 \text{mA}$, $I_{B2} = 20 \text{mA}$		4	μs
t _F	Fall Time	$R_L = 1.5 \text{K}\Omega$		1	μs

$h_{\mbox{\scriptsize FE}}$ Classification

Classification	N	R	0	Υ
h _{FE}	30 ~ 60	40 ~ 80	60 ~ 120	100 ~ 200

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Typical Characteristics

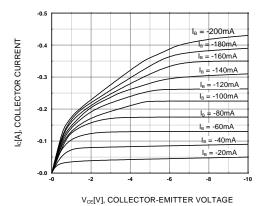


Figure 1. Static Characteristic

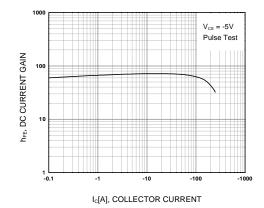


Figure 2. DC current Gain

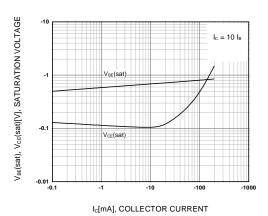


Figure 3. Collector-Emitter Saturation Voltage Base-Emitter Saturation Voltage

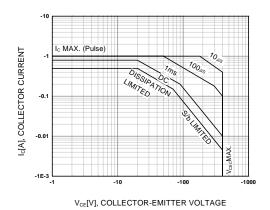


Figure 4. Safe Operating Area

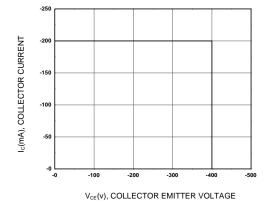


Figure 5. Reverse Bias Safe Operating Area

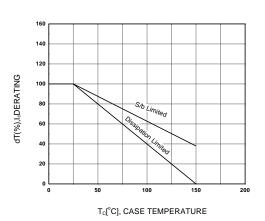


Figure 6. Derating Curve of Safe Operating Areas

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Typical characteristics (Continued)

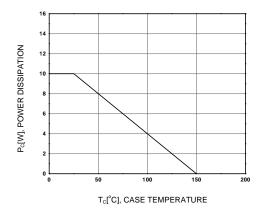
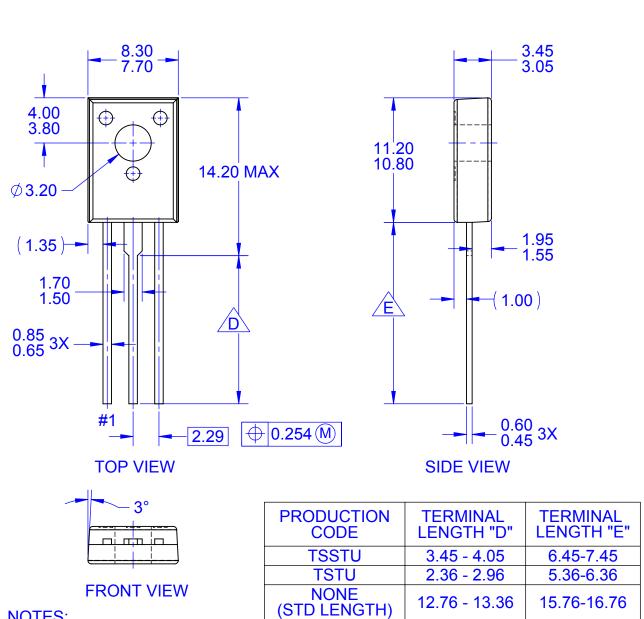


Figure 7. Power Derating

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- **NOTES:**
- A. NO INDUSTRY STANDARD APPLIES TO THIS **PACKAGE**
- В. ALL DIMENSIONS ARE IN MILLIMETERS
- C. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH, AND TIE BAR PROTRUSIONS





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