

ICM7555/7556

General Purpose Timers

ABSOLUTE MAXIMUM RATINGS (Note 1)

Supply Voltage		+18 Volts
Input Voltage	TRIGGER	
Control Voltage	THRESHOLD	$<V^+ + 0.3V$ to $\geq -0.3V$
	RESET	
Output Current		100mA
Power Dissipation ²	ICM7555	300mW
	ICM7556	200mW
Operating Temperature Range	ICM7555/7556	-20°C to +85°C
	ICM7555/7556	-20°C to +85°C

ICM7555/7556	(Maxim)	-20°C to +85°C
ICM7555/7556	IPD	-20°C to +85°C
ICM7555/7556	ITV	-20°C to +85°C
ICM7555/7556	IPD	-20°C to +85°C
ICM7555/7556	MTV	-55°C to +125°C
ICM7555/7556	MJD	-55°C to +125°C
Storage Temperature		-65°C to +150°C
Lead Temperature (Soldering 60 Seconds)		+300°C

Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

($V^+ = +2$ to $+15$ volts; $T_A = 25^\circ\text{C}$, Unless Noted)

PARAMETER	SYMBOL	TEST CONDITIONS	VALUE			UNITS
			MIN	TYP	MAX	
Supply Voltage	V^+	$-20^\circ\text{C} \leq T_A \leq +70^\circ\text{C}$ $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$	2 3		18 16	V V
Supply Current ³	I^+	ICM7555 $V^+ = 2V$ $V^+ = 18V$		60 120	200 300	μA μA
		ICM7556 $V^+ = 2V$ $V^+ = 18V$		120 240	400 600	μA μA
Timing Error		$R_A, R_B = 1k$ to $100k$, $C = 0.1\mu\text{F}$ Note 4				
Initial Accuracy		$5V \leq V^+ \leq 15V$ $V^+ = 5V$		2.0 50	5.0	% ppm/ $^\circ\text{C}$
Drift with Temperature		$V^+ = 10V$ $V^+ = 15V$		75 100		
Drift with Supply Voltage		$V^+ = 5V$		1.0	3.0	%/V
Threshold Voltage	V_{TH}	$V^+ = 5V$	0.63	0.66	0.67	V^+
Trigger Voltage	V_{TRIG}	$V^+ = 5V$	0.29	0.33	0.34	V^+
Trigger Current	I_{TRIG}	$V^+ = 18V$ $V^+ = 5V$ $V^+ = 2V$		50 10 1		pA pA pA
Threshold Current	I_{TH}	$V^+ = 18V$ $V^+ = 5V$ $V^+ = 2V$		50 10 1		pA pA pA
Reset Current	I_{RST}	$V_{RESET} = \text{Ground}$ $V^+ = 18V$ $V^+ = 5V$ $V^+ = 2V$		100 20 2		pA pA pA
Reset Voltage	V_{RST}	$V^+ = 18V$ $V^+ = 2V$	0.4 0.4	0.7 0.7	1.0 1.0	V V
Control Voltage Lead	V_{CV}	$V^+ = 5V$	0.62	0.66	0.67	V^+
Output Voltage Drop	V_O	Output Lo $V^+ = 18V$ $I_{SINK} = 3.2\text{mA}$ $V^+ = 5V$ $I_{SINK} = 3.2\text{mA}$ Output Hi $V^+ = 18V$ $I_{SOURCE} = 1.0\text{mA}$ $V^+ = 5V$ $I_{SOURCE} = 1.0\text{mA}$		0.1 0.15 17.25 4.0	0.4 0.4 17.8 4.5	V V V V
Rise Time of Output	t_r	$R_L = 10M\Omega$ $C_L = 10pF$ $V^+ = 5V$	35	40	75	ns
Fall Time of Output	t_f	$R_L = 10M\Omega$ $C_L = 10pF$ $V^+ = 5V$	35	40	75	ns
Guaranteed Max Osc Freq	f_{max}	Astable Operation	500			kHz

Note 1: Due to the SCR structure inherent in the CMOS process used to fabricate these devices, connecting any terminal to a voltage greater than $V^+ + 0.3V$ or less than $V^- - 0.3V$ may cause destructive latchup. For this reason it is recommended that no inputs from external sources not operating from the same power supply be applied to the device before its power supply is established. In multiple systems, the supply of the ICM7555/6 must be turned on first.

Note 2: Junction temperatures should not exceed 135°C and the power dissipation must be limited to 20mW at 125°C . Below 125°C power dissipation may be increased to 300mW at 25°C . Derating factor is approximately 3mW/ $^\circ\text{C}$ (7556) or 2mW/ $^\circ\text{C}$ (7555).

Note 3: The supply current value is essentially independent of the TRIGGER, THRESHOLD and RESET voltages.

Note 4: Parameter is not 100% tested. Majority of all units meet this specification.

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ADVANTAGE™

General Purpose Timers

- ◆ Lower Supply Current
- ◆ Increased Output Source Current
- ◆ Guaranteed THRESHOLD, TRIGGER and RESET Input Currents
- ◆ Guaranteed Discharge Output Voltage
- ◆ Supply Current Guaranteed Over Temperature
- ◆ Significantly Improved ESD Protection (Note 6)
- ◆ Maxim Quality and Reliability

ABSOLUTE MAXIMUM RATINGS This device conforms to the Absolute Maximum Ratings on adjacent page.

ELECTRICAL CHARACTERISTICS Specifications below satisfy or exceed all "tested" parameters on adjacent page.
($V^+ = +2$ to $+15$ volts; $T_A = 25^\circ\text{C}$, unless noted.)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	V^+	$-20^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$ $-55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$	2 3		16.5 16	V V
Supply Current (Note 3)	I^+	ICM 7555 $V^+ = 2-16.5\text{V}; T_A = +25^\circ\text{C}$ $V^+ = 5\text{V}; T_A = +25^\circ\text{C}$ $V^+ = 5\text{V}; -20^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$ $V^+ = 5\text{V}; -55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$ ICM 7556 $V^+ = 2-16.5\text{V}; T_A = +25^\circ\text{C}$ $V^+ = 5\text{V}; T_A = +25^\circ\text{C}$ $V^+ = 5\text{V}; -20^\circ\text{C} \leq T_A \leq +85^\circ\text{C}$ $V^+ = 5\text{V}; -55^\circ\text{C} \leq T_A \leq +125^\circ\text{C}$		30 60	250 120 250 300 500 240 500 600	μA μA μA μA μA μA μA μA
Timing Error (Note 4)		Circuit of figure 1(b); $R_A = R_B = 100\text{k}\Omega$, $C = 0.1\mu\text{F}$, $V^+ = 5\text{V}$				
Initial Accuracy (Note 5)				2.0	5.0	%
Drift with Temperature		$V^+ = 5\text{V}$ $V^+ = 10\text{V}$ $V^+ = 15\text{V}$		50 75 100		ppm/ $^\circ\text{C}$ ppm/ $^\circ\text{C}$ ppm/ $^\circ\text{C}$
Drift with Supply Voltage		$V^+ = 5\text{V}$		1.0	3.0	%/V
Threshold Voltage	V_{TH}	$V^+ = 5\text{V}$	0.63	0.66	0.67	V^+
Trigger Voltage	V_{TRIG}	$V^+ = 5\text{V}$	0.29	0.33	0.34	V^+
Trigger Current	I_{TRIG}	$V^+ = 16.5\text{V}$ $V^+ = 5\text{V}$ $V^+ = 2\text{V}$		50 10 1		pA pA pA
Threshold Current	I_{TH}	$V^+ = 16.5\text{V}$ $V^+ = 5\text{V}$ $V^+ = 2\text{V}$		50 10 1		pA pA pA
Reset Current	I_{RST}	$V_{RESET} = \text{Ground}$ $V^+ = 16.5\text{V}$ $V^+ = 5\text{V}$ $V^+ = 2\text{V}$		100 20 2		pA pA pA
Reset Voltage	V_{RST}	$V^+ = 16.5\text{V}$ $V^+ = 2\text{V}$	0.4 0.4	0.7 0.7	1.2 1.2	V V
Control Voltage	V_{CV}	$V^+ = 5\text{V}$	0.62	0.66	0.67	V^+
Output Voltage Drop	V_O	Output Lo $V^+ = 16.5\text{V}$ $I_{SINK} = 3.2\text{mA}$ $V^+ = 5\text{V}$ $I_{SINK} = 3.2\text{mA}$ Output Hi $V^+ = 16.5\text{V}$ $I_{SOURCE} = 2.0\text{mA}$ $V^+ = 5\text{V}$ $I_{SOURCE} = 2.0\text{mA}$	15.75 4.0	0.1 0.15 16.25 4.5	0.4 0.4	V V V V
Discharge Output Voltage	V_{DIS}	$V^+ = 5\text{V}$, $I_{DIS} = 3.2\text{mA}$		0.1	0.4	V
Rise Time of Output (Note 4)	t_r	$R_L = 10\text{M}\Omega$ $C_L = 10\text{pF}$ $V^+ = 5\text{V}$	35	40	75	ns
Fall Time of Output (Note 4)	t_f	$R_L = 10\text{M}\Omega$ $C_L = 10\text{pF}$ $V^+ = 5\text{V}$	35	40	75	ns
Guaranteed Max Osc. Freq. (Note 4)	f_{max}	Astable Operation	500			kHz

Note 1: Due to the SCR structure inherent in the CMOS process used to fabricate these devices, connecting any terminal to a voltage greater than $V^+ + 0.3\text{V}$ or less than $V^- - 0.3\text{V}$ may cause destructive latchup. For this reason it is recommended that no inputs from external sources not operating from the same power supply be applied to the device before its power supply is established. In multiple systems, the supply of the ICM7555/6 must be turned on first.

Note 2: Junction temperatures should not exceed 135°C and the power dissipation must be limited to 20mW at 125°C . Below 125°C power dissipation may be increased to 300mW at 25°C . Derating factor is approximately $3\text{mW}/^\circ\text{C}$ (7556) or $2\text{mW}/^\circ\text{C}$ (7555).

Note 3: The supply current value is essentially independent of the TRIGGER, THRESHOLD AND RESET voltages.

Note 4: Parameter is not 100% tested. Majority of all units meet this specification.

Note 5: Deviation from $f = 1.46/(R_A + 2R_B)C$, $V^+ = 5\text{V}$.

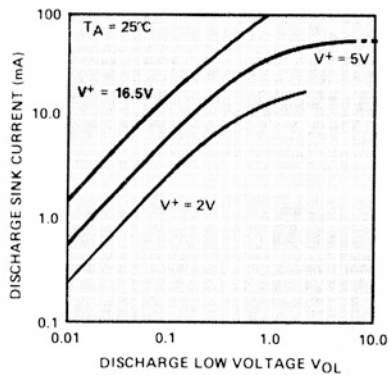
Note 6: All pins are designed to withstand electrostatic discharge (ESD) levels in excess of 2000V . (Mil Std 883B, Method 3015.1 Test Circuit.)

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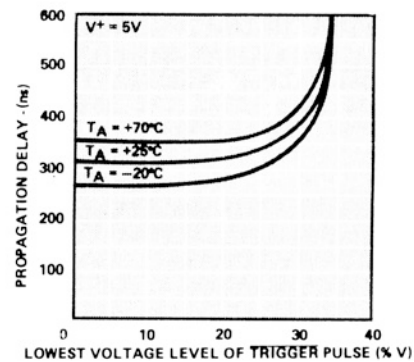
General Purpose Timers

Typical Operating Characteristics

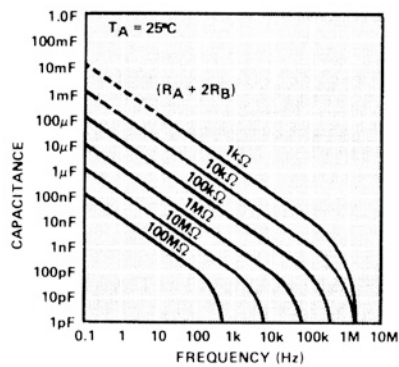
DISCHARGE OUTPUT CURRENT
AS A FUNCTION OF
DISCHARGE OUTPUT VOLTAGE



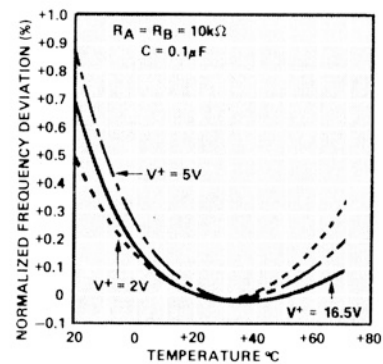
PROPAGATION DELAY
AS A FUNCTION OF
VOLTAGE LEVEL OF TRIGGER PULSE



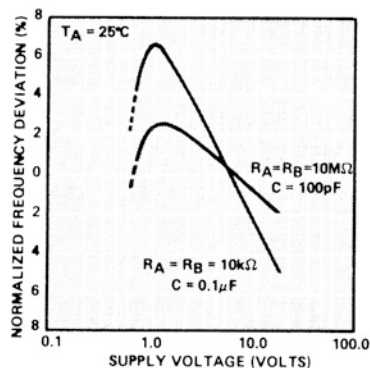
FREE RUNNING FREQUENCY AS A
FUNCTION OF R_A , R_B AND C



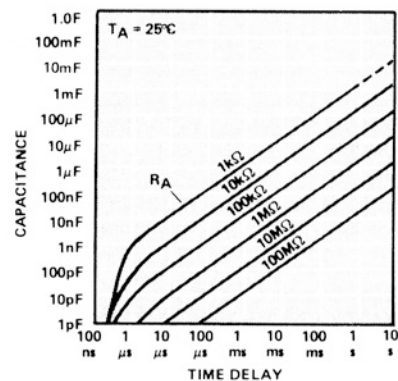
NORMALIZED FREQUENCY STABILITY
IN THE ASTABLE MODE AS A
FUNCTION OF TEMPERATURE



NORMALIZED FREQUENCY STABILITY
IN THE ASTABLE MODE AS A
FUNCTION OF SUPPLY VOLTAGE



TIME DELAY IN THE MONOSTABLE
MODE AS A FUNCTION OF R_A AND C

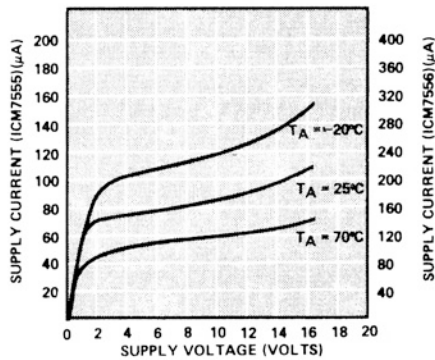


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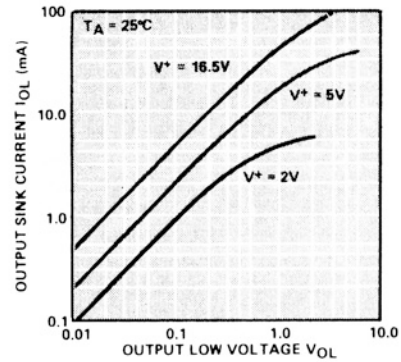
General Purpose Timers

Typical Operating Characteristics

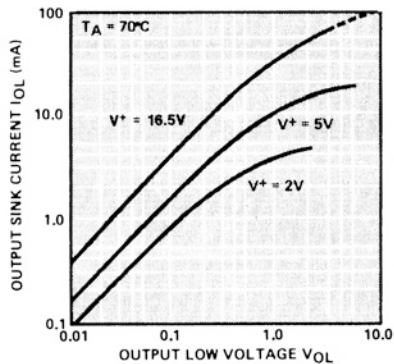
SUPPLY CURRENT AS A
FUNCTION OF SUPPLY VOLTAGE



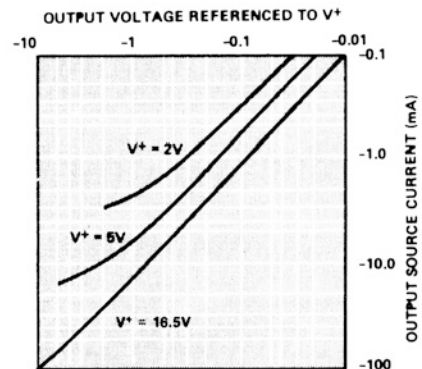
OUTPUT SINK CURRENT AS A
FUNCTION OF OUTPUT VOLTAGE



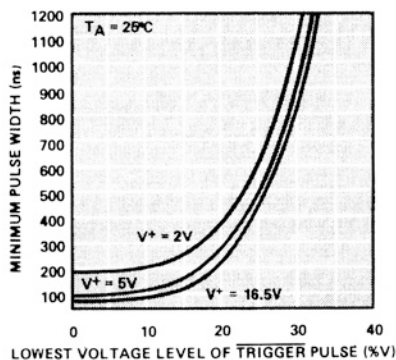
OUTPUT SINK CURRENT AS A
FUNCTION OF OUTPUT VOLTAGE



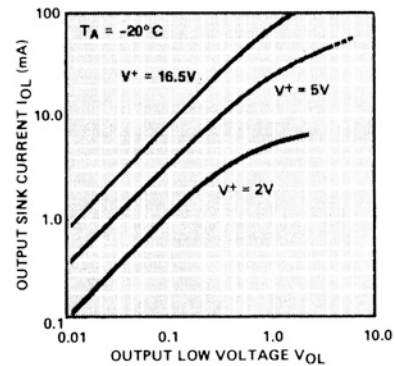
OUTPUT SOURCE CURRENT AS A
FUNCTION OF OUTPUT VOLTAGE



MINIMUM PULSE WIDTH
REQUIRED FOR TRIGGERING



OUTPUT SINK CURRENT AS A
FUNCTION OF OUTPUT VOLTAGE



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General Purpose Timers

Detailed Description

Both the ICM7555 timer and the ICM7556 dual timer can be configured for either astable or monostable operation. In the astable mode the free running frequency and the duty cycle are controlled by two external resistors and one capacitor. Similarly, the pulse width in the monostable mode is precisely controlled by one external resistor and capacitor.

The external component count is decreased when replacing a bipolar timer with the ICM7555 or ICM7556. The bipolar devices produce large crowbar currents in the output driver. To compensate for this spike, a capacitor is used to decouple the power supply lines. The CMOS timers produce supply spikes of only 2-3mA vs. 300-400mA (Bipolar), therefore supply decoupling is typically not needed. This current spike comparison is illustrated in Figure 3. Another component is eliminated at the control voltage pin. These CMOS timers, due to the high impedance inputs of the comparators, do not require decoupling capacitors on the control voltage pin.

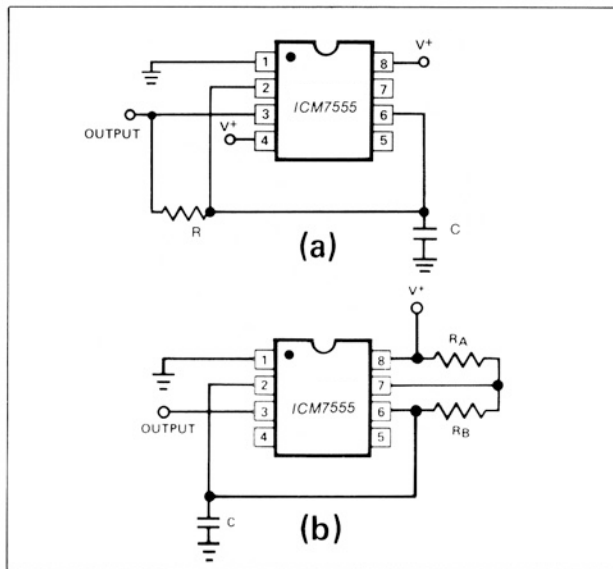


Figure 1. Maxim ICM7555 used in two different astable configurations.

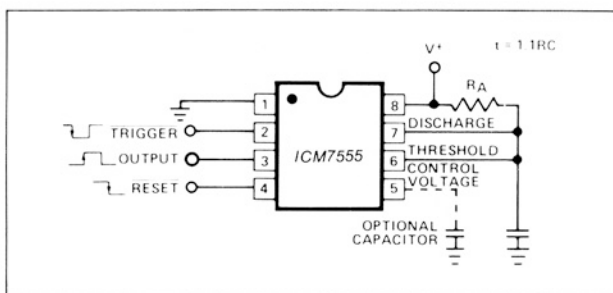


Figure 2. Maxim ICM7555 in a monostable operation.

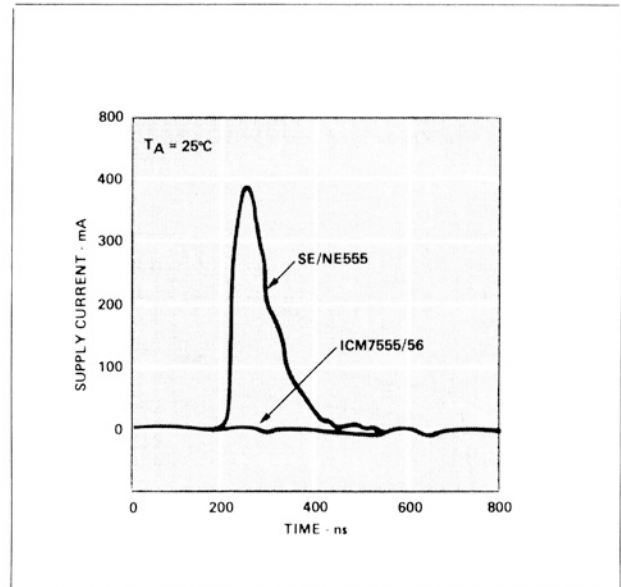


Figure 3. Supply current transient compared with a standard bipolar 555 during an output transition.

Applications Information

Astable Operation

We recommend either of the two astable circuit configurations illustrated in Figure 1. The circuit in (1a) provides a 50% duty cycle output using one timing resistor and capacitor. The oscillator waveform across the capacitor is symmetrical and triangular, swinging from $\frac{1}{3}$ to $\frac{2}{3}$ of the supply voltage. The frequency generated is defined by:

$$f = \frac{1}{1.4 RC}$$

The circuit in (1b) provides a means of varying the duty cycle of the oscillator. The frequency is defined by:

$$f = \frac{1.46}{(R_A + 2R_B)C}$$

The duty cycle is:

$$D = \frac{R_B}{R_A + 2R_B}$$

Monostable Operation

The circuit diagram in Figure 2 illustrates monostable operation. In this mode the timer acts as a one shot. Initially the external capacitor is held discharged by the discharge output. Upon application of a negative TRIGGER pulse to pin 2, the capacitor begins to charge exponentially through R_A . The device resets after the voltage across the capacitor reaches $\frac{2}{3}(V^+)$.

$$t_{\text{output}} = -\ln\left(\frac{1}{3}\right)R_AC = 1.1 R_AC$$

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General Purpose Timers

Reset

The reset function is significantly improved over the standard bipolar 555 and 556 in that it controls only the internal flip-flop, which in turn simultaneously controls the state of the Output and Discharge pins. This avoids the multiple threshold problems sometimes encountered with slow-falling edges of the bipolar devices. This input is designed to have essentially the same trip voltage as the standard bipolar devices (0.6 to 0.7V). At all supply voltages this input maintains an extremely high impedance.

Control Voltage

The control voltage regulates the two trip voltages for the THRESHOLD and TRIGGER internal comparators. This pin can be used for frequency modulation in the astable mode. By varying the applied voltage to the control voltage pin, delay times can be changed in the monostable mode.

Power Supply Considerations

Since the TRIGGER, THRESHOLD and Discharge leakage currents are very low, high impedance timing components may be used, keeping total system supply current at a minimum.

Output Drive Capability

The CMOS output stage is capable of driving most logic families including CMOS and TTL. The ICM7555 and ICM7556 will drive at least two standard TTL loads at a supply voltage of 4.5V or greater. When driving CMOS, the output swing at all supply voltage levels will equal the supply voltage.

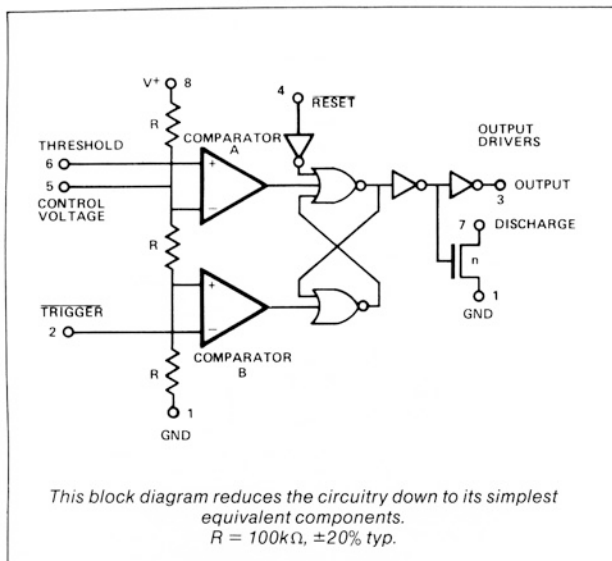


Figure 4. Block diagram of ICM7555.

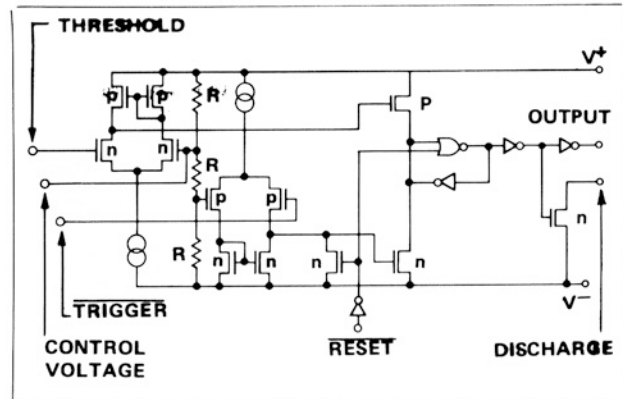


Figure 5. Equivalent circuit.

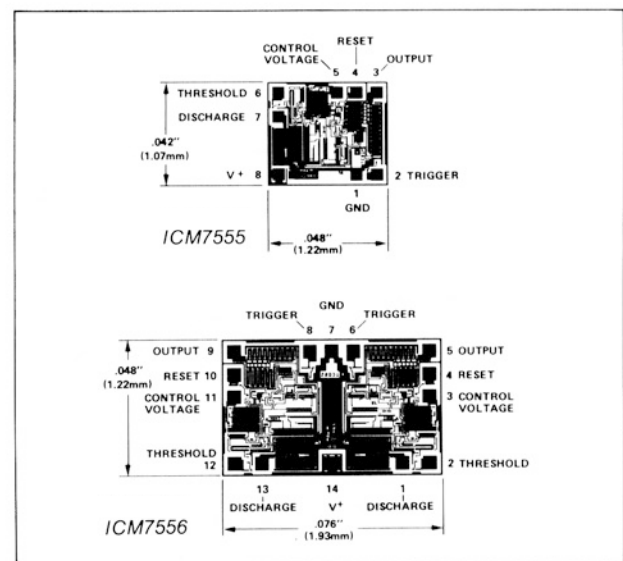
Function Table

RESET	TRIGGER VOLTAGE†	THRESHOLD VOLTAGE†	OUTPUT	DISCHARGE SWITCH
Low	Irrelevant	Irrelevant	Low	On
High	$< \frac{1}{3} V^+$	Irrelevant	High	Off
High	$> \frac{1}{3} V^+$	$> \frac{2}{3} V^+$	Low	On
High	$> \frac{1}{3} V^+$	$< \frac{2}{3} V^+$	As previously established	

† Voltages levels shown are nominal.

NOTE: RESET will dominate all other inputs. TRIGGER will dominate over THRESHOLD.

Chip Topographies

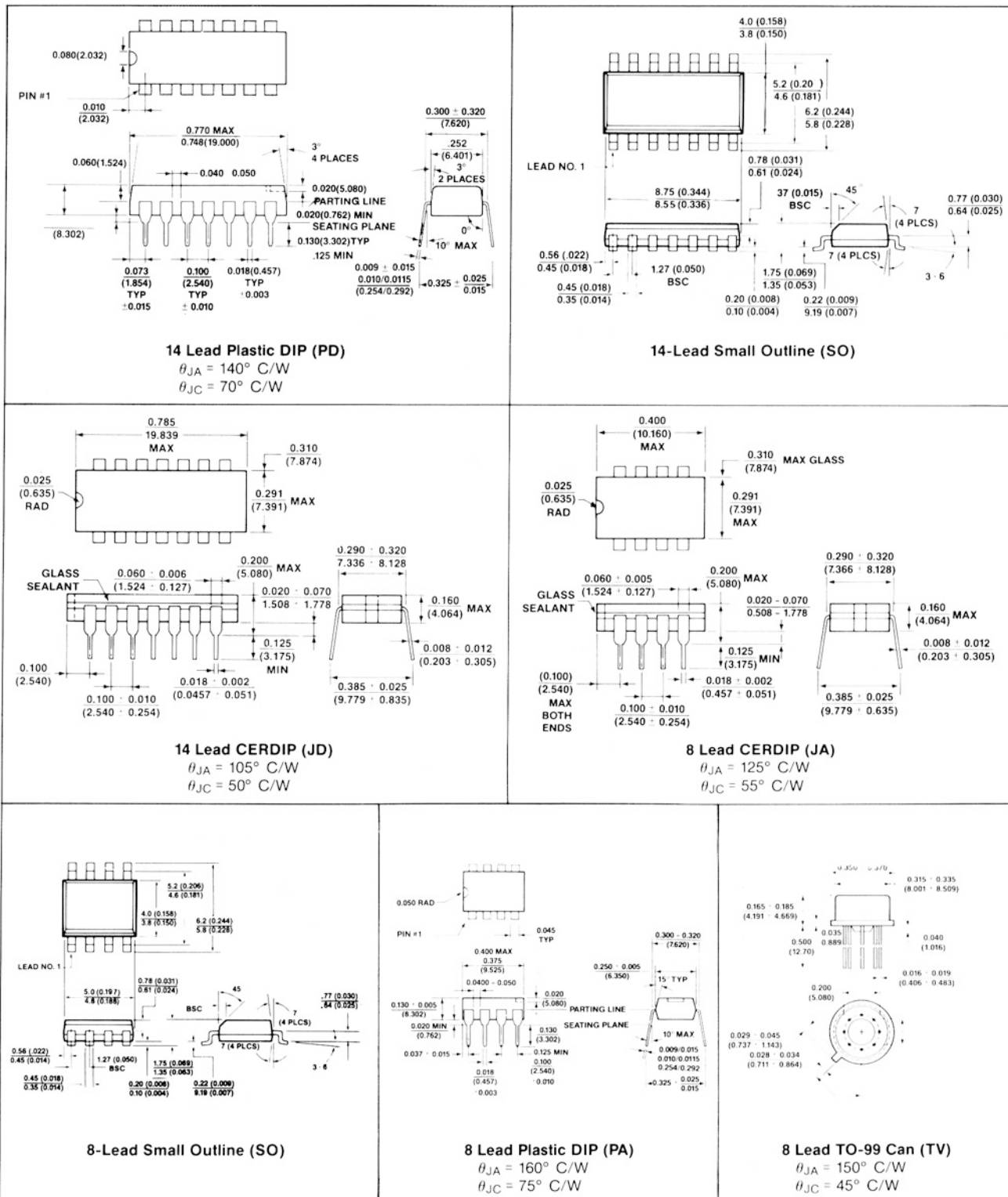


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Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to www.maxim-ic.com/packages.)



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General Purpose Timers



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time. The parametric values (min and max limits) shown in the Electrical Characteristics table are guaranteed. Other parametric values quoted in this data sheet are provided for guidance.

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