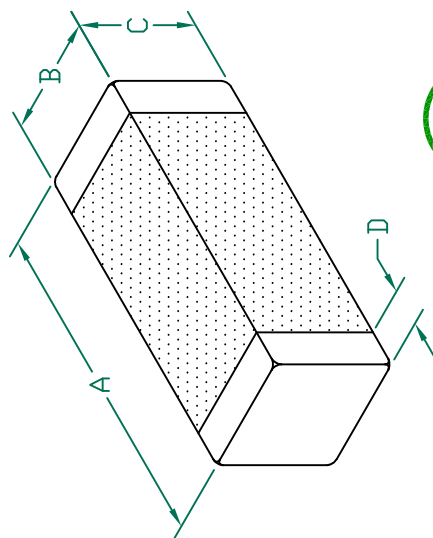
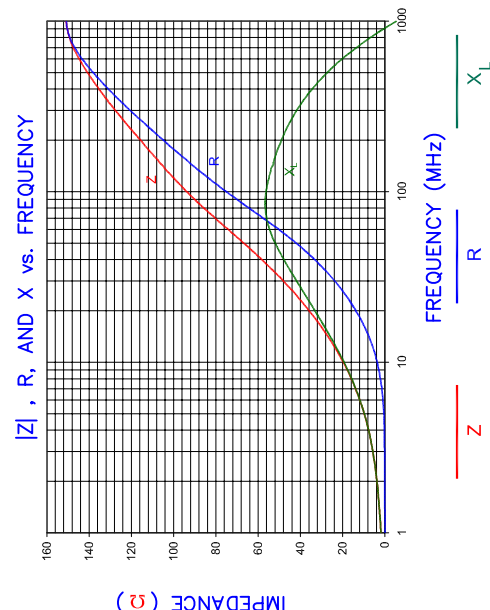


**UNCONTROLLED
DOCUMENT**

A	4.50	[.177]	$\begin{smallmatrix} + \\ - \end{smallmatrix}$	0.25	[.010]
B	1.60	[.063]	$\begin{smallmatrix} + \\ - \end{smallmatrix}$	0.25	[.010]
C	1.60	[.063]	$\begin{smallmatrix} + \\ - \end{smallmatrix}$	0.25	[.010]
D	0.51	[.020]	$\begin{smallmatrix} + \\ - \end{smallmatrix}$	0.25	[.010]

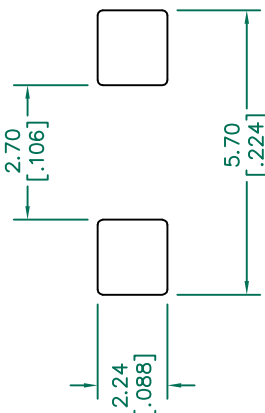


ELECTRICAL CHARACTERISTICS:			
Z @ 100MHz (Ω)	DCR (Ω)	Rated Current	
Nominal	91		
Minimum	68		
Maximum	114	0.030	3000 mA

NOTES: UNLESS OTHERWISE SPECIFIED

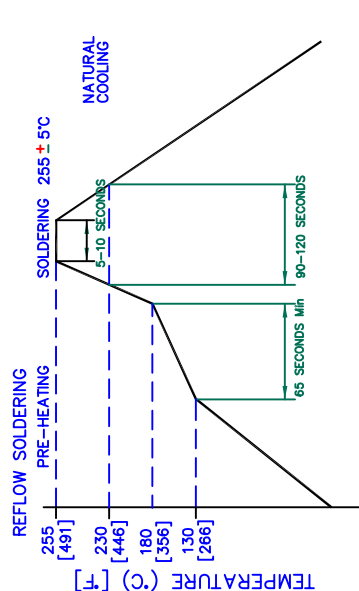
1. TAPED AND REELED per CURRENT EIA SPECIFICATIONS 7" REELS, 2,000 PCS/REEL.
2. COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
3. TERMINATION FINISH IS 100% TIN.
4. OPERATING TEMPERATURE TEMP: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$ (INCLUDING SELF-HEATING)

LAND PATTERNS FOR REFLOW SOLDERING



(For wave soldering, add 0.762 [".030"] to this dimension.)

RECOMMENDED SOLDERING CONDITIONS



RoHS 2002/95/EC

DIMENSIONS ARE IN mm [INCHES]		This print is the property of Laird Tech. and is loaned in confidence subject to return upon request and with the understanding that no copies shall be made without the written consent of Laird Tech. All rights to design or invention are reserved.			
E	ADD OPERATING TEMPERATURE UPDATE LAIRD LOGO AND REFLOW CURVE	08/05/13	QU		
D	CHANGE B DIMENSION	06/25/09	JRK	REV	PART TYPE: CO-FIRE
C	CHG REEL QTY FROM 3K TO 2K UPDATE COMPANY LOGO	11/05/08	JRK	PROJECT/PCB NUMBER: HI1806N910R-10	DRAWN BY: JRK
B	UPDATE COMPANY LOGO ADD ROHS	01/18/08	JRK	DATE:	SCALE: 04/15/04
A	ORIGINAL DRAFT	04/15/04	JRK	DATE:	SCALE: 04/15/04
REV	DESCRIPTION	DATE	INT	CD #	HI1806N910R-10-F
				SHEET:	2 of 2

AGILENT E4991A RF Impedance/Material Analyzer
HP 16194A Test Fixture. TEST REF. 3124