## **Product specifications**

Part	OCL <sup>1</sup>	FLL <sup>2</sup>	Irms <sup>3</sup>	l <sub>sat</sub> 1⁴	I <sub>sat</sub> 2⁵	DCR (m $\Omega$ )	
Number <sup>7</sup>	± 10% (nH)	Min. (nH)	(A)	(A) @+25 °C	(Amps) @+125 °C	@+20 °C	K-factor <sup>6</sup>
FP1505R1-R10-R	100	72		105	90		356.3
FP1505R1-R12-R	120	86		87	75		356.3
FP1505R1-R15-R	150	108	53	72	60	$0.47 \pm 7\%$	356.3
FP1505R1-R25-R	250	180		42	32	0.47 ± 7 /0	356.3
FP1505R1-R30-R	300	217		35	26		356.3
FP1505R1-R40-R	400	288		24	19.5		356.3

1 Open Circuit Inductance (OCL) Test Parameters: 100 kHz, 1.0 Vrms, 0.0 Adc

 Full Load Inductance (FLL) Test Parameters: 100 kHz, 1.0 V<sub>rms</sub>, I<sub>sat</sub>1
 I<sub>rms</sub>: DC current for an approximate temperature rise of 40 °C without core loss. Derating is necessary for AC currents. PCB layout, trace thickness and width, air-flow, and proximity of other heat generating components will affect the temperature rise. It is recommended that the temperature of the part not exceed +125 °C under worst case operating conditions verified in the end application.

4 Isat1: Peak current for approximately 20% rolloff at +25 °C.

5 Isat2: Peak current for approximately 20% rolloff at +125 °C.

Gate State of the second seco 7 Part Number Definition: FP1505Rx-Rxx-R

• FP1505 = Product code and size

• Rx= DCR indicator

• Rxx= Inductance value in uH, R = decimal point

• -R suffix = RoHS compliant



#### Packaging information - mm



Supplied in tape-and-reel packaging, 744 parts per reel, 13" diameter reel.

# FP1505 High frequency, high current power inductor

# Temperature rise vs total loss



### Core loss vs Bp-p



## Inductance characteristics

% of OCL vs. % of  $I_{sat}1$ 



### **Solder Reflow Profile**



Table 1 - Sta	andard SnF	Pb Solder (T <sub>C</sub> )	
	Volume	Volume	
Package	mm <sup>3</sup>	mm <sup>3</sup>	
Thickness	<350	≥350	
<2.5mm	235°C	220°C	
>2.5mm	220°C	220°C	
22:011111	LLO O	ELO O	
		e Solder (T <sub>C</sub> )	
			Volume
	ad (Pb) Fre	e Solder (T <sub>C</sub> )	Volume mm <sup>3</sup>
Table 2 - Lea	ad (Pb) Fre Volume	e Solder (T <sub>C</sub> ) Volume	
Table 2 - Lea Package	ad (Pb) Fre Volume mm <sup>3</sup>	e Solder (T <sub>C</sub> ) Volume mm <sup>3</sup>	mm <sup>3</sup>

250°C

245°C

245°C

>2.5mm

### **Reference JDEC J-STD-020**

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak	<ul> <li>Temperature min. (T<sub>smin</sub>)</li> </ul>	100°C	150°C	
	<ul> <li>Temperature max. (T<sub>smax</sub>)</li> </ul>	150°C	200°C	
	<ul> <li>Time (T<sub>smin</sub> to T<sub>smax</sub>) (t<sub>s</sub>)</li> </ul>	60-120 Seconds	60-120 Seconds	
Average ramp up rate T <sub>smax</sub> to T <sub>p</sub>		3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperature (TL)		183°C	217°C	
Time at liquidous (t <sub>L</sub> )		60-150 Seconds	60-150 Seconds	
Peak package body temperature (T <sub>P</sub> )*		Table 1	Table 2	
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**	
Average ramp-down rate (Tp to Tsmax)		6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.	

 $^{\ast}$  Tolerance for peak profile temperature (T\_p) is defined as a supplier minimum and a user maximum.

\*\* Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.

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